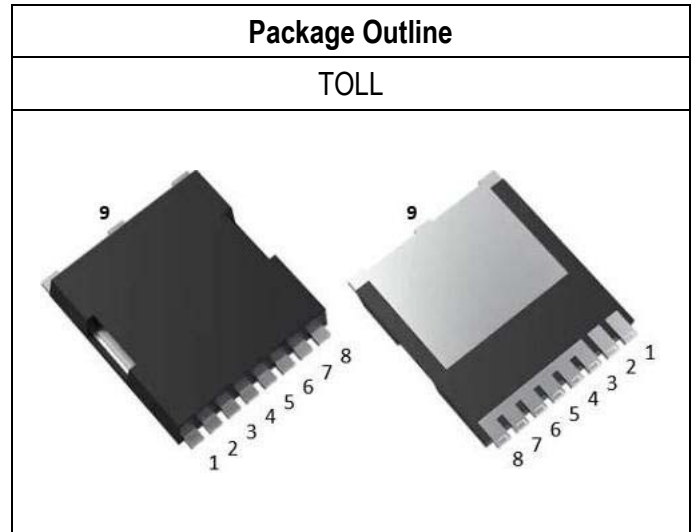


Key Electrical Characteristics		
Parameter / Symbol	Value / Description	Unit
$BV_{DSS \text{ min.}}$	600	V
$R_{DS(ON) \text{ Typ. @10V}}$	33	m Ω
I_D	53.3	A
$V_{TH \text{ Typ.}}$	3.9	V
$C_{iss \text{ Typ.}}$	6570	pF
$Q_g \text{ 10V}$	133	nC
E_{AS}	655	mJ



General Description

These devices are N-channel power MOSFET developed using Generation-3 Super Junction structure technology. There is high speed switching capacity, low $R_{DS(ON)}$ resistance, excellent power density, stabilizing qualified and characteristics for these devices. Moreover, it is a good choose in improved efficiency of circuit and raise power density are required. These features combine to be an advantage design for use in wide variety of application including converter and inverter design.

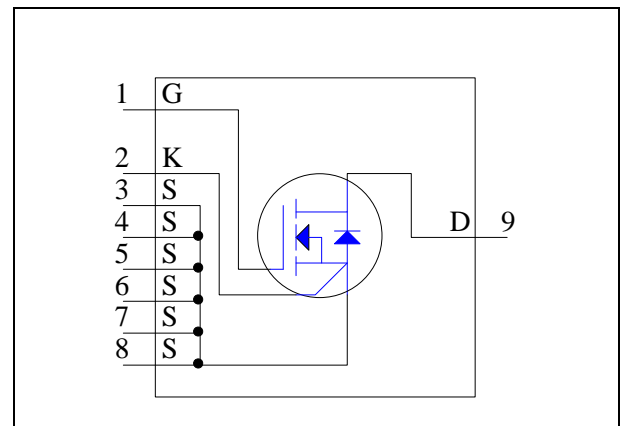
Features

- ✧ Ultra-fast body diode
- ✧ Fast reverse recovery time (t_{rr})
- ✧ Low $R_{DS(on)}$ resistance
- ✧ Low Switching Loss
- ✧ High power and current handling capacity
- ✧ Excellent single pulse avalanche energy
- ✧ Pb-free lead plating and RoHS compliant

Potential Applications

- ◆ Switch Mode Power Supply
- ◆ High power density application
- ◆ PV inverter
- ◆ Server power system
- ◆ EV charging system

Symbol and Pin assignment



Ordering Information

Item	Description
Orderable P/N	SJ600N037ZY3
Package Type	TOLL
Package Code	Z
Packing Type	Tape & Reel
Quantity/pcs	1,200
RoHS Status	Halogen-Free

Content

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2.	Thermal Resistance Ratings -----	3
3.	Electrical Characteristics -----	4
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1. Absolute Maximum Ratings (T_J=25°C unless otherwise noted)

Parameter	Symbol	Values			Unit	
		Min.	Typ.	Max.		
Drain-Source Voltage	V _{DS}	-	-	600	V	
Gate-Source Voltage	V _{GS}	-	-	±30	V	
Drain Current-Continuous ^{Note 1}	I _D	T _C =25°C	-	-	53.3	A
		T _C =100°C	-	-	33.7	A
Drain Current-Pulsed ^{Note 2}	I _{DM}	-	-	160	A	
Avalanche Current	I _{AS}	-	-	12.8	A	
Single Pulse Avalanche Energy ^{Note 3}	E _{AS}	-	-	655	mJ	
Maximum Power Dissipation	P _D	T _C =25°C	-	-	245	W
		T _C =100°C	-	-	98	W
		Derate Factor Above T _C =25°C	-	-	1.96	W/°C
Body Diode dv/dt ^{Note 4}	dv/dt	-	-	1.5	V/nS	
Max. Operating Junction Temperature	T _J	-	-	150	°C	
Storage Temperature Range	T _{STG}	-55	-	150	°C	

2. Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Thermal resistance, Junction-Case ^{Note 5}	R _{θJC}	Steady State	-	-	0.51	°C/W
Thermal resistance, Junction-Ambient ^{Note 5}	R _{θJA}	Steady State	-	-	27.43	°C/W

Notes:

- Limited by silicon chip capability and R_{θJC} junction-to-case thermal resistance.
- Must be ensure junction temperature does not exceed 150-degree C. (Pulse Width ≤ 380uS, Duty ≤ 2%)
- Limited by T_{Jmax}, starting T_J=25°C, L=8 mH, R_g=25Ω, I_{AS}=12.8A, V_{GS}=10V.
- V_{DD} = 0~400 V, I_{SD}=I_S ≤ 20 A starting T_C = 25°C
- The value of thermal resistance is measured with the single device put on cooling plate under a still air environment temperature is 25 degree C based on JEDEC standard JESD51-14 and JESD51-2a. Thermal resistance obtained depends on the user's specific board design and given application.
- C_{o(er)} is fixed capacitance that gives same stored energy as C_{oss} while V_{DS} rising to 400V from 0V.
- C_{o(tr)} is fixed capacitance that gives same charging time as C_{oss} while V_{DS} rising to 400V from 0V.

3. Electrical Characteristics (T_J=25°C unless otherwise noted)

STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250μA	600	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =600V, V _{GS} =0V	-	-	5	μA
		V _{DS} =600V, V _{GS} =0V, T _J =125°C	-	-	1	mA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±1	μA

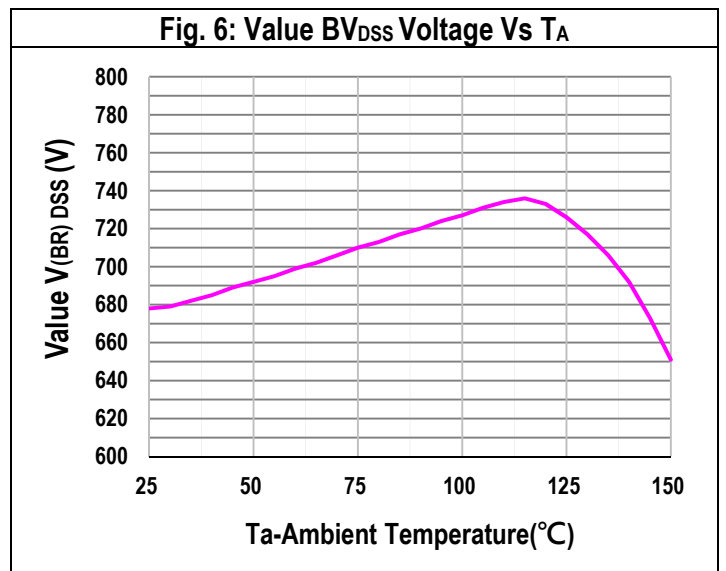
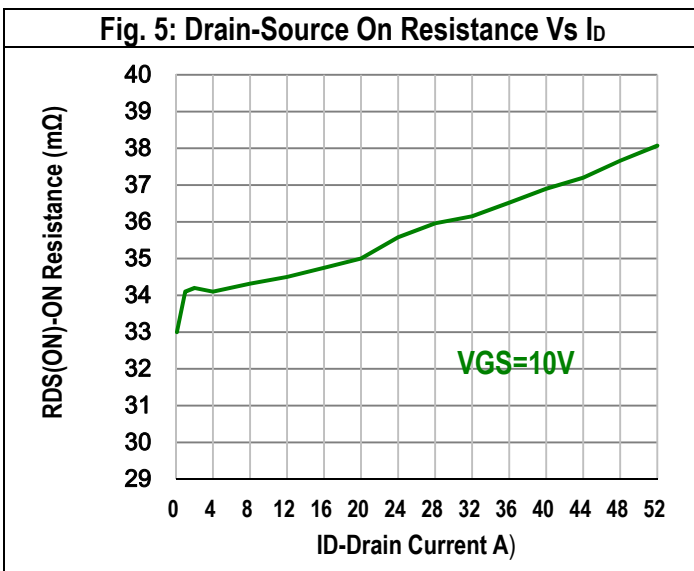
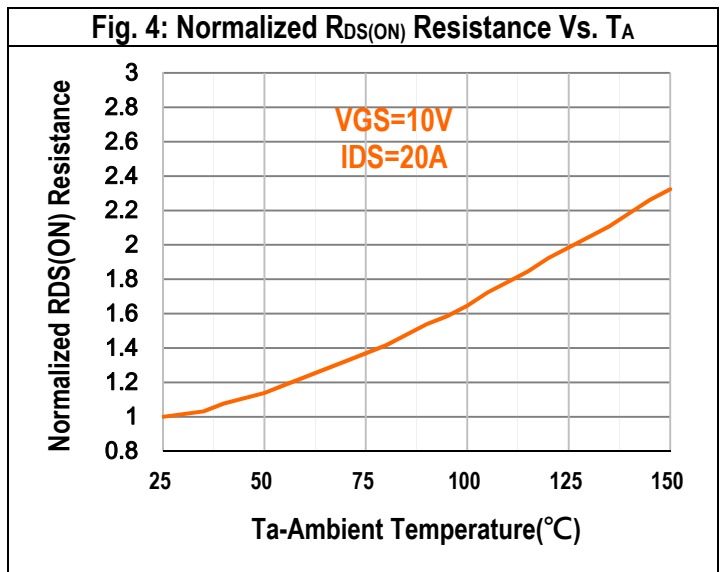
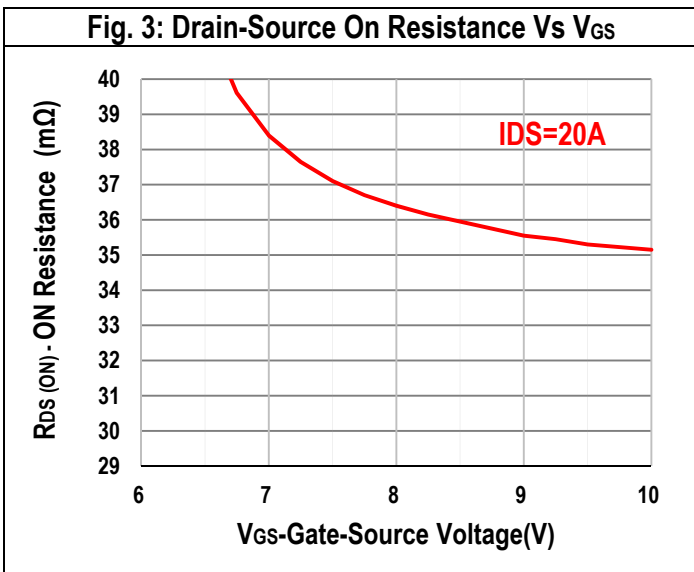
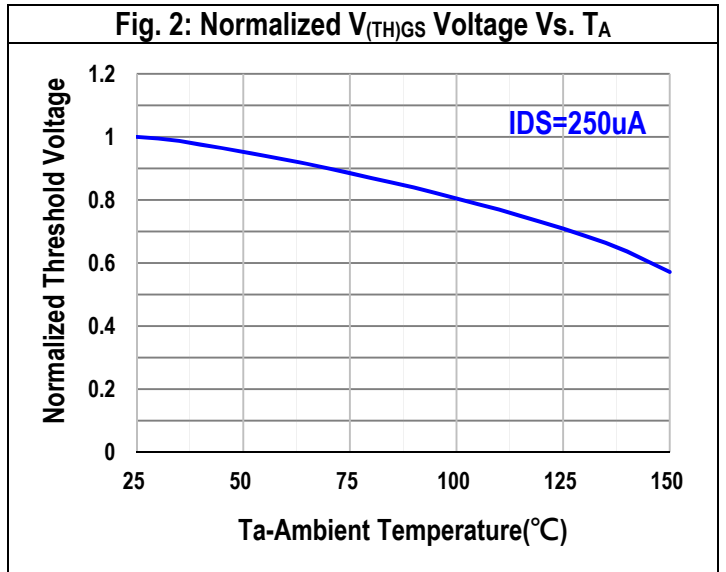
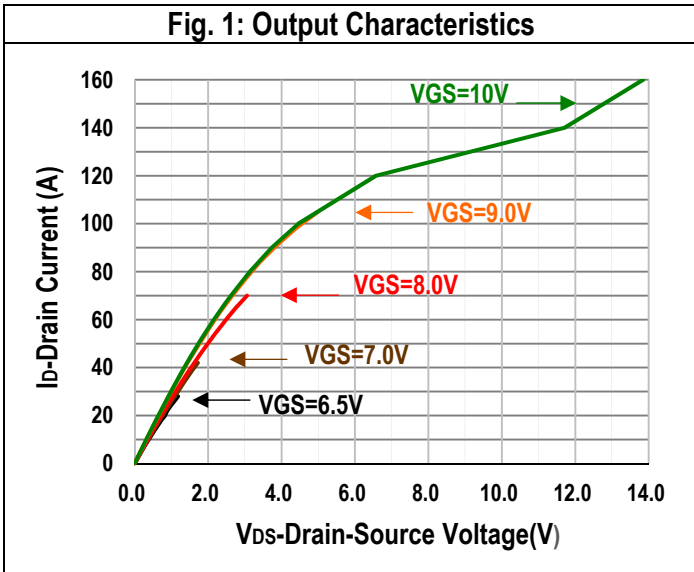
STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250μA	3.5	3.9	4.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _{DS} =20A	-	33	37	mΩ
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1.6	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =10V, I _{DS} =10A	-	20	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iSS}	V _{DD} =600V, V _{DS} =300V, V _{GS} =0V, F=500KHz	-	6570	-	pF
Output Capacitance	C _{oSS}	V _{DD} =600V, V _{DS} =300V, V _{GS} =0V, F=500KHz	-	106	-	pF
Reverse Transfer Capacitance	C _{rSS}	V _{DD} =600V, V _{DS} =300V, V _{GS} =0V, F=500KHz	-	138	-	pF
Effective output capacitance-energy	C _{o(er)}	V _{DD} =400V, V _G =10V, energy related ^{Note 6}	-	331	-	pF
Effective output capacitance-time	C _{o(tr)}	V _{DD} =400V, V _G =10V, time related ^{Note 7}	-	2140	-	pF
Turn-On Delay Time	T _{d(on)}	V _{DS} =400V, V _{GS} =10V, I _{DS} =13.3A, R _{GEN} =10Ω	-	61.5	-	nS
Rise Time	t _r	V _{DS} =400V, V _{GS} =10V, I _{DS} =13.3A, R _{GEN} =10Ω	-	32.7	-	nS
Turn-Off Delay Time	T _{d(off)}	V _{DS} =400V, V _{GS} =10V, I _{DS} =13.3A, R _{GEN} =10Ω	-	194.7	-	nS
Fall Time	t _f	V _{DS} =400V, V _{GS} =10V, I _{DS} =13.3A, R _{GEN} =10Ω	-	28.2	-	nS

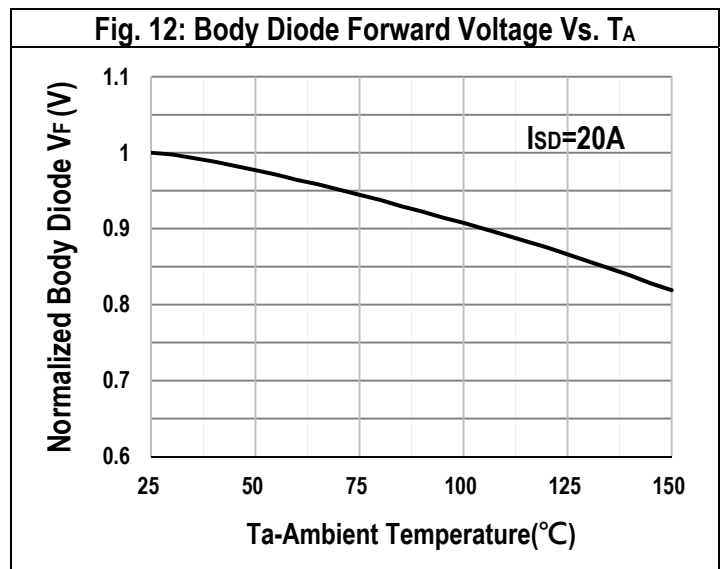
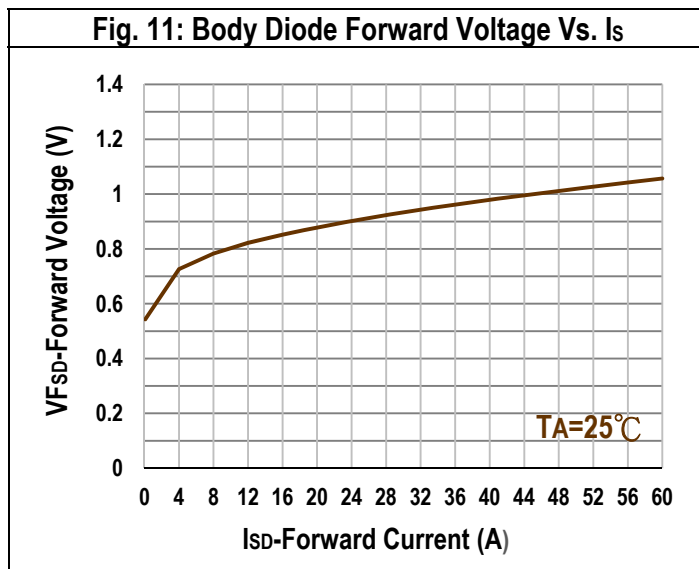
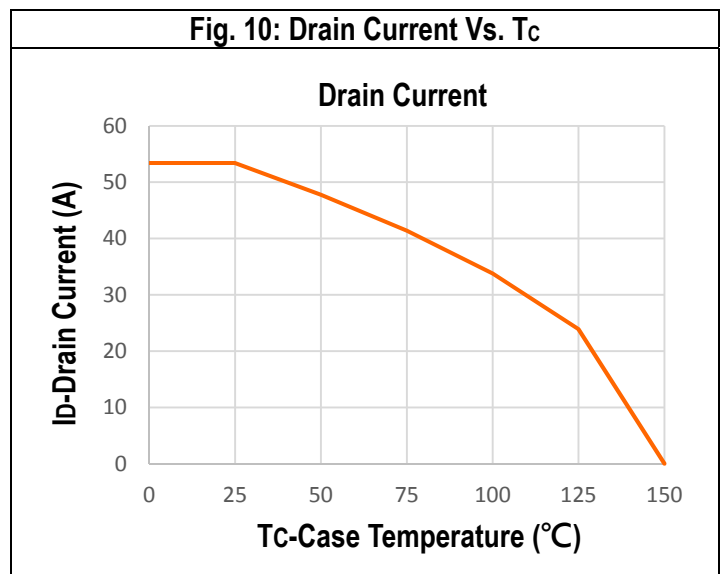
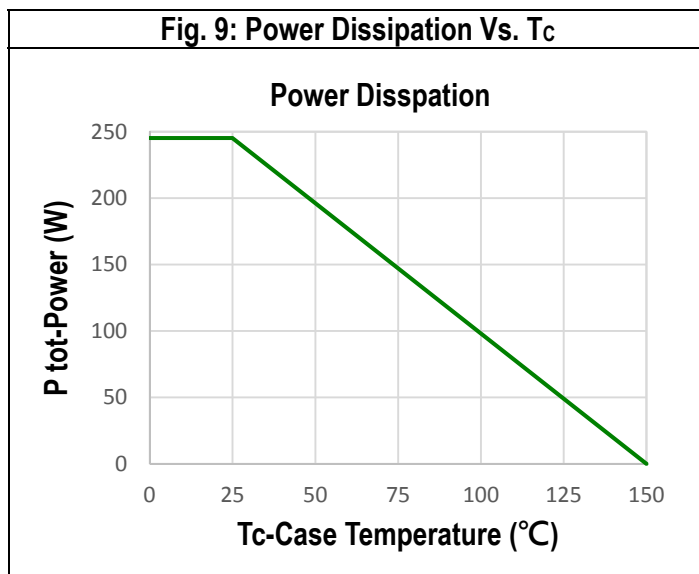
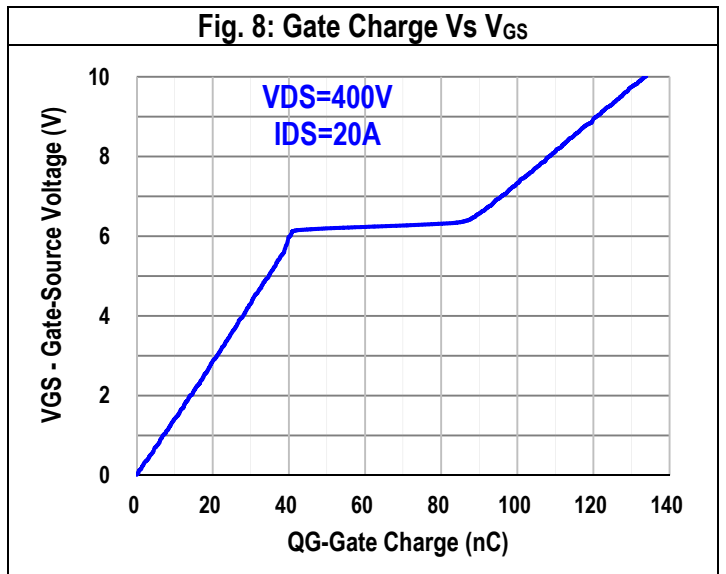
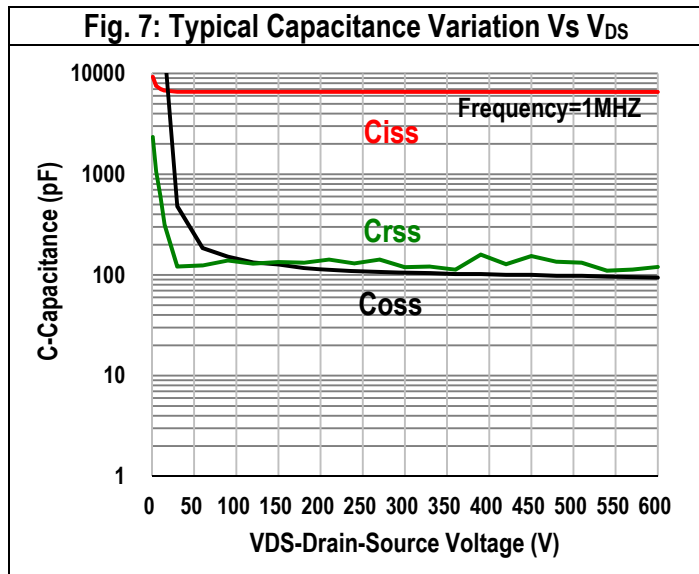
GATE CHARGE CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate charge total	Q _{g 10V}	V _{DD} =400V, I _D =20A, V _{GS} =0 to 10V	-	133	-	nC
Gate to Source Gate Charge	Q _{gs}	V _{DD} =400V, I _D =20A, V _{GS} =0 to 10V	-	45	-	nC
Gate to Drain Charge	Q _{gd}	V _{DD} =400V, I _D =20A, V _{GS} =0 to 10V	-	43.5	-	nC
Gate plateau voltage	V _{plateau}	V _{DD} =400V, I _D =20A, V _{GS} =0 to 10V	-	6.2	-	V

BODY DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Diode continuous forward current	I _S	T _C =25°C	-	-	53.3	A
Diode pulsed forward current	I _{SM}	T _C =25°C	-	-	160	A
Diode forward Voltage	V _{SD}	T _C =25°C, V _{GS} =0V, I _S = 20A	-	0.86	1.0	V
Diode reverse Recovery Time	t _{rr}	V _{DD} =400V, I _{SD} =20A, T _C =25°C, di/dt=100A/μs	-	149	-	nS
Diode reverse Recovery Charge	Q _{rr}	V _{DD} =400V, I _{SD} =20A, T _C =25°C, di/dt=100A/μs	-	1233	-	nC
Diode peak reverse recovery current	I _{rm}	V _{DD} =400V, I _{SD} =20A, T _C =25°C, di/dt=100A/μs	-	15.8	-	A

4. Typical Operating Characteristics diagrams



4. Typical Operating Characteristics diagrams



4. Typical Operating Characteristics diagrams

Fig. 13: Safe Operation Area

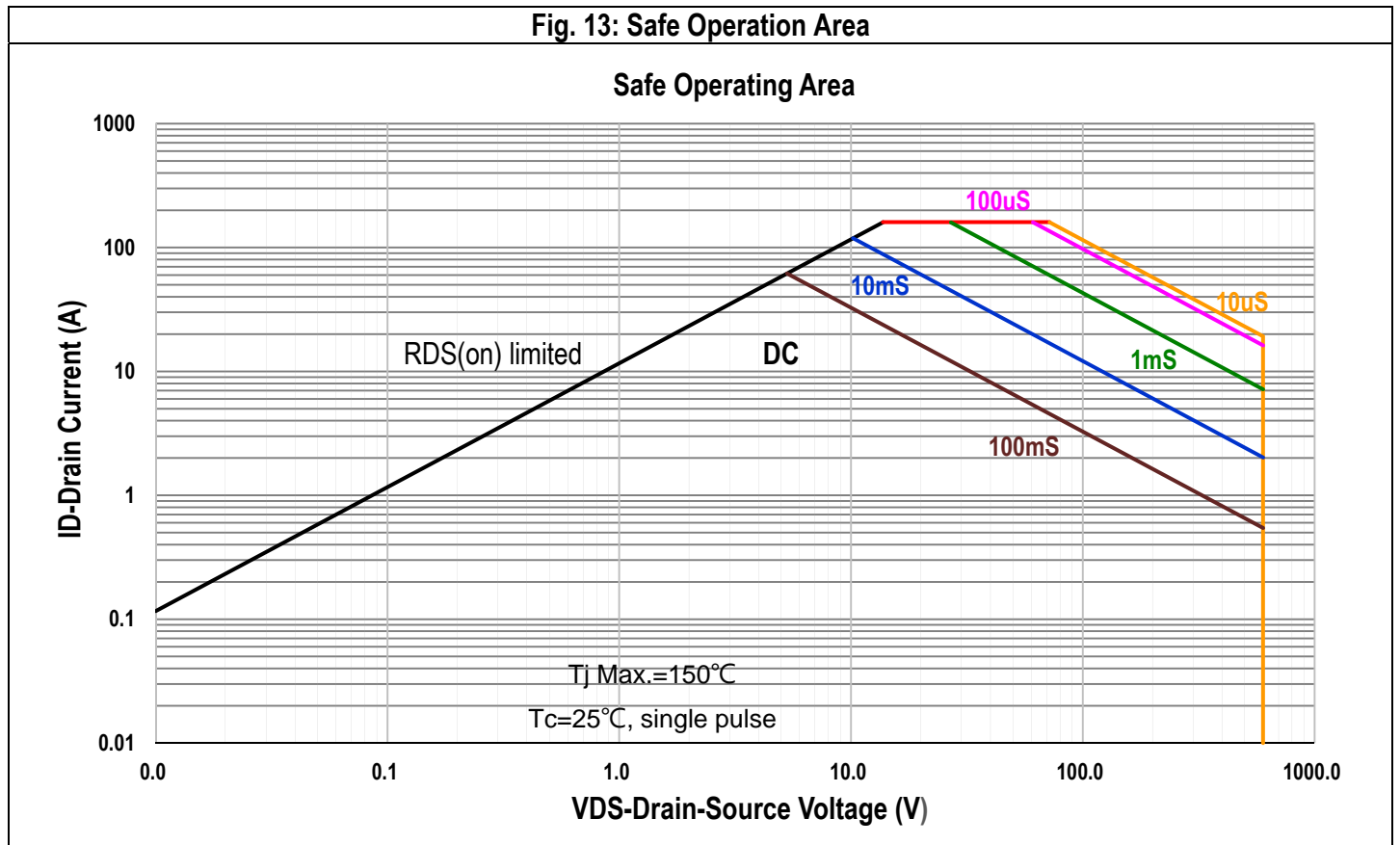
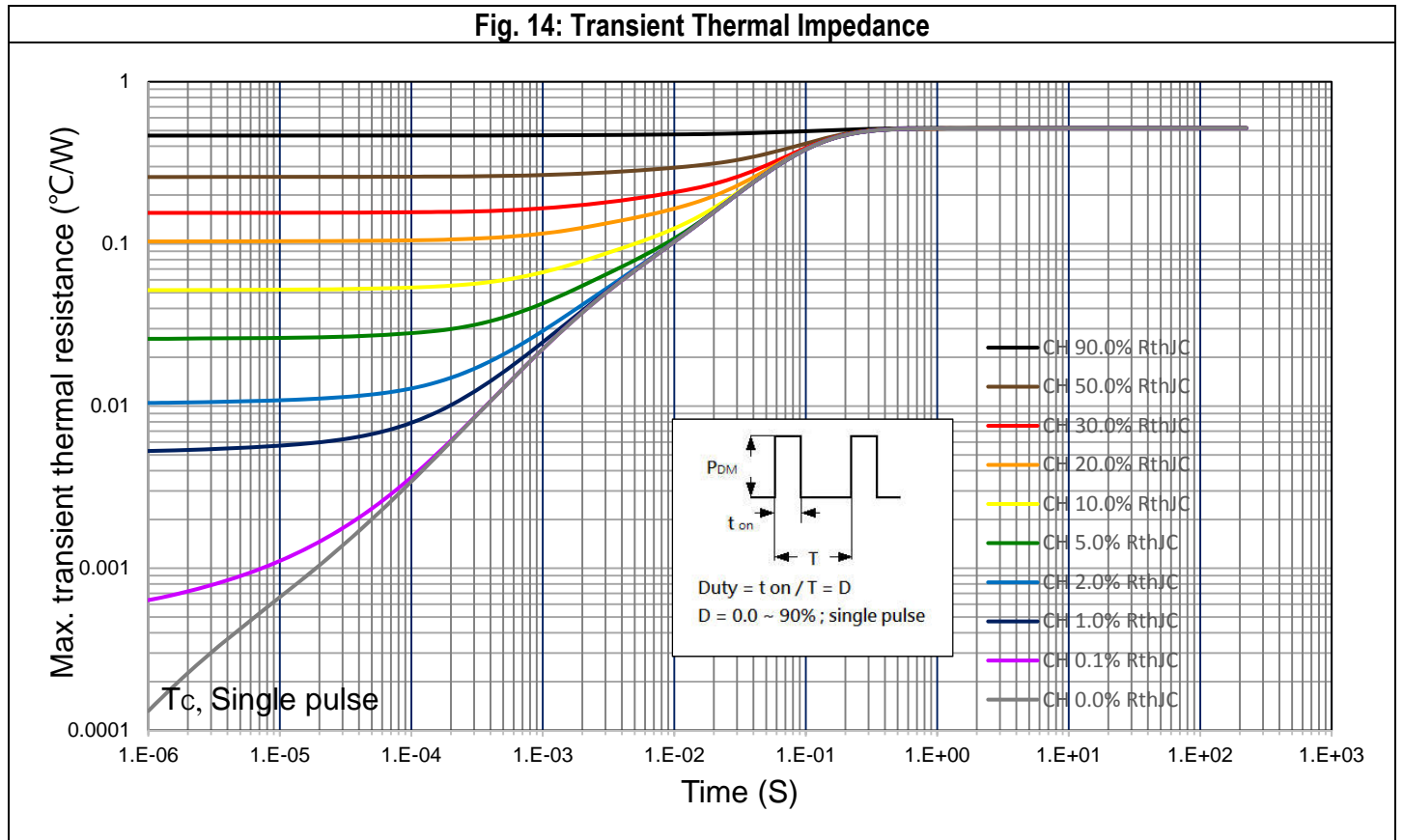


Fig. 14: Transient Thermal Impedance



5. Measurement Schematic

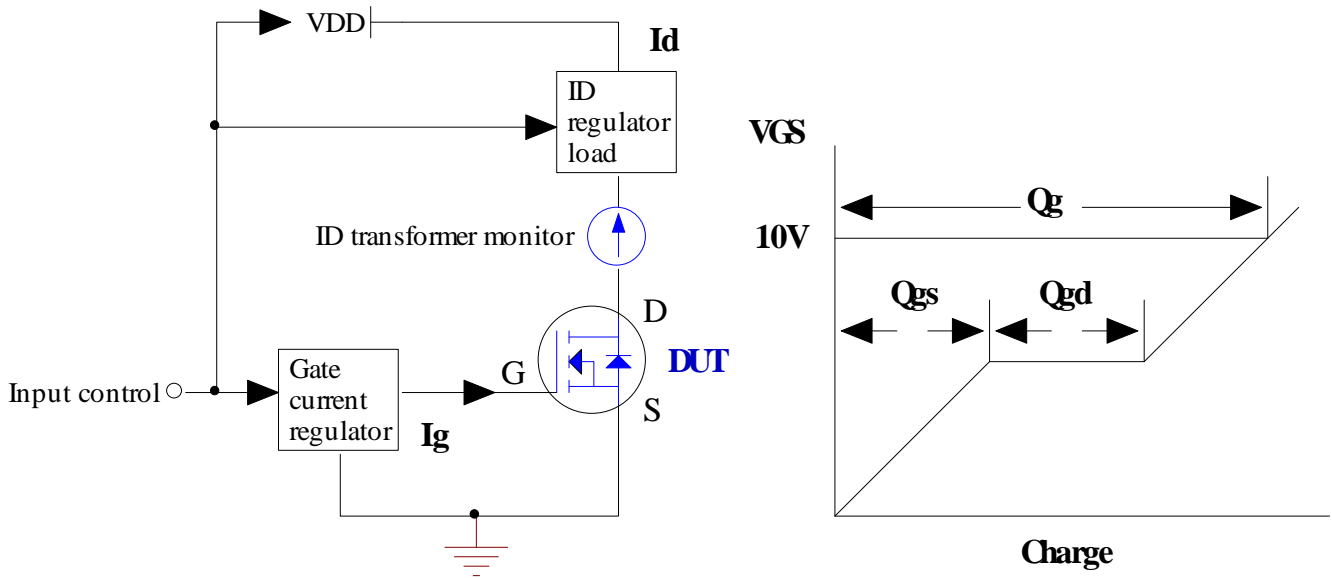


Diagram 5.1 Gate Charge Measurement Circuit and Waveforms

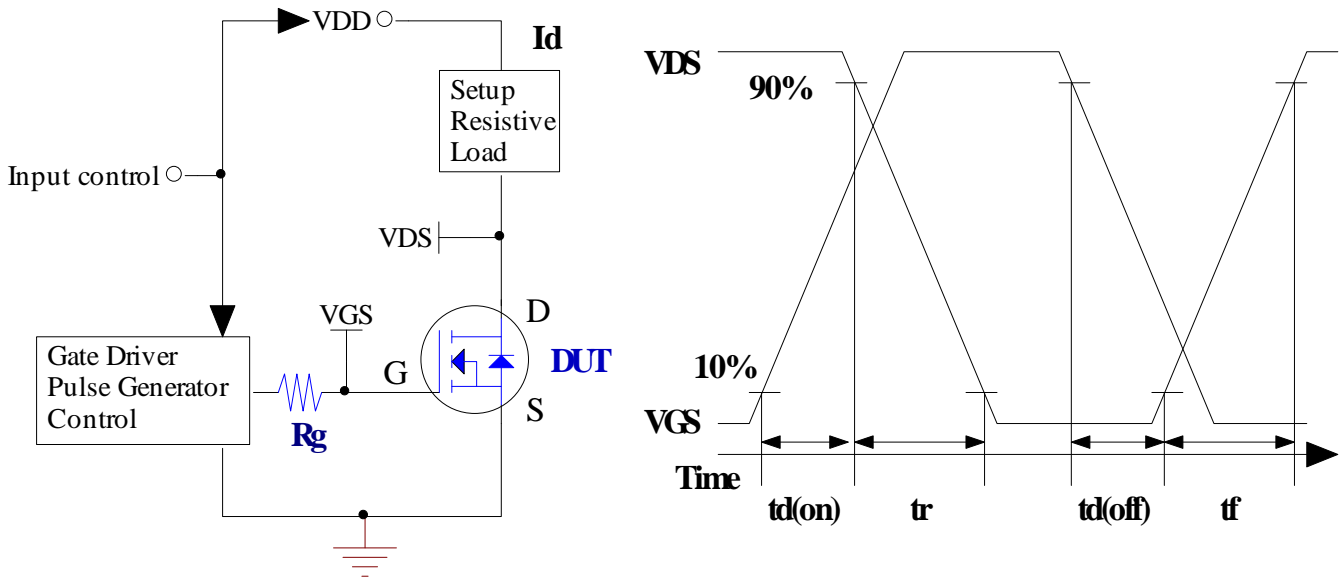


Diagram 5.2 Resistive Switching Measurement Circuit and Waveforms

5. Measurement Schematic

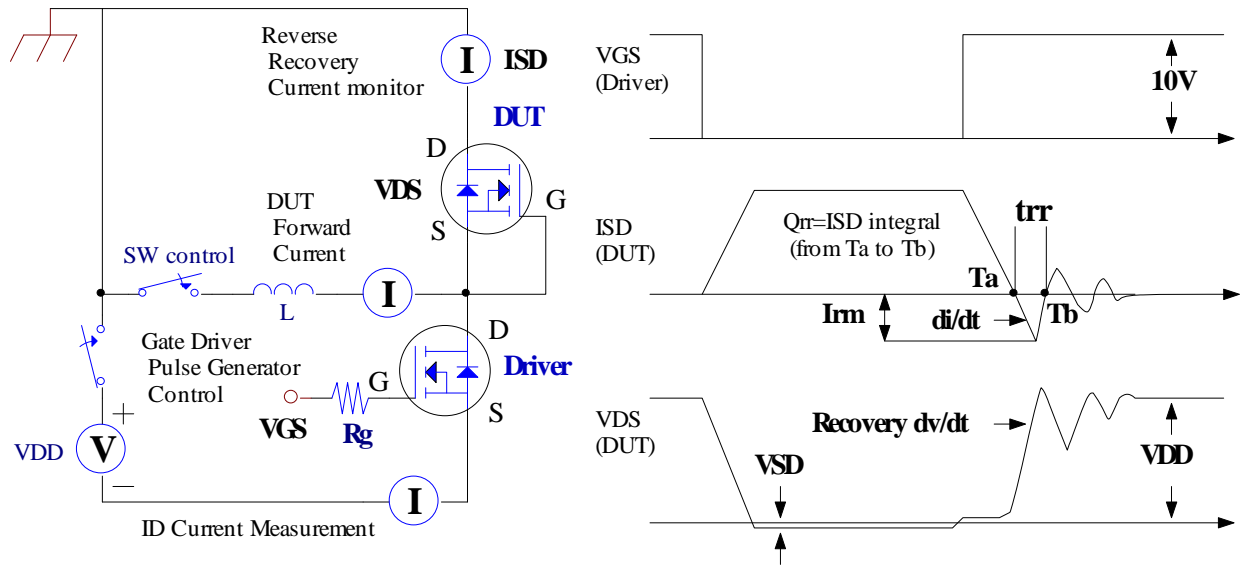


Diagram 5.3 Body Diode Recovery Characteristics Measurement Circuit and Waveforms

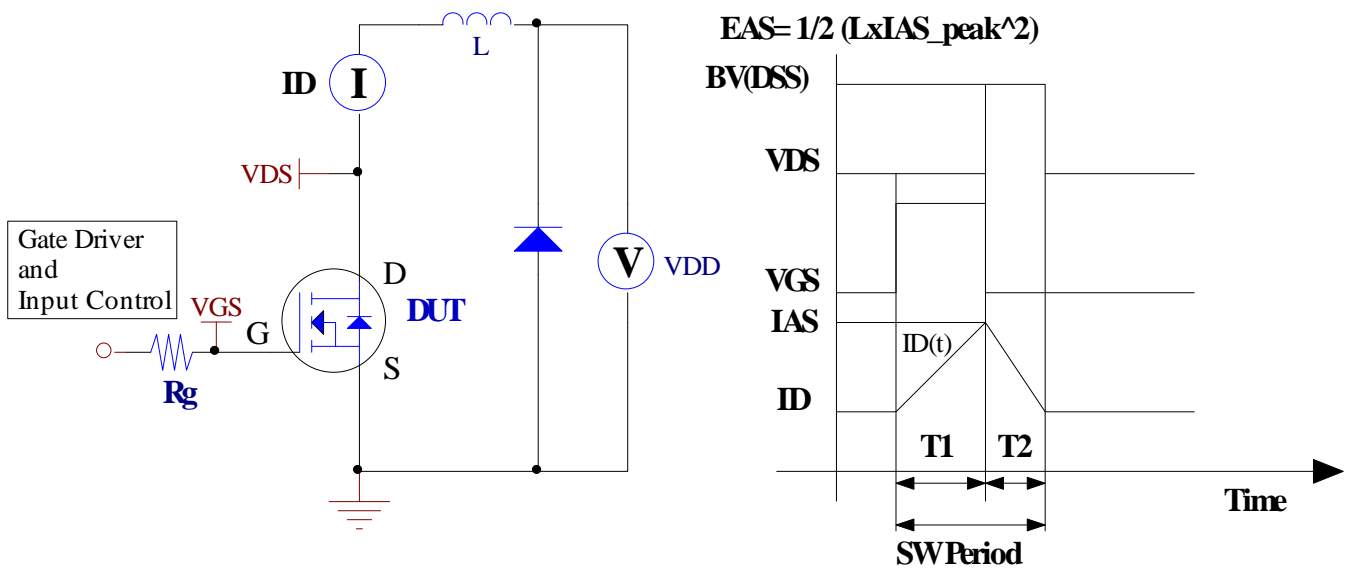
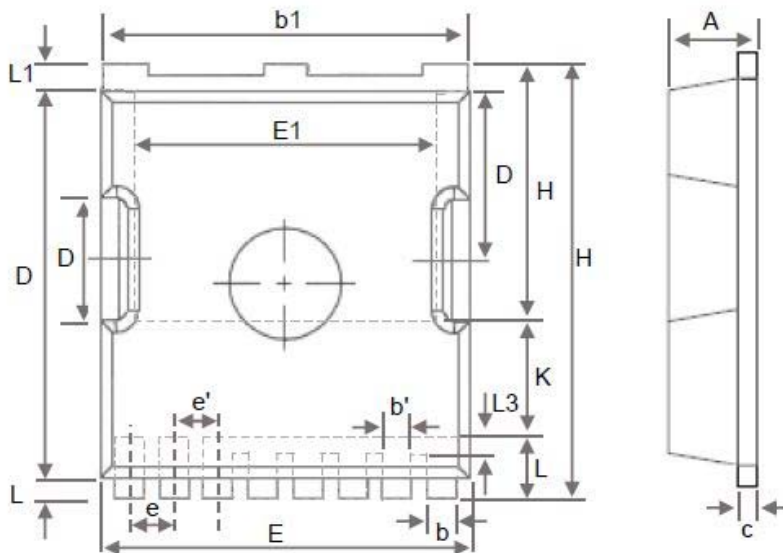


Diagram 5.4 Unclamped Inductive Switching Measurement Circuit and Waveforms

6. Package of Dimension

Package type: TOLL

H-Type

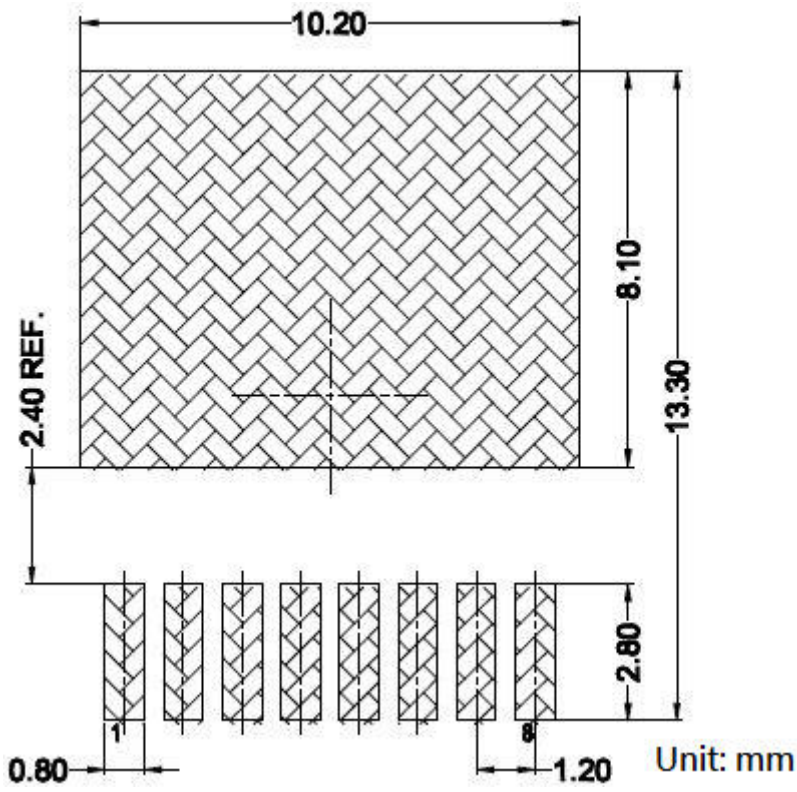


Symbol	Min	Nor	Max
A	2.15	2.30	2.45
b	0.70	0.80	0.90
b1	9.65	9.80	9.95
c	0.40	0.50	0.60
D	10.18	10.38	10.58
D2	3.15	3.30	3.45
D4	4.40	4.55	4.70
E	9.70	9.90	10.10
E1	7.95	8.10	8.25
e	1.20 ref.		
H	11.48	11.68	11.88
H1	6.80	6.95	7.10
K	2.80	2.87	2.93
L	1.40	1.75	2.10
L1	0.50	4.75	9.00
L2	0.48	0.60	0.72
L3	0.30	0.55	0.80
*b'	0.65	0.78	0.90
*e'	1.20 ref.		

*b' : H-Type 2~7 lead width

*e' : H-Type 2~7 lead pitch

7. Land pattern (Footprint)



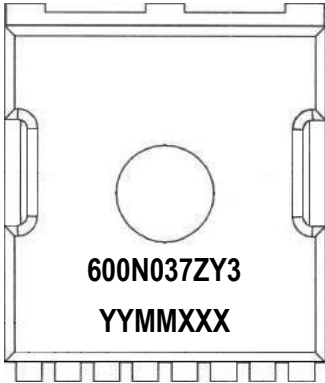
Note 1: Land pattern (Footprint) design is for reference only.

Note 2: Package body sizes exclude mold flash and burrs.

Note 3: Dimension is measured in gauge plane.

Note 4: Tolerance 0.1mm unless otherwise specified.

8. Marking Information

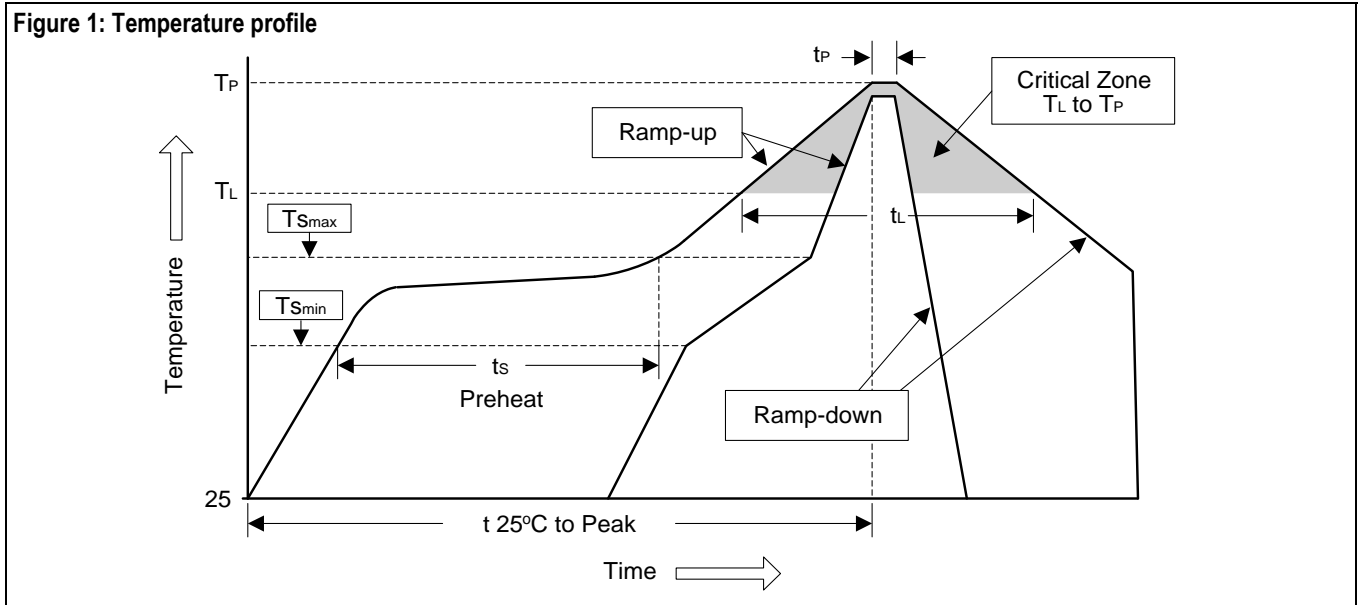
TOLL (Z)	Marking Rule
<p>Laser Marking</p>  <p>The diagram shows a top-down view of a MOSFET package. In the center, there is a circular marking area containing the text '600N037ZY3' on the top line and 'YYMMXXX' on the bottom line. The package has a square shape with a notch at the top and a series of pins along the bottom edge.</p>	<p><u>Line 1</u> : Device 600N037ZY3</p> <p><u>Line 2</u> : Date Code YYMMXXX</p> <p>YY : Year Code MM : Month Code XXX : Serial Number</p>

9. Appendix

Appendix-A

Soldering Methods for Silicongear's Products (Just for SMD type of device)

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

9. Appendix**Appendix-B****Important Notice****© Silicongear Corporation**

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