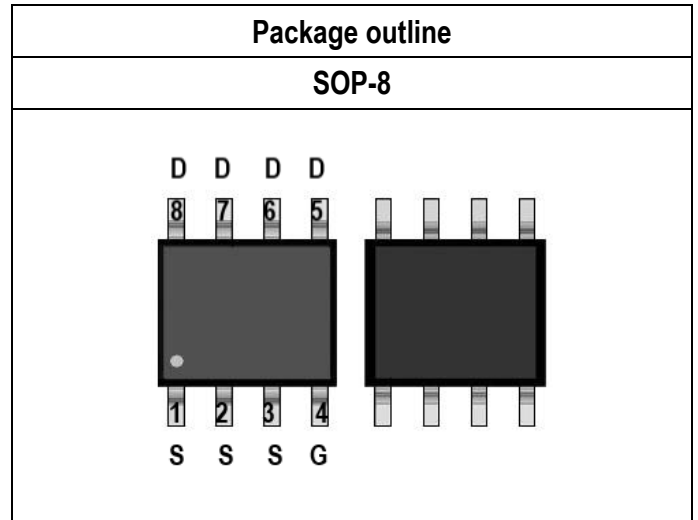


Key parameter	N <sub>channel</sub>	Unit
$V_{(BR)DSS}$ min.	100	V
$R_{DS(ON)}$ max. $V_{GS}=10V$	10	m $\Omega$
$R_{DS(ON)}$ max. $V_{GS}=4.5V$	13	m $\Omega$
$I_D$	35.8	A
$V_{GS(TH)}$ Typ.	1.6	V
$C_{iss}$ Typ.	1708	pF
$Q_g$ 10V Typ.	36.3	nC



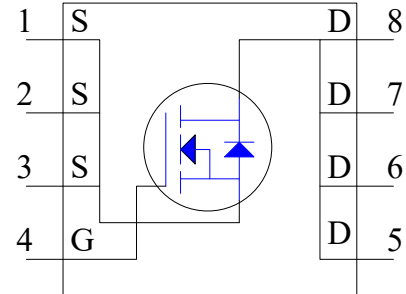
### Description

The DG100N03S used double-gate structure of MOSFET to provide excellent electrical parameter. There is high speed switching capability, low  $R_{DS(ON)}$  resistance, stabilizing qualified and characteristics for these devices. Moreover, it is had extreme high cell density in design. These features combine to be an advantage design for use in wide variety of application including small signal control and load switch application.

### Features

- ◇ Fast switch capacity
- ◇ Low  $R_{DS(ON)}$  resistance
- ◇ With voltage logic level driving characteristics
- ◇ Pb-free lead plating; RoHS compliant

### Symbol and Pin assignment



### Potential application

- AC-DC adaptor
- DC-DC converter
- Load Switch
- Electric tool application
- LED Applications
- Synchronous Rectifier for Power Delivery

### Order Information

Item	Description
1. Order Code	DG100N03S
2. Part Number	DG100N03S
3. Package Type	SOP-8
4. Package Code	S
5. Packing Type	Tape & Reel
6. Quantity in Pack	3,000
7. RoHS Status	Halogen-Free

## Content

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## 1. Absolute Maximum Ratings ( $T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	100	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Drain Current-Continuous <sup>Note 1</sup>	$T_C=25^\circ\text{C}$	$I_D$	35.8	A
	$T_C=100^\circ\text{C}$		22.6	A
Drain Current-Continuous <sup>Note 2</sup>	$T_A=25^\circ\text{C}$	$I_D$	8.9	A
	$T_A=70^\circ\text{C}$		7.2	A
Drain Current-Pulsed <sup>Note 3</sup>	$T_A=25^\circ\text{C}$	$I_{DM}$	140	A
Avalanche Current		$I_{AR}$	26.2	A
Single Pulse Avalanche Energy <sup>Note 4</sup>		$E_{AS}$	34.4	mJ
Maximum Power Dissipation	$T_C=25^\circ\text{C}$	$P_D$	29.2	W
	$T_C=100^\circ\text{C}$		11.7	W
	$T_A=25^\circ\text{C}$		1.8	W
	$T_A=70^\circ\text{C}$		1.2	W
	Derate Factor Above $T_C=25^\circ\text{C}$		0.235	W/ $^\circ\text{C}$
Max. Operating Junction Temperature		$T_J$	150	$^\circ\text{C}$
Operating and Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

## 2. Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Thermal resistance, Junction-Case	$R_{\theta JC-N}$	Please refer to Note 5	-	-	4.2	$^\circ\text{C}/\text{W}$
Thermal resistance, Junction-Ambient	$R_{\theta JA-N}$	Please refer to Note 5	-	-	67.2	$^\circ\text{C}/\text{W}$

### Notes:

- Limited by silicon chip capability and  $R_{\theta JC-N}$  junction-to-case thermal resistance.
- The maximum current rating is limited by package and  $R_{\theta JA-N}$  junction-to-ambient thermal resistance.
- Must be ensure junction temperature does not exceed 150-degree C. (Pulse Width  $\leq 100\mu\text{s}$ , Duty  $\leq 2\%$ )
- Limited by  $T_{Jmax}$ , starting  $T_J=25^\circ\text{C}$ ,  $L=0.1\text{mH}$ ,  $R_g=25\Omega$ ,  $I_D=26.2\text{A}$ ,  $V_{GS}=10\text{V}$ .
- The value of thermal resistance is measured with the single device mounted on 1 inch<sup>2</sup> FR-4 PCB with 2 oz. copper under a still air environment temperature is  $25^\circ\text{C}$  based on JEDEC standard JESD51-14 and JESD51-2a. Thermal resistance obtained depends on the user's specific board design and given application.

### 3. Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)

STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>DS</sub> =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V	-	-	1	μA
		V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, T <sub>J</sub> =125°C	-	-	100	μA
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA

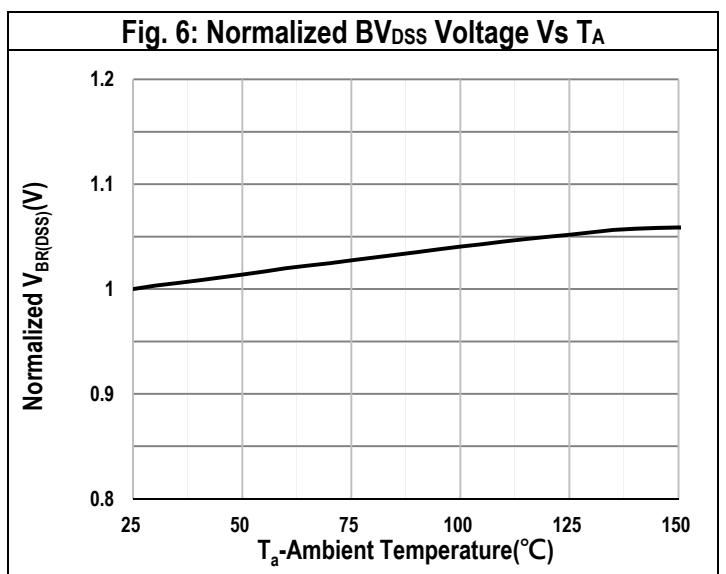
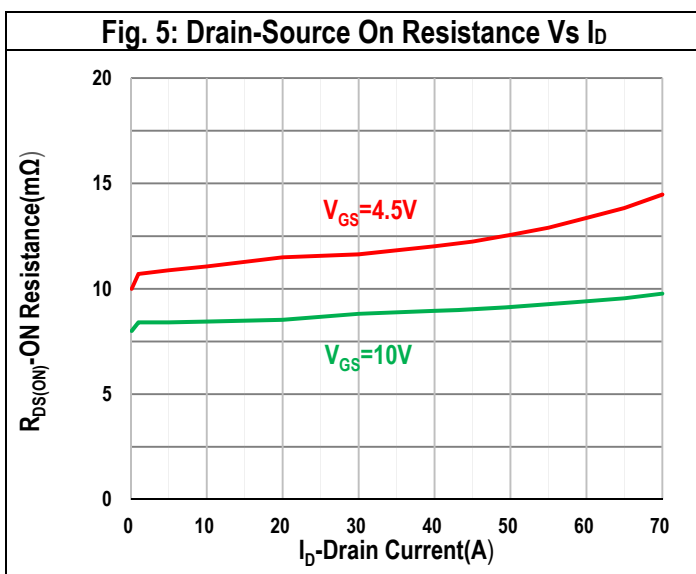
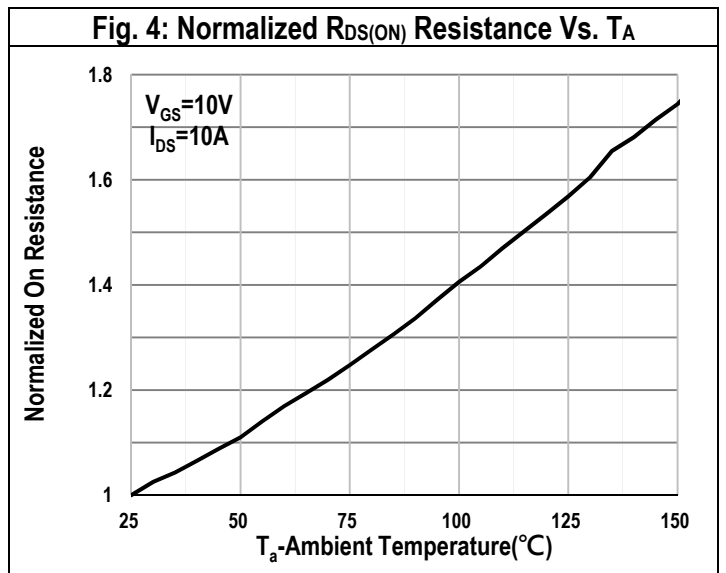
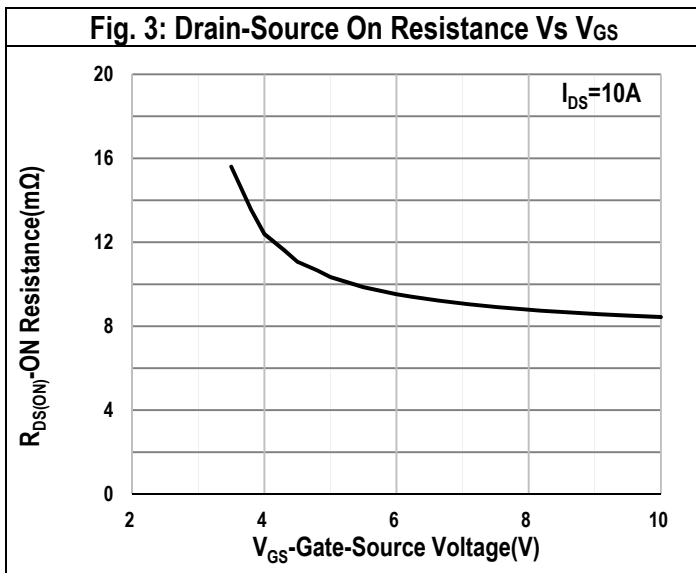
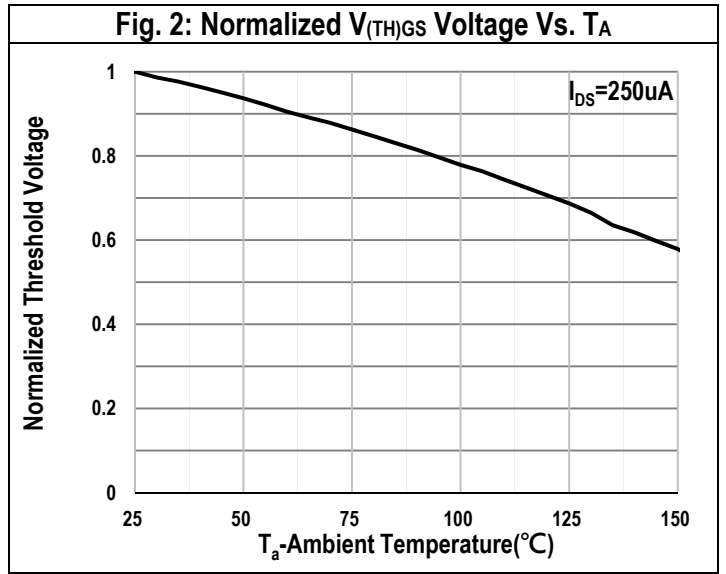
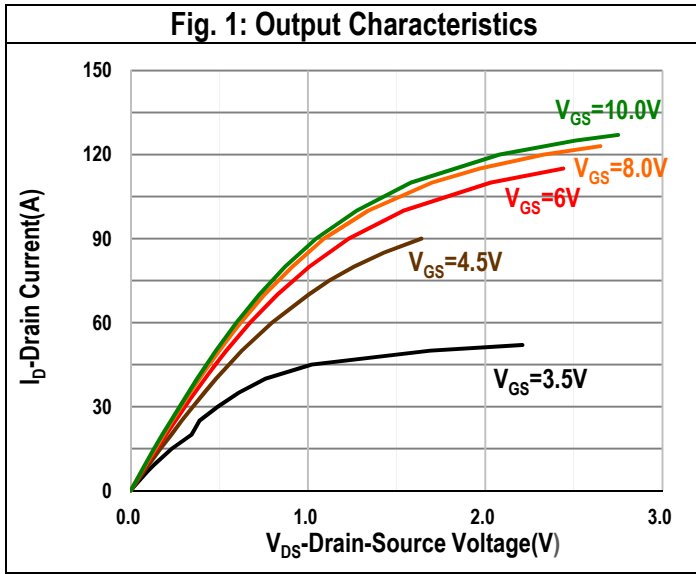
STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =250μA	1.2	1.6	2.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>DS</sub> =10A	-	8.3	10.0	mΩ
		V <sub>GS</sub> =4.5V, I <sub>DS</sub> =10A	-	10.8	13.0	mΩ
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	-	0.7	-	Ω
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>DS</sub> =5A	-	13.7	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C <sub>iss</sub>	V <sub>DD</sub> =100V, V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	1708	-	pF
Output Capacitance	C <sub>oss</sub>	V <sub>DD</sub> =100V, V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	267	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	V <sub>DD</sub> =100V, V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f=1MHz	-	11.7	-	pF
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>DS</sub> =10A, R <sub>GEN</sub> =3Ω	-	8.1	-	nS
Rise Time	T <sub>r</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>DS</sub> =10A, R <sub>GEN</sub> =3Ω	-	20.9	-	nS
Turn-Off Delay Time	T <sub>d(off)</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>DS</sub> =10A, R <sub>GEN</sub> =3Ω	-	27.4	-	nS
Fall Time	T <sub>f</sub>	V <sub>DS</sub> =50V, V <sub>GS</sub> =10V, I <sub>DS</sub> =10A, R <sub>GEN</sub> =3Ω	-	20.7	-	nS

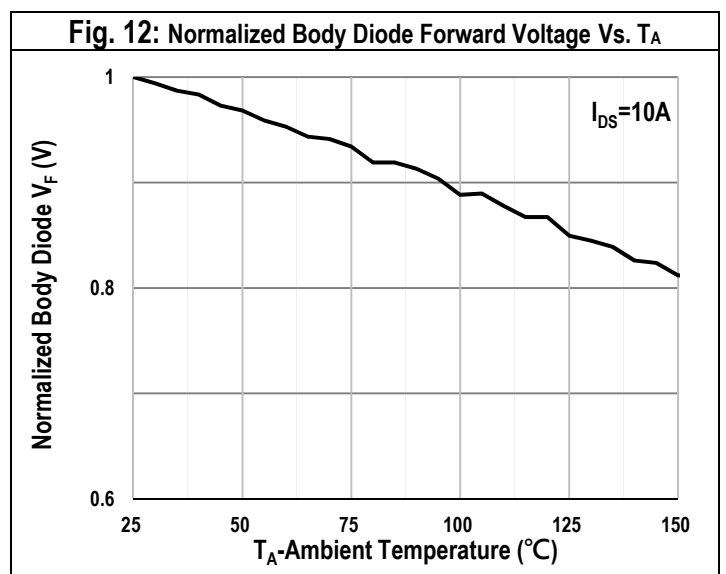
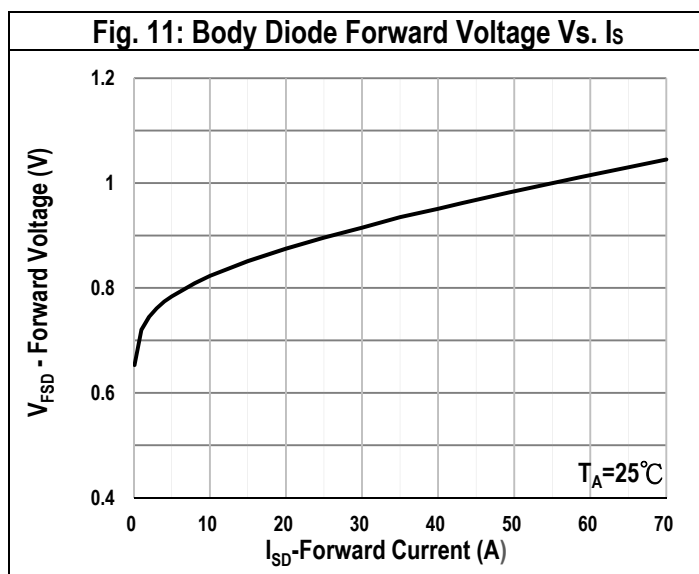
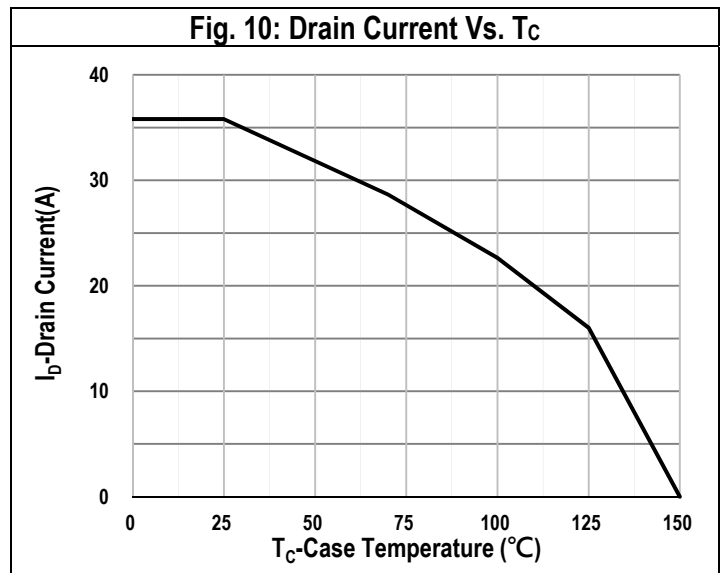
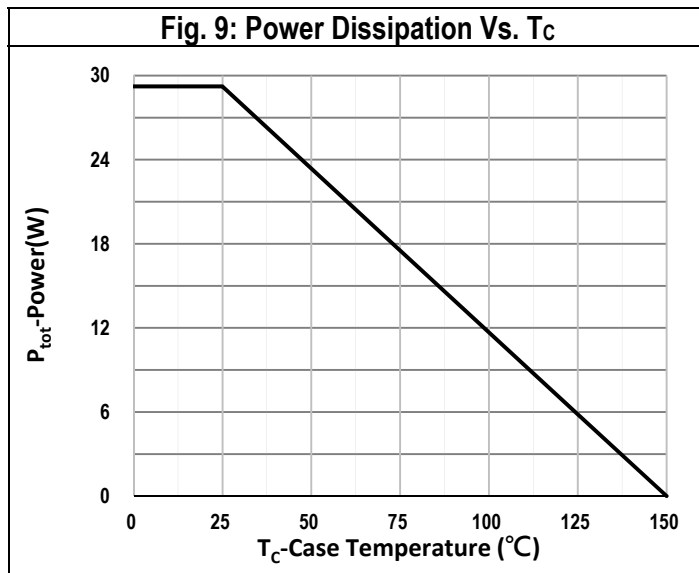
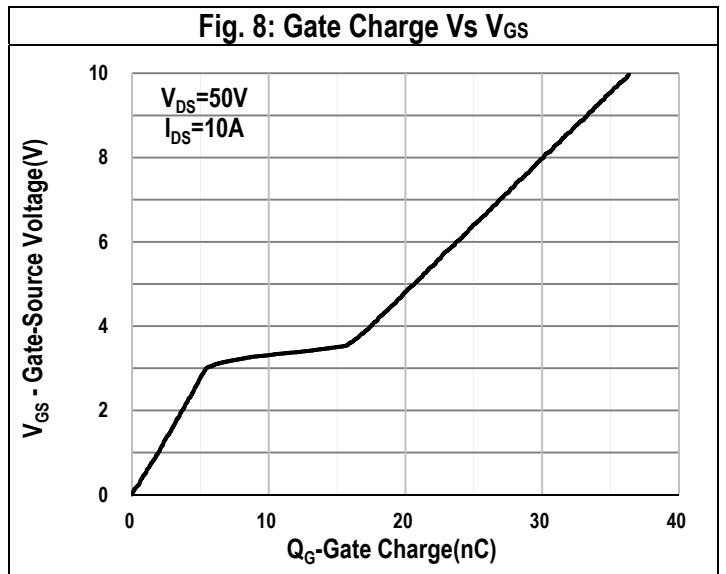
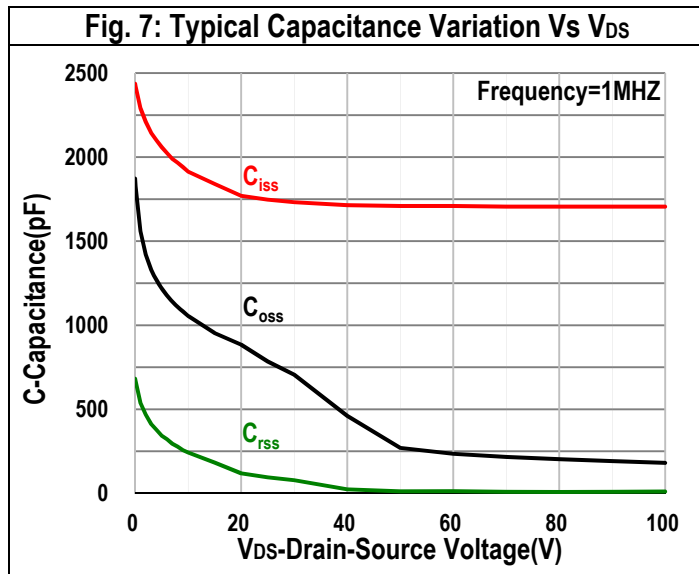
GATE CHARGE CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate to Source Gate Charge	Q <sub>gs</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	6.1	-	nC
Gate charge at threshold	Q <sub>g(th)</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	3.1	-	nC
Gate to Drain Charge	Q <sub>gd</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	9.4	-	nC
Switching charge	Q <sub>sw</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	12.3	-	nC
Gate charge total	Q <sub>g 10V</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	36.3	-	nC
Gate charge total	Q <sub>g 4.5V</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 4.5V	-	18.9	-	nC
Gate plateau voltage	V <sub>plateau</sub>	V <sub>DD</sub> =50V, I <sub>D</sub> =10A, V <sub>GS</sub> =0 to 10V	-	3.2	-	V
Gate charge total, sync. FET (Q <sub>g</sub> - Q <sub>gd</sub> )	Q <sub>g(sync)</sub>	V <sub>DS</sub> =0.1V, V <sub>GS</sub> =0 to 10V	-	26.8	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Body Diode continuous forward current	I <sub>S</sub>	T <sub>C</sub> =25°C	-	-	35.8	A
Body Diode pulse current	I <sub>SM</sub>	T <sub>C</sub> =25°C	-	-	140	A
Body Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =10A	-	0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=100A/μs	-	44.4	-	nS
		V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=200A/μs	-	34.6	-	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=100A/μs	-	44.5	-	nS
		V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=200A/μs	-	78	-	nC
Body Diode Reverse Recovery Current	I <sub>rm</sub>	V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=100A/μs	-	-1.6	-	A
		V <sub>DD</sub> =50V, I <sub>F</sub> =10A, di/dt=200A/μs	-	-3.7	-	A

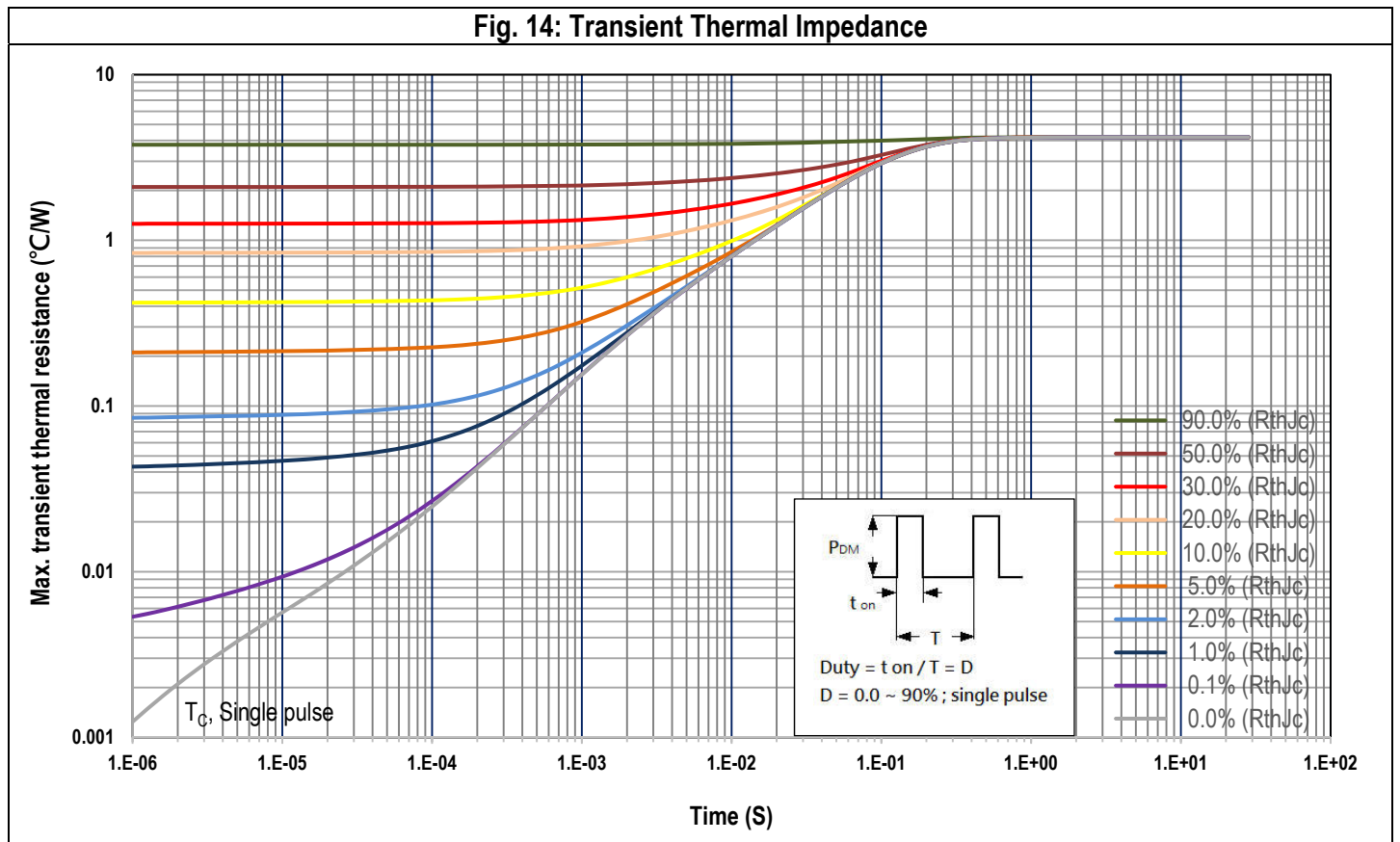
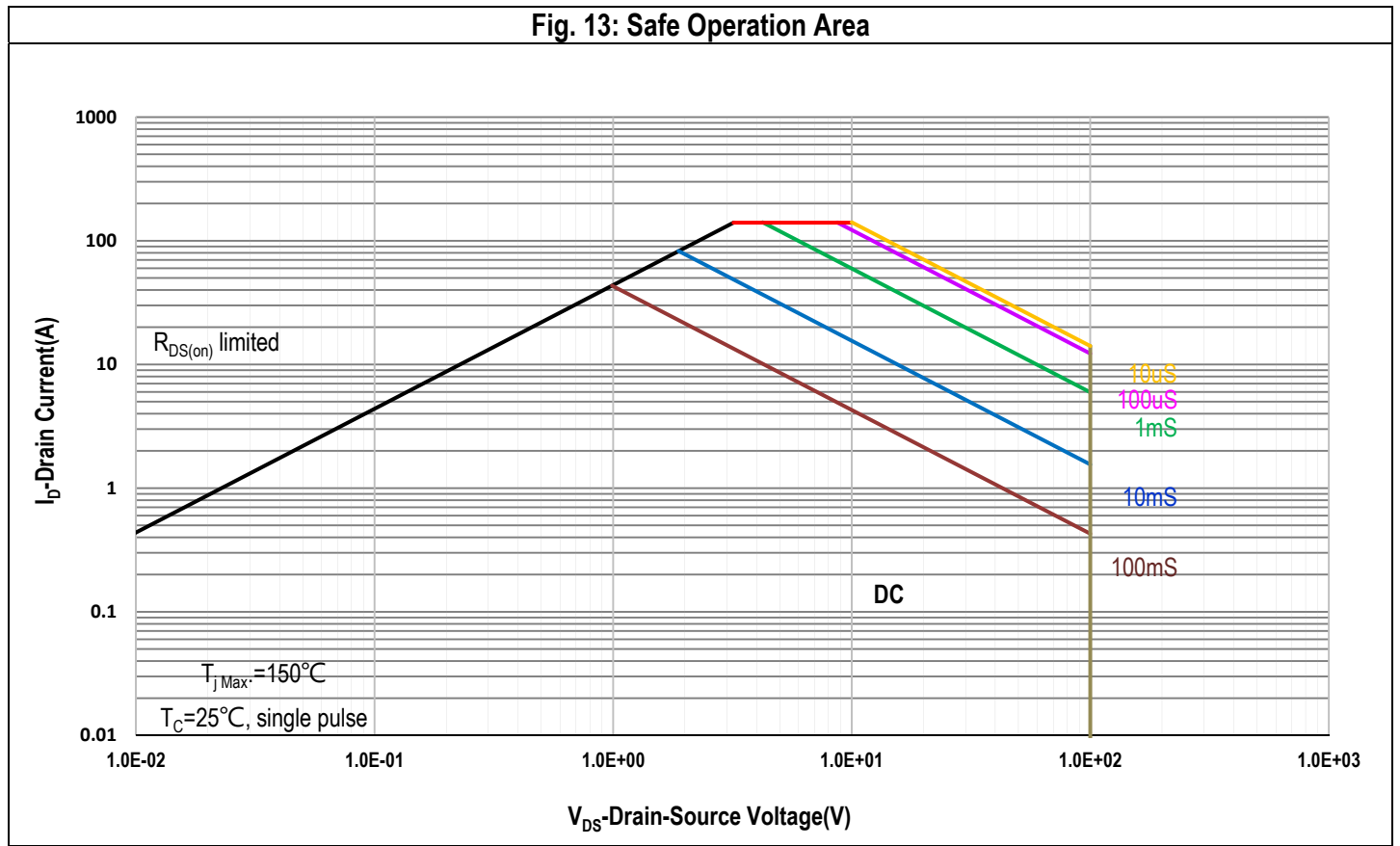
## 4. Typical Operating Characteristics Diagram



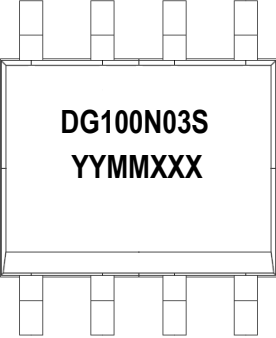
## 4. Typical Operating Characteristics Diagram



## 4. Typical Operating Characteristics Diagram



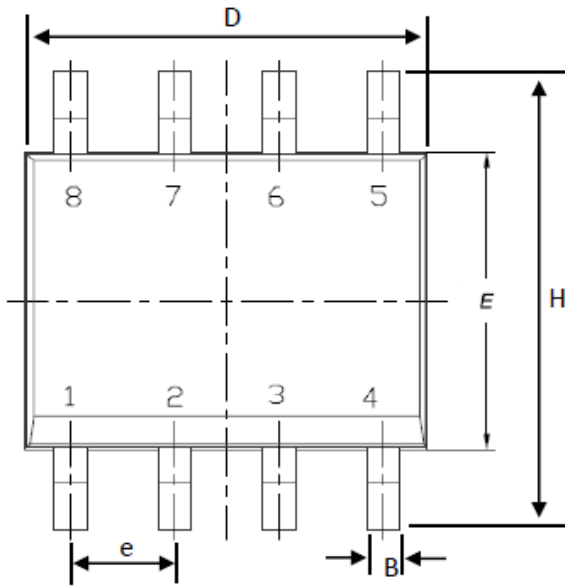
## 5. Marking Information

SOP-8 (S)	Marking Rule
<p>Laser Marking</p> 	<p><u>Line 1</u> : Device DG100N03S</p> <p><u>Line 2</u> : Date Code YYMMXXX</p> <p>YY : Year Code MM : Month Code XXX : Serial Number</p>



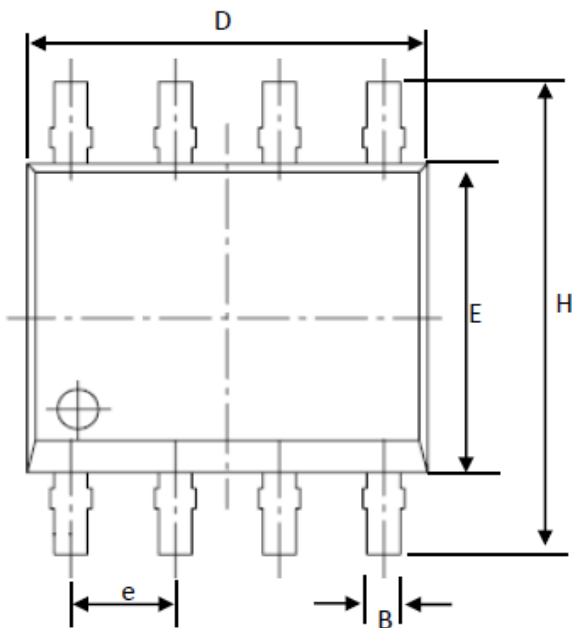
## 6. Package of Dimension

G-TYPE



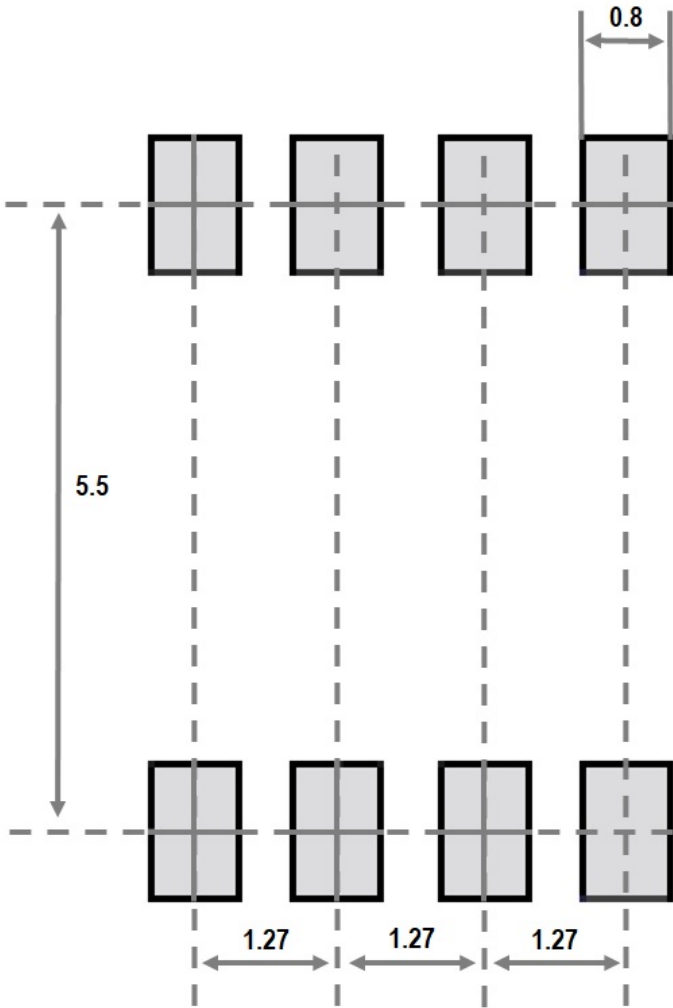
Symbol	Min	Nor	Max
A	1.35	1.55	1.75
A1	0.10	0.18	0.25
B	0.31	0.41	0.51
c	0.17	0.21	0.25
D	4.80	4.90	5.00
E	3.80	3.90	4.00
e	1.27	1.27	1.27
H	5.80	6.00	6.20
L	0.40	0.84	1.27
$\alpha$	0.00	4.00	8.00

B-TYPE



1. All dimension are in millimeters.
2. Dimension does not include burrs and mold flash/protrusions.

## 7. Land pattern (Footprint)



Note 1: Land pattern (Footprint) design is for reference only.

Note 2: Package body sizes exclude mold flash and burrs.

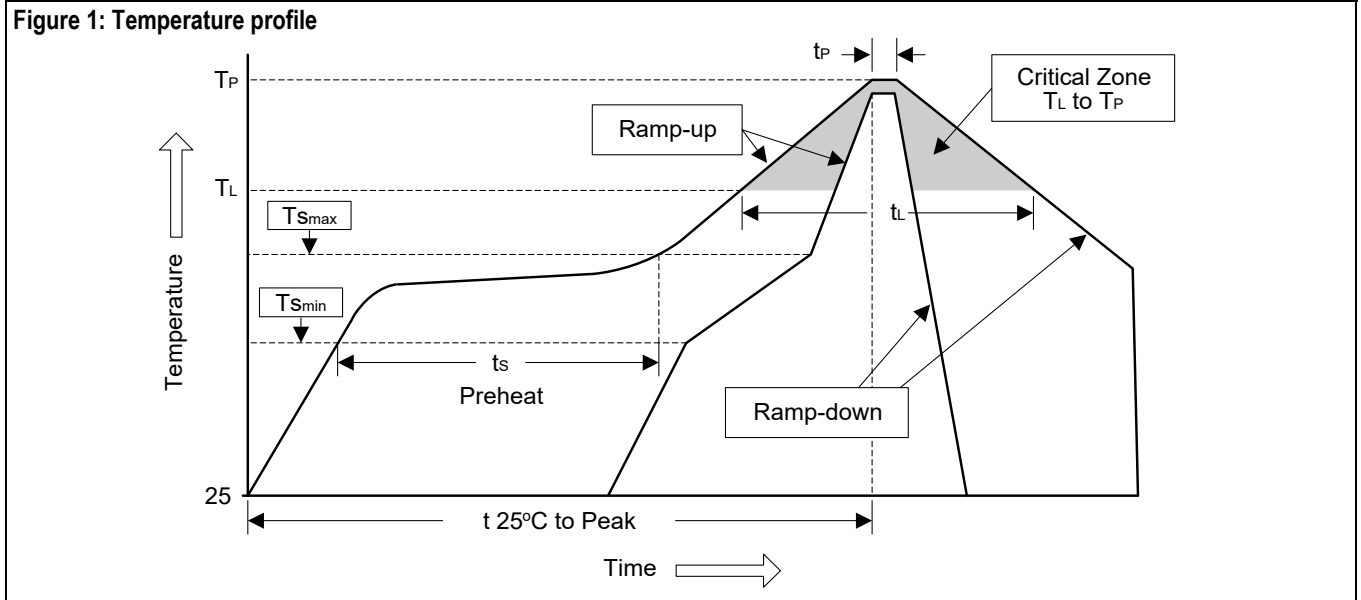
Note 3: Dimension is measured in gauge plane.

Note 4: Tolerance 0.1mm unless otherwise specified.

## 8. Appendix-A

### Soldering Methods for Silicongear's Products (Just for SMD type of device)

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min ( $T_{smin}$ )	100°C	150°C
- Temperature Max ( $T_{smax}$ )	150°C	200°C
- Time (min to max) ( $t_s$ )	60 to 120 sec	60 to 180 sec
$T_{smax}$ to $T_L$		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature ( $T_L$ )	183°C	217°C
- Time ( $t_L$ )	60 to 150 sec	60 to 150 sec
Peak Temperature ( $T_P$ )	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature ( $t_P$ )	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

### 3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

**8. Appendix-B****Important Notice****© Silicongear Corporation**

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