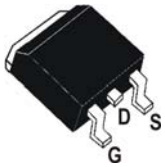
SG100P01D

-100V P-Channel Power MOSFET

V_{DSS} , -100V $R_{DS(ON)}$, 115mΩ (max.) @ V_{GS}=-10V I_D , -15A	TO-252	D G S



Description

The SG100P01D uses advanced Trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.

Features

- Low On-Resistance
- Low Input Capacitance
- Low Miller Charge
- Low Input / Output Leakage
- Pb-free lead plating; RoHS compliant

Applications

- Motor / Body Load Control
- Automotive Systems
- Load Switch
- DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG100P01D	Halogen-Free	TO-252	D	Tape & Reel	2,500

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-100	V
Gate-Source Voltage		V_{GS}	±25	V
Drain Current-Continuous	T_C=25°C	I_D	-15	A
	T_C=70°C		-12.1	A
Drain Current-Pulsed Note 1		I_{DM}	-50	A
Drain Current-Continuous	T_A=25°C	I_D	-3.5	A
	T_A=70°C		-2.8	A
Avalanche Current, L=0.3mH		I_{AS}	-22	A
Avalanche Energy, L=0.3mH		E_{AS}	72	mJ
Maximum Power Dissipation	T_C=25°C	P_D	48	W
	T_C=70°C		30.7	W
	T_A=25°C		2.5	W
	T_A=70°C		1.6	W
Storage Temperature Range		T_{STG}	-55 to +150	°C
Operating Junction Temperature Range		T_J	-55 to +150	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient Note 2	$R_{\theta JA}$	Steady State	-	-	50	°C/W
Maximum Junction-to-Case Note 2	$R_{\theta JC}$	Steady State	-	-	2.6	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_{DS}=-250μA	-100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-80V, V_{GS}=0V	-	-	-1	μA
Gate-Body Leakage	I_{GSS}	V_{GS}=±25V, V_{DS}=0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Gate Threshold Voltage	$V_{GS(TH)}$	V_{DS}=V_{GS}, I_{DS}=-250μA	-2	-3	-4	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	V_{GS}=-10V, I_{DS}=-12A	-	-	115	mΩ

DYNAMIC CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Input Capacitance	C_{iss}	V_{DS}=-30V, V_{GS}=0V, f=1MHz	-	1618	-	pF
Output Capacitance	C_{oss}		-	163	-	
Reverse Transfer Capacitance	C_{rss}		-	46	-	
Gate Resistance	R_g	V_{GS}=0V, V_{DS}=0V, f=1MHz	-	4.5	-	Ω

SWITCHING CHARACTERISTICS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Turn-On Delay Time	$T_{d(on)}$	V_{DS}=-50V, I_{DS} =-15A, V_{GS}=-10V, R_{GEN}=3Ω	-	27	-	ns
Rise Time	t_r		-	12	-	
Turn-Off Delay Time	$T_{d(off)}$		-	78	-	
Fall Time	t_f		-	17	-	
Total Gate Charge	Q_g	V_{DS}=-50V, I_{DS}=-15A, V_{GS}=-10V	-	27.7	-	nC
Gate to Source Gate Charge	Q_{gs}		-	7.3	-	
Gate to Drain “Miller” Charge	Q_{gd}		-	7.5	-	

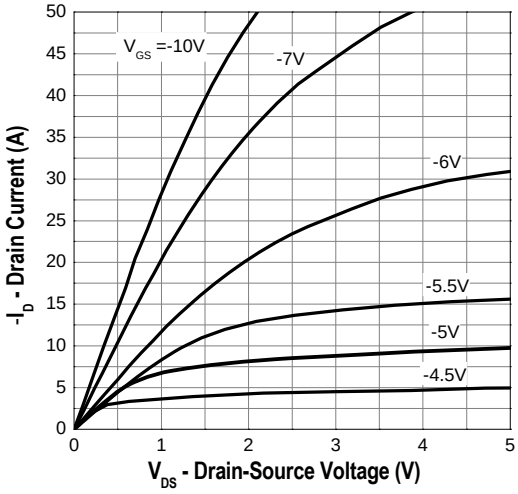
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	**Symbol**	**Conditions**	**Min.**	**Typ.**	**Max.**	**Unit**
Drain-Source Diode Forward Voltage	V_{SD}	V_{GS}=0V, I_{DS}=-15A	-	-	-1.3	V
Body Diode Reverse Recovery Time	t_{rr}	I_F=-15A, dI/dt=100A/μs	-	48	-	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	120	-	nC

Notes:

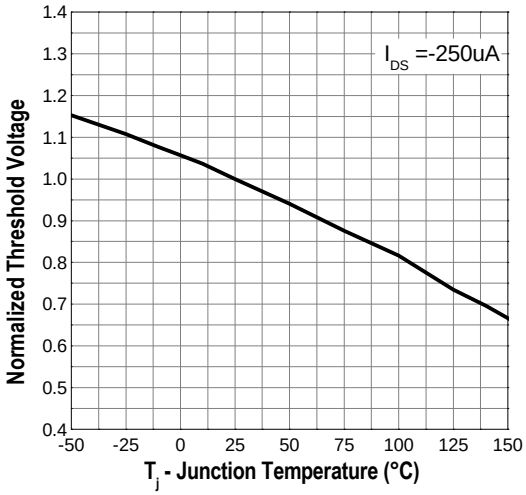
1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta JA}$ is determined by the user's board design. $R_{\theta JA}$ shown below for single device operation on FR-4 in still air.

Typical Operating Characteristics

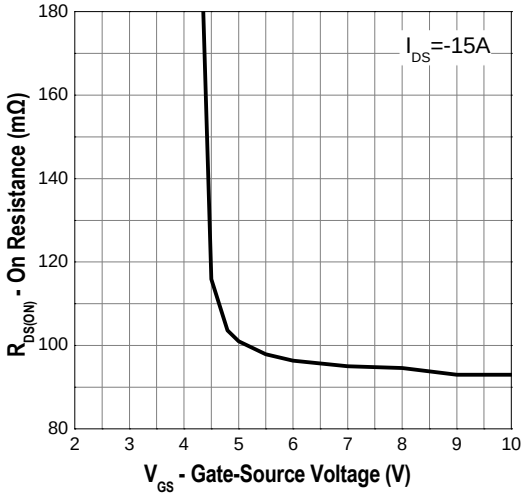
Output Characteristics



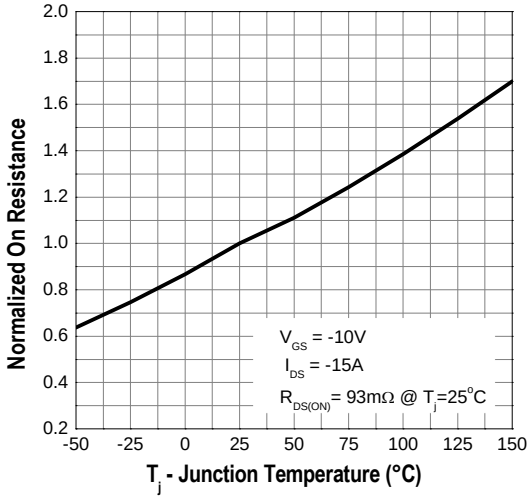
Gate Threshold Voltage



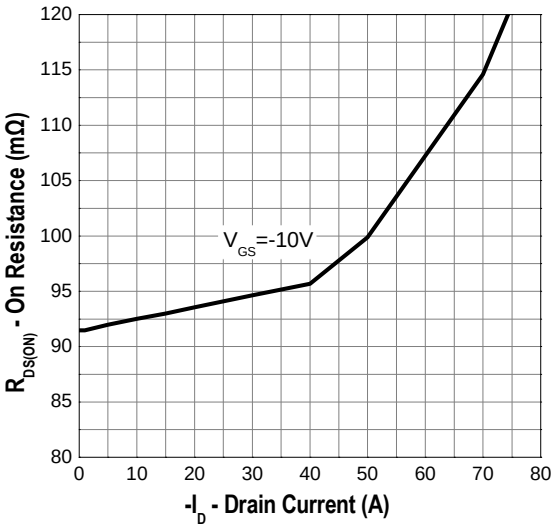
Gate-Source On Resistance



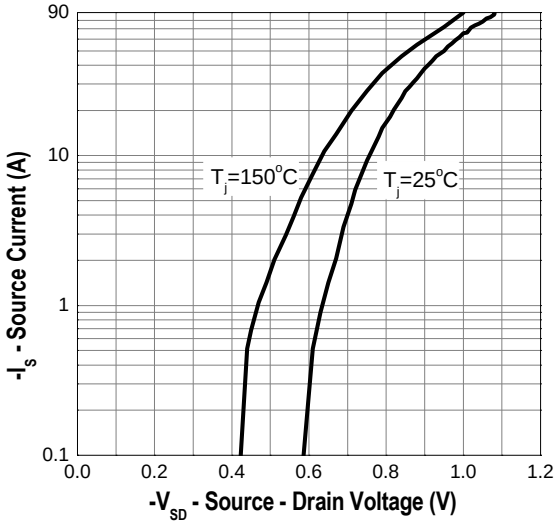
Drain-Source On Resistance



Drain-Source On Resistance

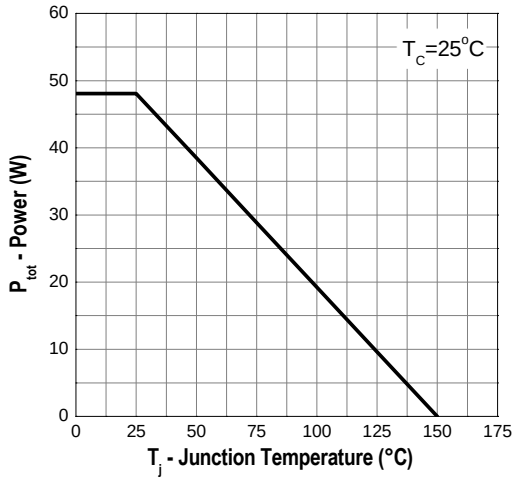


Source-Drain Diode Forward

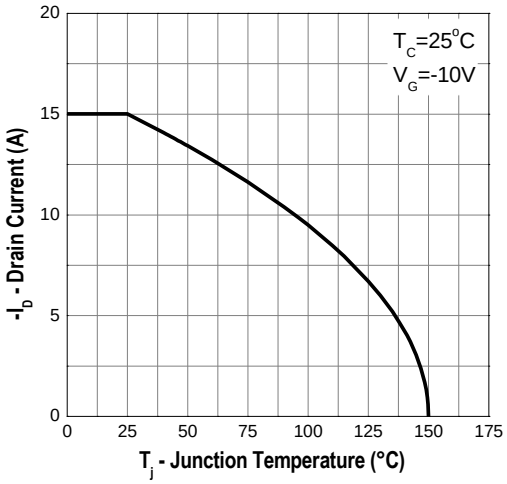


Typical Operating Characteristics (Cont.)

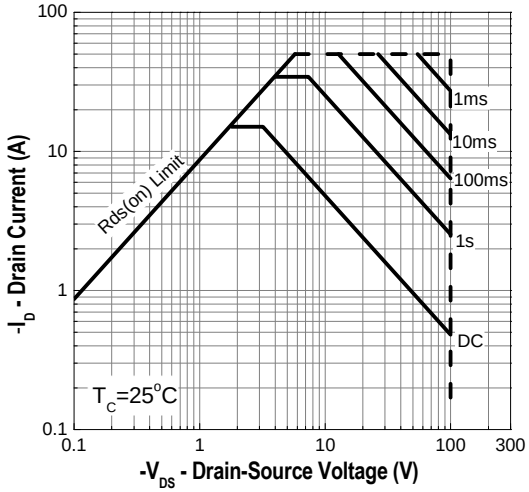
Power Dissipation



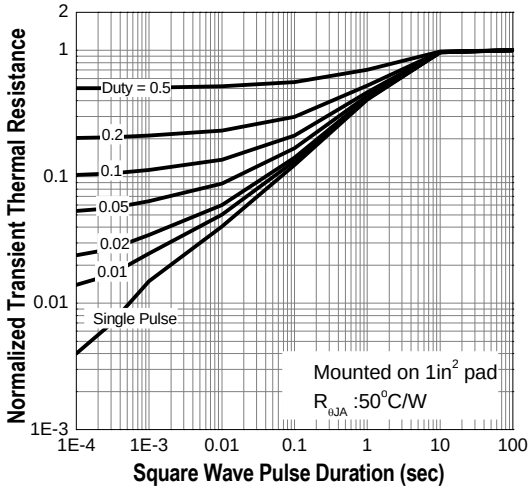
Drain Current



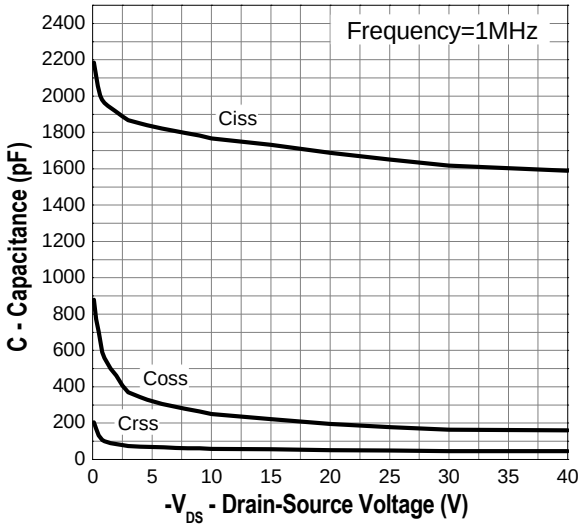
Safe Operation Area



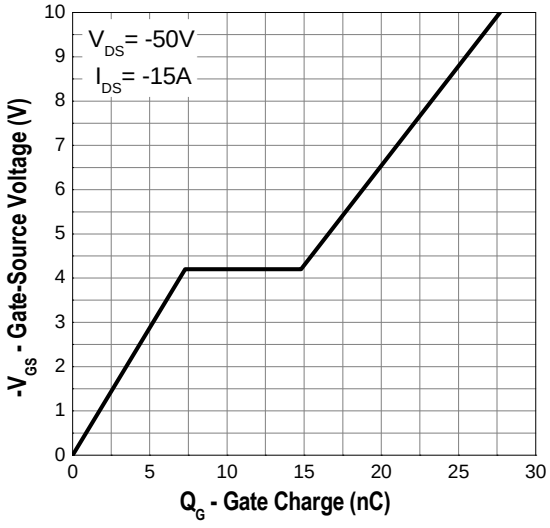
Transient Thermal Impedance



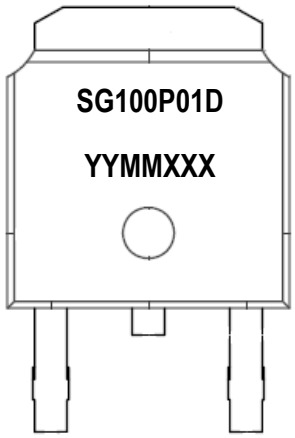
Capacitance



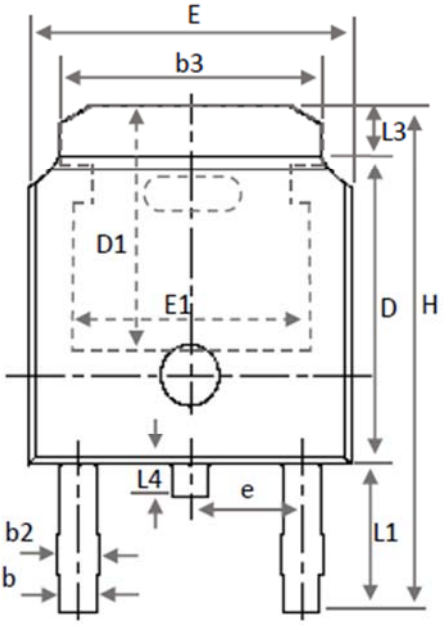
Gate Charge



Marking Information

TO-252 (D)	Marking Rule
Laser Marking  Diagram	Line 1：Device SG100P01D Line 2：Date Code YYMMXXX YY：Year Code MM：Month Code XXX：Serial Number

Package of Dimension

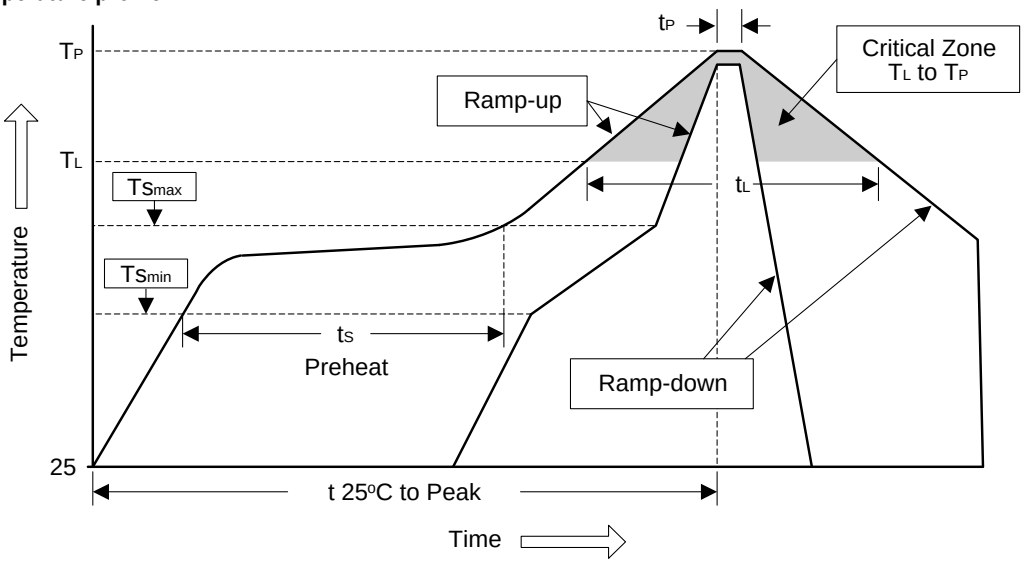


Symbol	Min	Nor	Max
E	6.35	6.54	6.731
L	1.40	1.59	1.78
L1		2.743 Ref.	
L2		0.508 BSC	
L3	0.89	1.08	1.27
L4	0.60	0.81	1.01
D	5.97	6.10	6.223
H	9.40	9.91	10.41
b	0.64	0.77	0.89
b2	0.76	0.95	1.14
b3	4.95	5.21	5.46
e		2.286 BSC	
A	2.18	2.29	2.39
A1	0.00	0.07	0.13
c2	0.46	0.68	0.89
D1	5.21	-	-
E1	4.32	-	-

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat - Temperature Min (Ts_{min}) - Temperature Max (Ts_{max}) - Time (min to max) (ts)	 100°C 150°C 60 to 120 sec	 150°C 200°C 60 to 180 sec
Tsmax to T_L - Ramp-up Rate	 <3°C/sec	 <3°C/sec
Time maintained above: - Temperature (T_L) - Time (t_L)	 183°C 60 to 150 sec	 217°C 60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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