

V_{DSS} , 30V R_{DS(ON)} , 3.5mΩ (max.) @ V_{GS}=10V R_{DS(ON)} , 4.9mΩ (max.) @ V_{GS}=4.5V I_D , 80A	PDFN 3.3x3.3-8L	

Description	Features
The SG30N06E uses advanced Trench technology and designs to provide excellent R _{DS(ON)} with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	<ul style="list-style-type: none"> • Low On-Resistance • Low Input Capacitance • Low Miller Charge • Low Input / Output Leakage • Pb-free lead plating; RoHS compliant
	Applications

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG30N06E	Halogen-Free	PDFN 3.3x3.3-8L	E	Tape & Reel	5,000

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current-Continuous	I _D	T _C =25°C	80	A
		T _C =100°C	50	A
Drain Current-Pulsed ^{Note 1}	I _{DM}	320	A	
Drain Current-Continuous	I _D	T _A =25°C	20	A
		T _A =70°C	13	A
Avalanche Current	I _{AS}	50	A	
Avalanche Energy, L=0.1mH	E _{AS}	125	mJ	
Maximum Power Dissipation	P _D	T _C =25°C	62.5	W
		T _C =100°C	25	W
		T _A =25°C	2	W
		T _A =70°C	0.8	W
Operating Junction Temperature Range	T _J T _{STG}	-55 to +150	°C	

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient	R _{θJA}	Steady State	-	-	62	°C/W
Maximum Junction-to-Case	R _{θJC}	Steady State	-	-	2	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250μA	1	-	2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _{DS} =30A	-	-	3.5	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _{DS} =15A	-	-	4.9	

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	2156	-	pF
Output Capacitance	C _{oss}		-	274	-	
Reverse Transfer Capacitance	C _{rss}		-	173	-	
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1	-	Ω

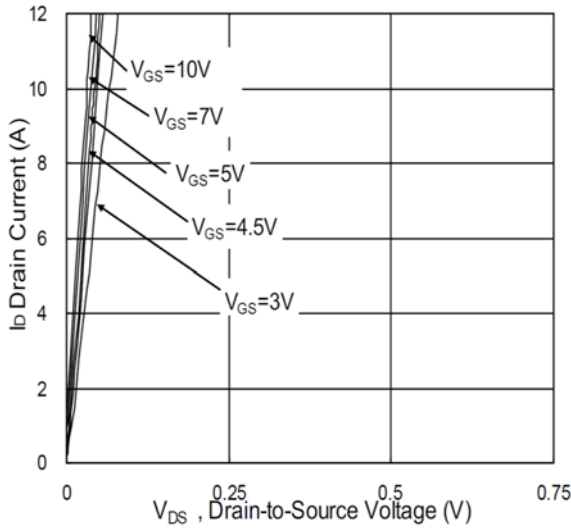
SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	V _{DD} =15V, I _D =30A, V _{GS} =10V, R _g =3.3Ω	-	12.3	-	ns
Rise Time	t _r		-	19.1	-	
Turn-Off Delay Time	T _{d(off)}		-	41.9	-	
Fall Time	t _f		-	12.9	-	
Total Gate Charge	Q _g	V _{DS} =15V, I _{DS} =30A, V _{GS} =4.5V	-	31	-	nC
Gate to Source Gate Charge	Q _{gs}		-	8.4	-	
Gate to Drain "Miller" Charge	Q _{gd}		-	11.4	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Body-Diode Continuous Current	I _S	V _G =V _D =0V, Force Current	-	-	80	A
Pulsed Source Current	I _{SM}	V _G =V _D =0V, Force Current	-	-	320	A
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =30A	-	0.8	1.2	V

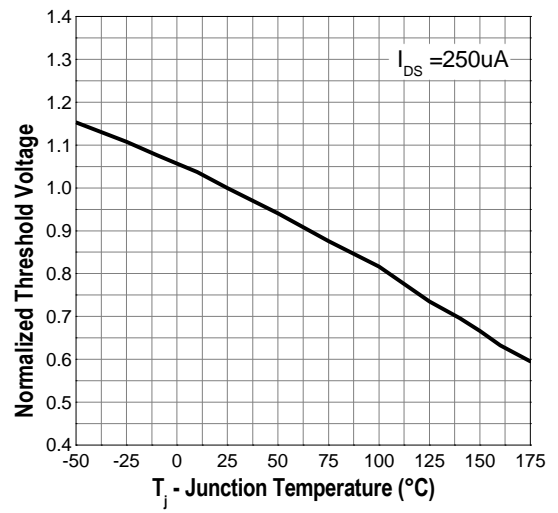
- Notes:**
- Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
 - R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design. R_{θJA} shown below for single device operation on FR-4 in still air.

Typical Operating Characteristics

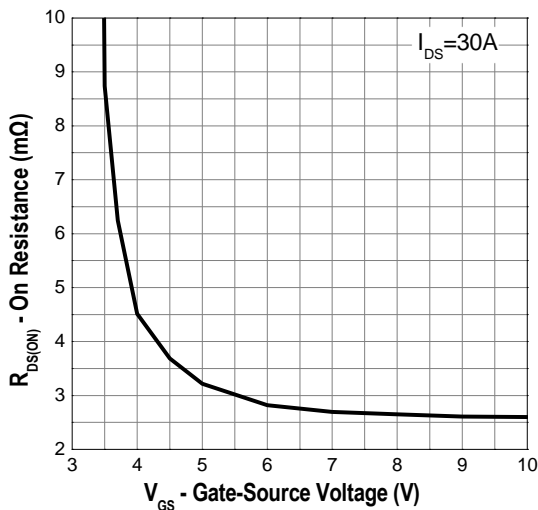
Output Characteristics



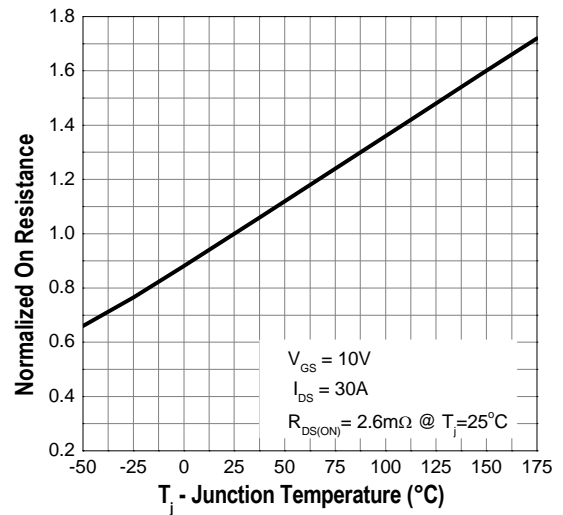
Gate Threshold Voltage



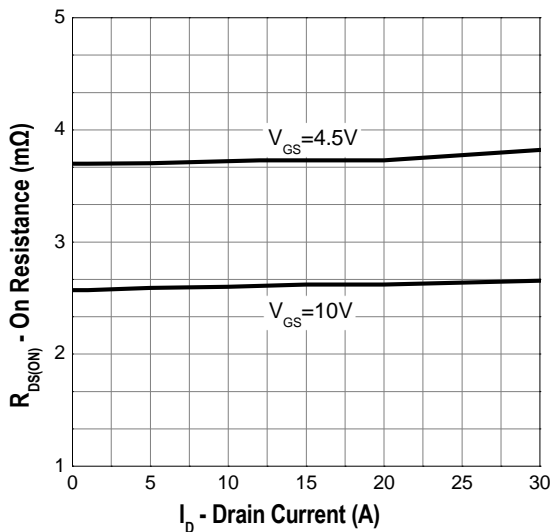
Gate-Source On Resistance



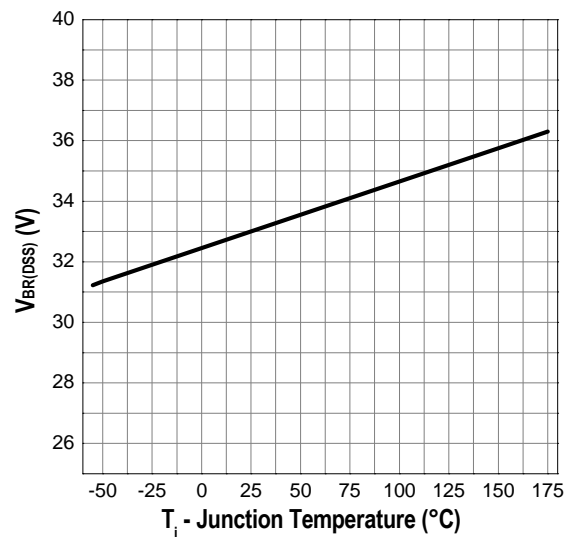
Drain-Source On Resistance



Drain-Source On Resistance

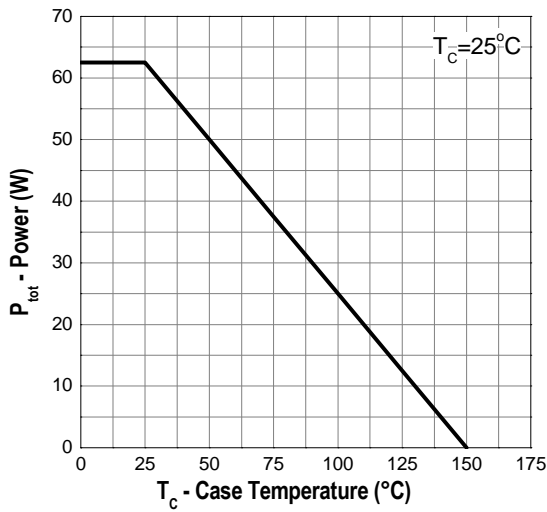


Drain-source Breakdown Voltage

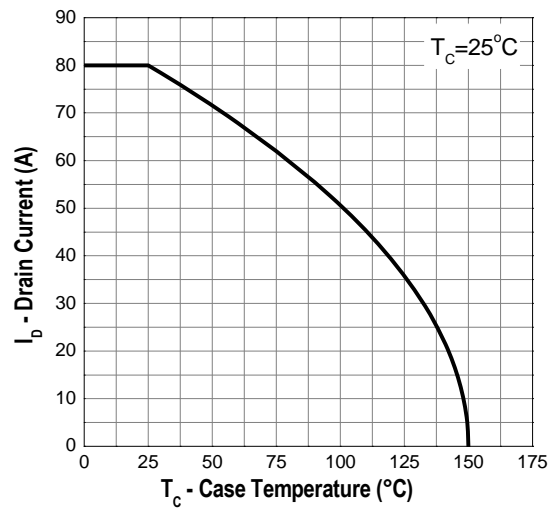


Typical Operating Characteristics (Cont.)

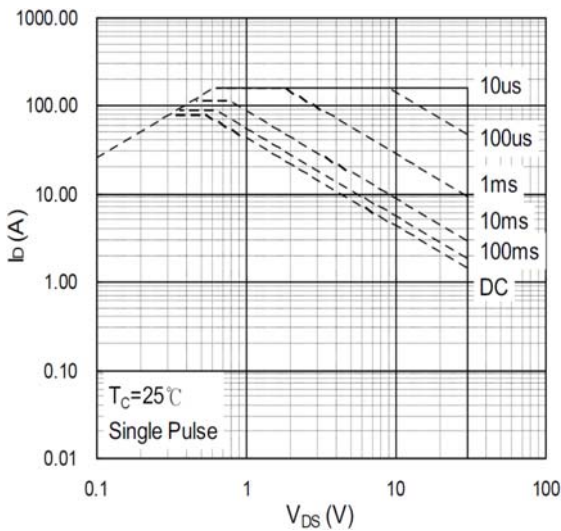
Power Dissipation



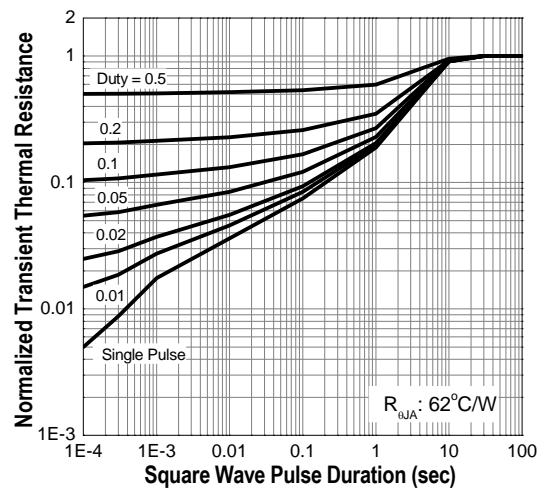
Drain Current



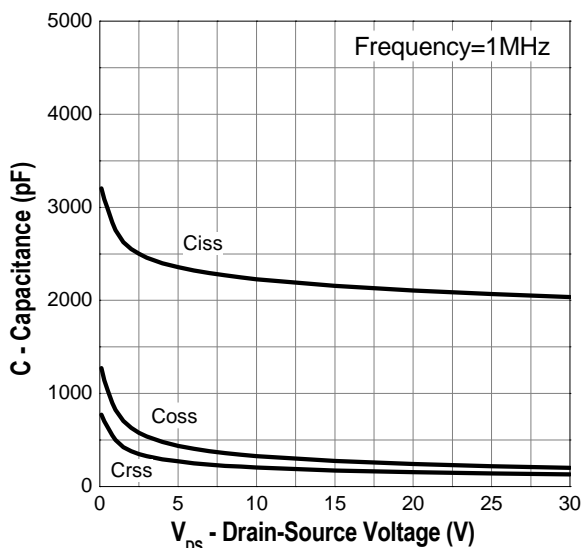
Safe Operation Area



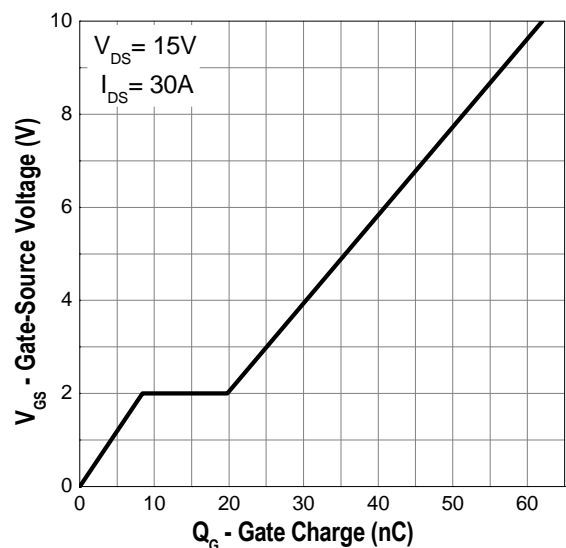
Transient Thermal Impedance



Capacitance



Gate Charge



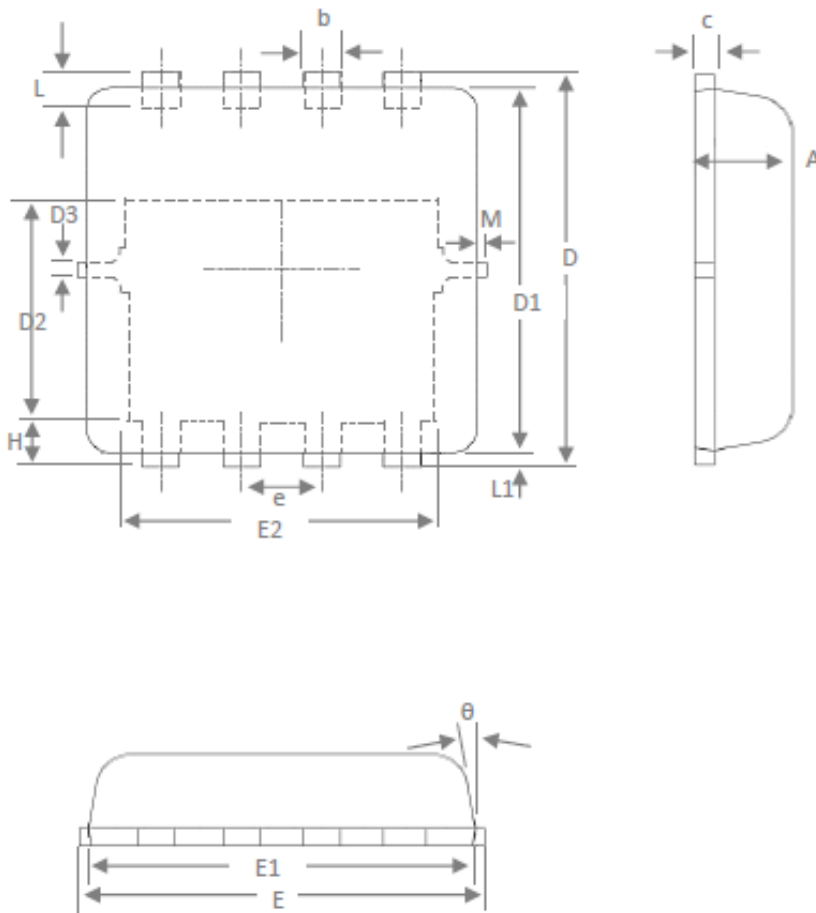
Marking Information

PDFN 3.3x3.3-8L (E)	Marking Rule
<p data-bbox="129 360 300 394">Laser Marking</p> <div data-bbox="339 450 571 689" style="text-align: center;"> <p data-bbox="419 517 517 551">30N06E</p> <p data-bbox="403 591 517 624">YMMXXX</p> </div> <p data-bbox="400 741 507 775">Diagram</p>	<p data-bbox="807 360 991 394"><u>Line 1</u> : Device</p> <p data-bbox="807 405 903 439">30N06E</p> <p data-bbox="807 450 1038 483"><u>Line 2</u> : Date Code</p> <p data-bbox="807 495 922 528">YMMXXX</p> <p data-bbox="807 584 983 618">Y : Year Code</p> <p data-bbox="807 629 1027 663">MM : Month Code</p> <p data-bbox="807 674 1062 707">XXX : Serial Number</p> <p data-bbox="807 763 1190 797">Year Code Description As Below</p>

Year Code Description

Year Code	Year	
0	2010	2020
1	2011	2021
2	2012	2022
3	2013	2023
4	2014	2024
5	2015	2025
6	2016	2026
7	2017	2027
8	2018	2028
9	2019	2029

Package of Dimension



Symbol	Min	Nor	Max
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	-	0.13	-
E	3.00	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
θ	-	10°	12°
M	-	-	0.15

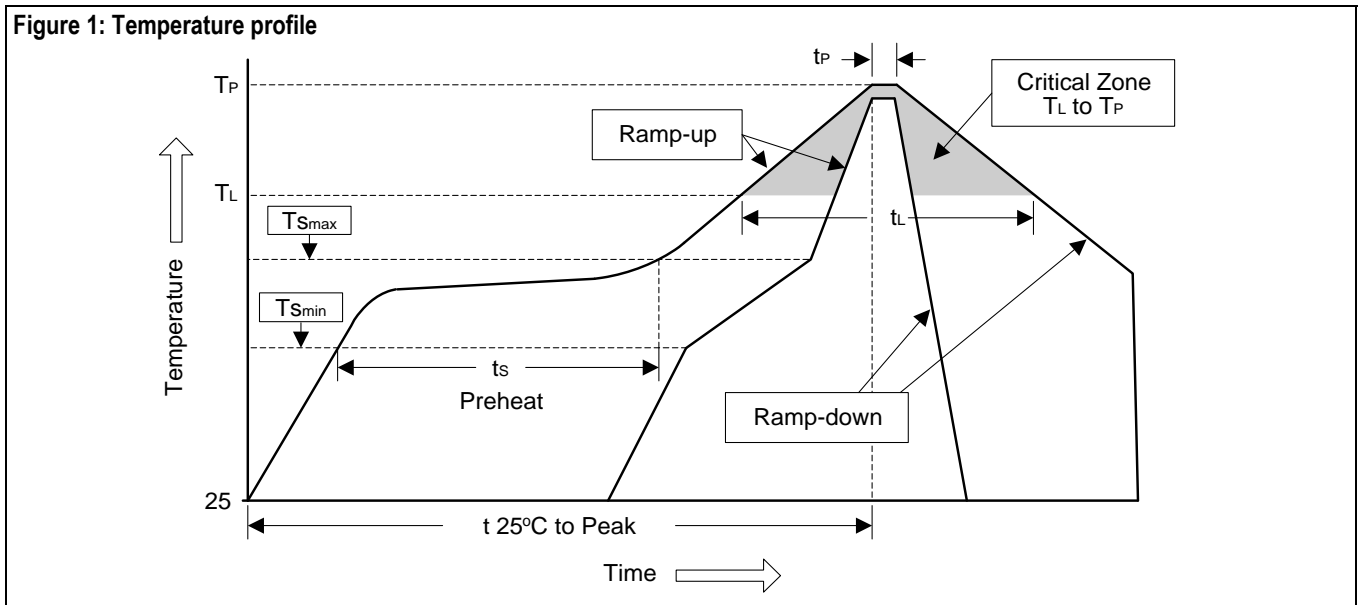
Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs.
Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body
Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash,
But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

Important Notice

© Silicongear Corporation

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Silicongear cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in an Silicongear product. No circuit patent licenses, copyrights, mask work rights, or other intellectual property rights are implied.

Silicongear Corporation, its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Silicongear"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Silicongear makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Silicongear disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Silicongear's knowledge of typical requirements that are often placed on Silicongear products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Silicongear's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Silicongear products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Silicongear product could result in personal injury or death. Customers using or selling Silicongear products not expressly indicated for use in such applications do so at their own risk and agree to fully indemnify and hold Silicongear and its distributors harmless from and against any and all claims, liabilities, expenses and damages arising or resulting in connection with such use or sale, including attorneys fees, even if such claim alleges that Silicongear or its distributor was negligent regarding the design or manufacture of the part. Please contact authorized Silicongear personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Silicongear. Product names and markings noted herein may be trademarks of their respective owners.

Silicongear and the Silicongear logo are trademarks of Silicongear Corporation. All other brand and product names appearing in this document are registered trademarks or trademarks of their respective holders.