

V_{DS} , 30V $R_{DS(ON)}$, 4.0m Ω (max.) @ $V_{GS}=8V$ $R_{DS(ON)}$, 4.4m Ω (max.) @ $V_{GS}=4.5V$ $R_{DS(ON)}$, 5.3m Ω (max.) @ $V_{GS}=2.5V$ I_D , 40A ^{Note 3}	PDFN 3.3x3.3-8L		

Description	Features
<p>The SG30N06LE uses advanced Trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.</p>	<ul style="list-style-type: none"> • Low On-Resistance • Low Input Capacitance • Low Miller Charge • Low Input / Output Leakage • Pb-free lead plating; RoHS compliant
	Applications
	<ul style="list-style-type: none"> • Lithium-Ion Secondary Batteries • Notebook PCs • Load Switch • DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG30N06LE	Halogen-Free	PDFN 3.3x3.3-8L	E	Tape & Reel	5,000

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	+10/-8	V
Drain Current-Continuous ^{Note 3}	I_D	$T_C=25^\circ\text{C}$	40
		$T_C=70^\circ\text{C}$	40
Drain Current-Pulsed ^{Note 1}	I_{DM}	100	A
Drain Current-Continuous	I_D	$T_A=25^\circ\text{C}$	25
		$T_A=70^\circ\text{C}$	20
Avalanche Current, $L=0.1\text{mH}$	I_{AS}	38	A
Avalanche Energy, $L=0.1\text{mH}$	E_{AS}	72	mJ
Maximum Power Dissipation	P_D	$T_C=25^\circ\text{C}$	52
		$T_C=70^\circ\text{C}$	33
		$T_A=25^\circ\text{C}$	3.8
		$T_A=70^\circ\text{C}$	2.4
Operating Junction Temperature Range	T_J T_{STG}	-55 to +150	$^\circ\text{C}$

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient	$R_{\theta JA}$	$t \leq 10\text{s}$	-	-	33	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Case	$R_{\theta JC}$	Steady State	-	-	2.4	$^\circ\text{C}/\text{W}$

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =+10/-8V, V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250μA	0.6	-	1.1	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =8V, I _{DS} =20A	-	-	4.0	mΩ
Drain-Source On-State Resistance		V _{GS} =4.5V, I _{DS} =20A	-	-	4.4	mΩ
Drain-Source On-State Resistance		V _{GS} =2.5V, I _{DS} =20A	-	-	5.3	mΩ
Forward Transconductance ^{Note 1}	g _{fs}	V _{DS} =15V, I _D =20A	-	82	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1MHz	-	1666	2065	pF
Output Capacitance	C _{oss}		-	342	434	
Reverse Transfer Capacitance	C _{rss}		-	142	174	
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	3	4	Ω

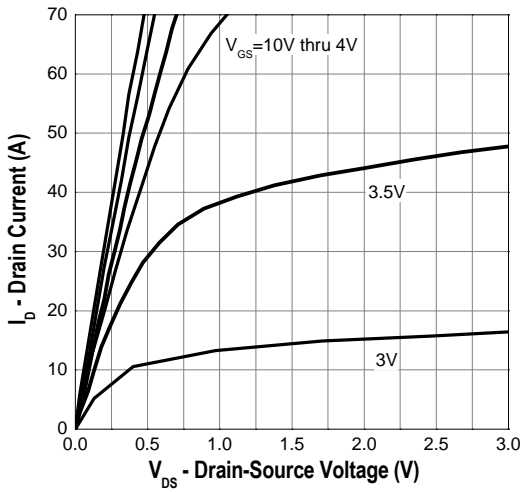
SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	V _{DD} =15V, I _D =10A, V _{GEN} =4.5V, R _{GEN} =1Ω, R _L =1.5Ω	-	23	-	ns
Rise Time	t _r		-	20	-	
Turn-Off Delay Time	T _{d(off)}		-	24	-	
Fall Time	t _f		-	16	-	
Total Gate Charge at 10V	Q _g	V _{DS} =15V, V _{GS} =4.5V, I _D =19A	-	11	16.5	nC
Gate to Source Gate Charge	Q _{gs}		-	5	-	
Gate to Drain "Miller" Charge	Q _{gd}		-	4	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Body-Diode Continuous Current	I _S	-	-	-	40	A
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =1A	-	0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}	I _F =10A, dI/dt=100A/μs, T _J =25°C	-	24	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	16	-	nC

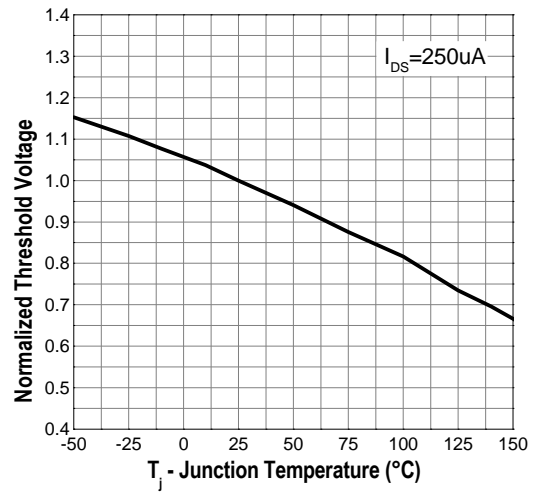
- Notes:**
1. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
 2. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design. R_{θJA} shown below for single device operation on FR-4 in still air.
 3. The maximum current rating is limited by package.

Typical Operating Characteristics

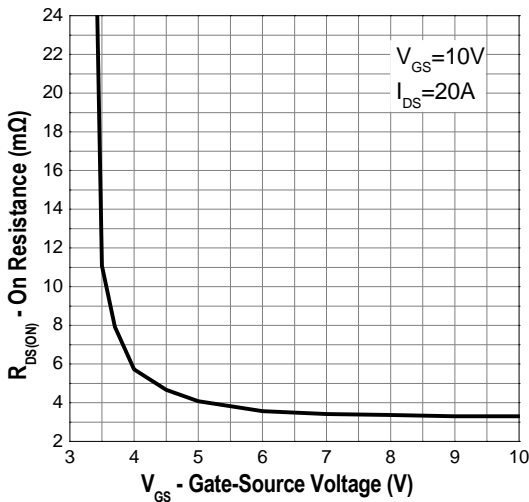
Output Characteristics



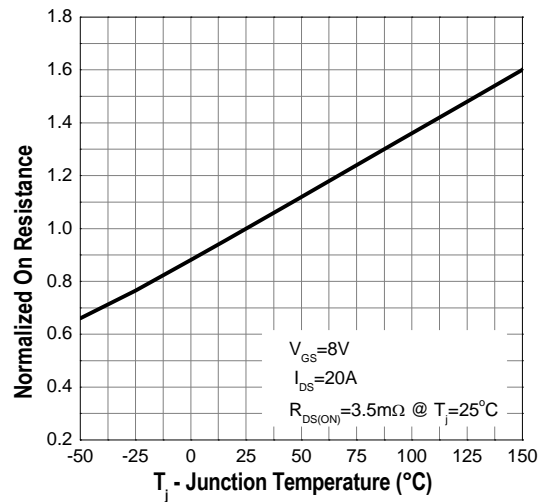
Gate Threshold Voltage



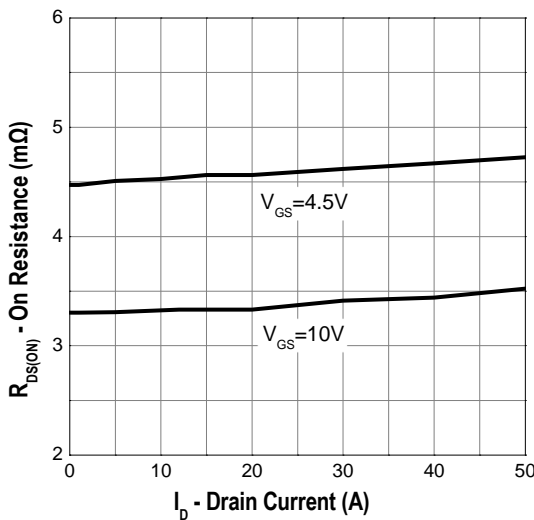
Gate-Source On Resistance



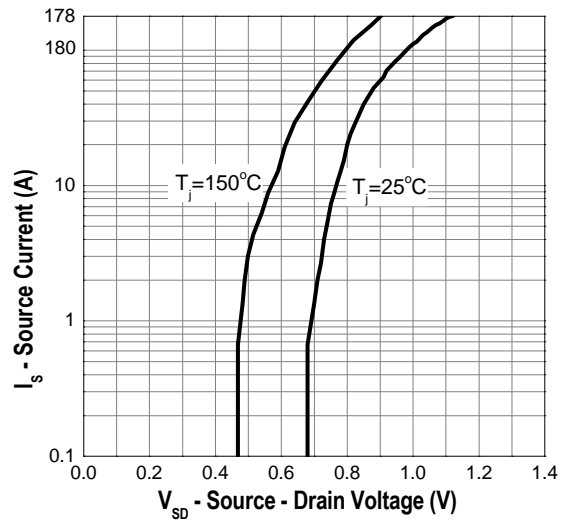
Drain-Source On Resistance



Drain-Source On Resistance

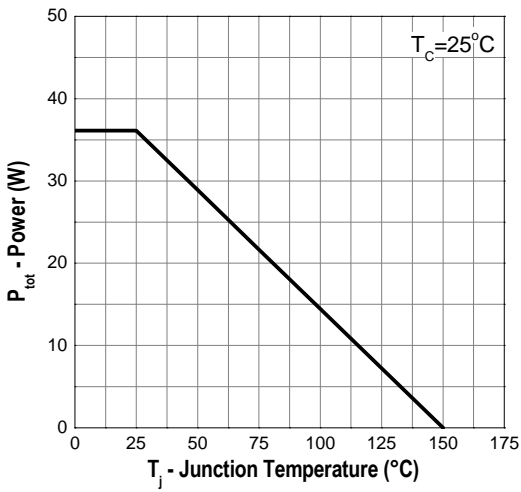


Source-Drain Diode Forward

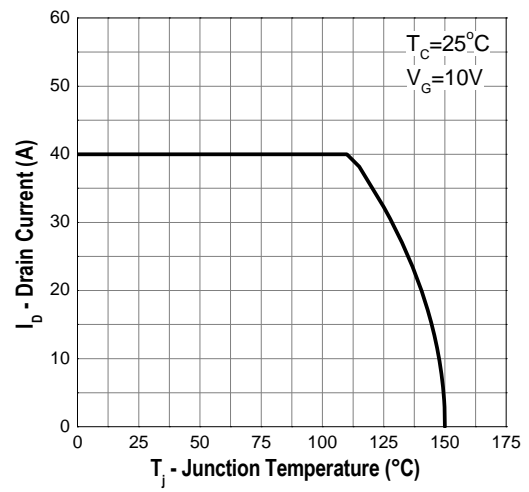


Typical Operating Characteristics (Cont.)

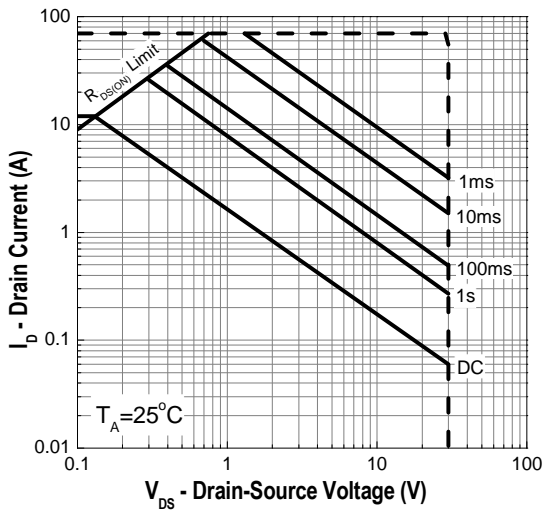
Power Dissipation



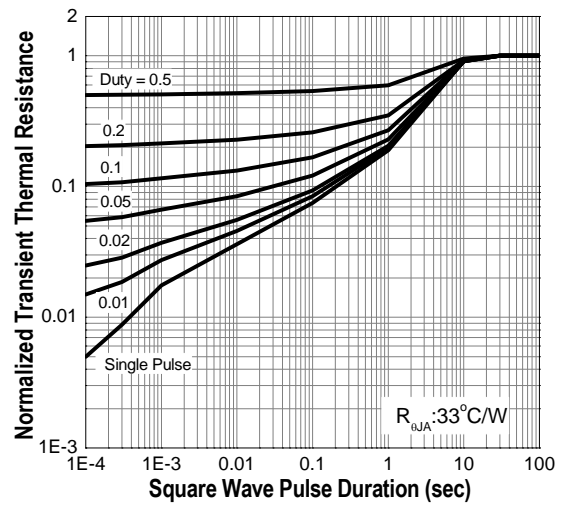
Drain Current



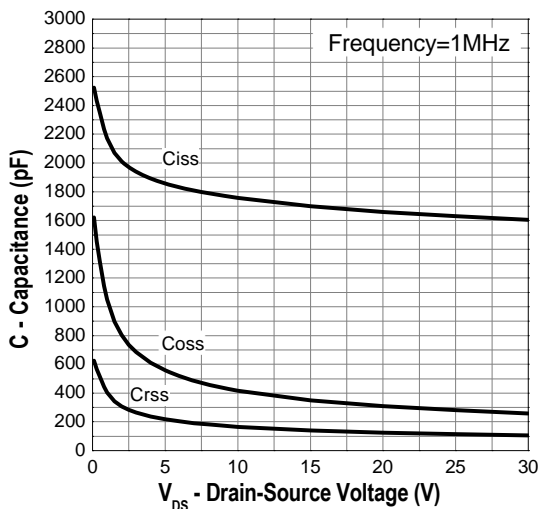
Safe Operation Area



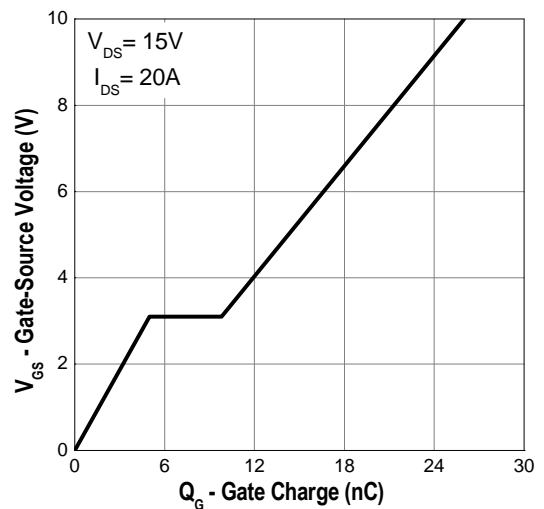
Transient Thermal Impedance




Capacitance



Gate Charge



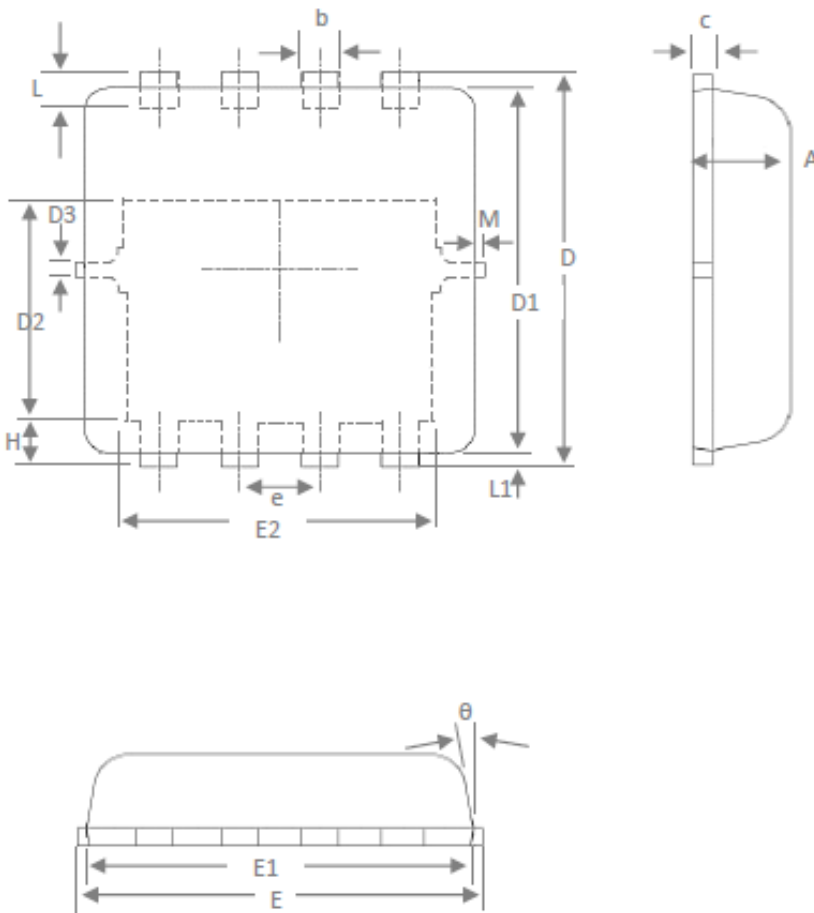
Marking Information

PDFN 3.3x3.3-8L (E)	Marking Rule
<p>Laser Marking</p>  <p>Diagram</p>	<p><u>Line 1</u> : Device Name 30N06L</p> <p><u>Line 2</u> : Date Code YMMXXX</p> <p>Y : Year Code MM : Month Code XXX : Serial Number</p> <p>Year Code Description As Below</p>

Year Code Description

Year Code	Year	
0	2010	2020
1	2011	2021
2	2012	2022
3	2013	2023
4	2014	2024
5	2015	2025
6	2016	2026
7	2017	2027
8	2018	2028
9	2019	2029

Package of Dimension



Symbol	Min	Nor	Max
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.78	1.88	1.98
D3	-	0.13	-
E	3.00	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
θ	-	10°	12°
M	-	-	0.15

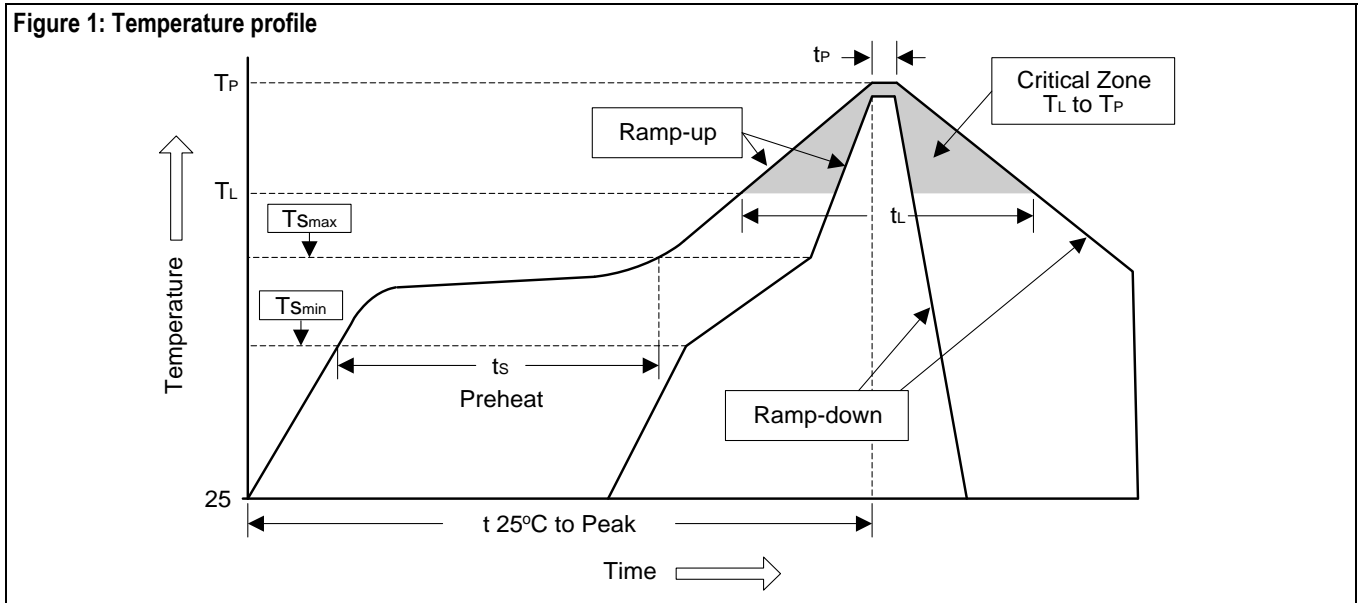
Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs.
Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body
Exclusive Of Mold Flash, Tie Bar Burrs, Gate Burrs And Interlead Flash,
But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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