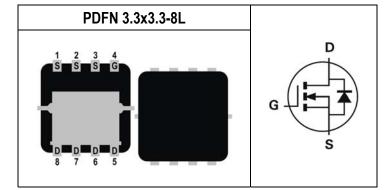


SG40N04E

40V N-Channel Power MOSFET

Parameter	Value	Unit
V _{DSS}	40	V
R _{DS(ON)} max. V _{GS} =10V	11	mΩ
$R_{DS(ON)}$ max. V_{GS} =4.5V	16	mΩ
ld	38	А



Features	Application
 Low On-Resistance Low Input Capacitance Low Miller Charge Low Input / Output Leakage Pb-free lead plating; RoHS compliant 	 Motor / Body Load Control Automotive Systems Load Switch DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG40N04E	Halogen-Free	PDFN 3.3x3.3-8L	E	Tape & Reel	5,000

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parame	ter	Symbol	Value	Unit
Drain-Source Voltage		VDS	40	V
Gate-Source Voltage		V _{GS}	±20	V
	Tc=25°C		38	Α
Drain Current-Continuous	Tc=100°C		24	Α
	T _A =25°C		10	Α
	T _A =70°C		6	Α
Drain Current-Pulsed Note 1		lом	100	Α
Avalanche Current		I _{AS}	20	Α
Avalanche Energy, L=0.1mH		Eas	20	mJ
	Tc=25°C		23.1	W
Maximum Power Dissipation	Tc=100°C	P _D	9.3	W
Maximum Fower Dissipation	Tc=25°C	FD FD	1.7	W
	T _C =70°C		0.7	W
Storage Temperature Range		Tstg	-55 to +150	°C
Operating Junction Temperature Range		TJ	-55 to +150	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Junction-to-Ambient	Reja	Steady State	-	-	75	°C/W
Maximum Junction-to-Case	Rejc	Steady State	-	-	5.4	°C/W



Electrical Characteristics (TJ=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	Vgs=0V, Ids=250µA	40	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =40V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage	lgss	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250µA	1	-	2.5	V
Drain-Source On-State Resistance	D	V _{GS} =10V, I _{DS} =9A	-	13	14.5	mΩ
Drain-Source On-State Resistance	- R _{DS(ON)}	V _{GS} =4.5V, I _{DS} =5A	-	17.5	20	mΩ
Forward Transconductance Note 1	g fs	V _{DS} =5V, I _D =20A	-	35	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	C _{iss}		-	1248	-	
Output Capacitance	Coss	V _{DS} =20V, V _{GS} =0V, f=1MHz	-	114	-	pF
Reverse Transfer Capacitance	Crss		-	84	-	
Gate Resistance	Rg	V_{GS} =0V, V_{DS} =0V, f=1MHz	-	2	-	Ω

SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}		-	8.17	-	
Rise Time	tr	V _{DD} =20V, I _D =20A, V _{GS} =10V,	-	3.23	-]
Turn-Off Delay Time	T _{d(off)}	Rg=3.3Ω	-	23.75	-	ns
Fall Time	tr		-	2.09	-]
Total Gate Charge	Qg		-	10.16	-	
Gate to Source Gate Charge	Q _{gs}	V _{DS} =20V, I _{DS} =20A, V _{GS} =4.5V	-	3.13	-	nC
Gate to Drain "Miller" Charge	Q _{gd}		-	3.99	-	<u>] </u>

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Body-Diode Continuous Current	ls	V _G =V _D =0V, Force Current	-	-	38	Α
Pulsed Source Current	I _{SM}	V _G =V _D =0V, Force Current	-	-	100	Α
Drain-Source Diode Forward Voltage	Vsd	V _{GS} =0V, I _S =20A	-	-	1.2	V

Notes:

1. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

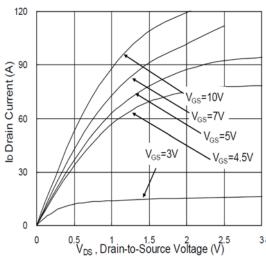
 R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BJA} is determined by the user's board design. R_{BJA} shown below for single device operation on FR-4 in still air.



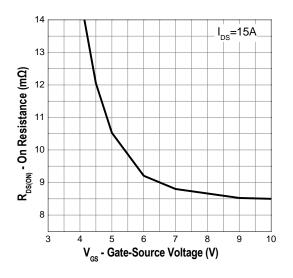


Typical Operating Characteristics

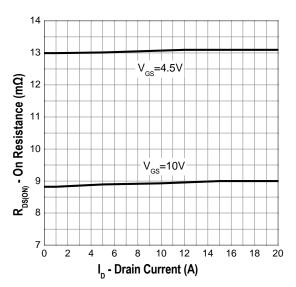
Output Characteristics



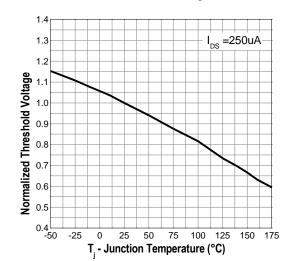
Gate-Source On Resistance



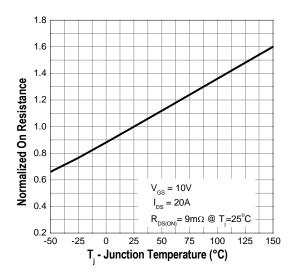
Drain-Source On Resistance



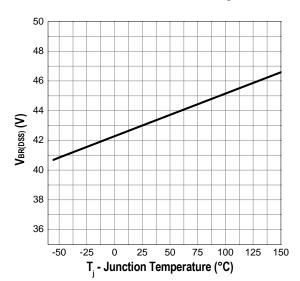
Gate Threshold Voltage



Drain-Source On Resistance



Drain-source Breakdown Voltage

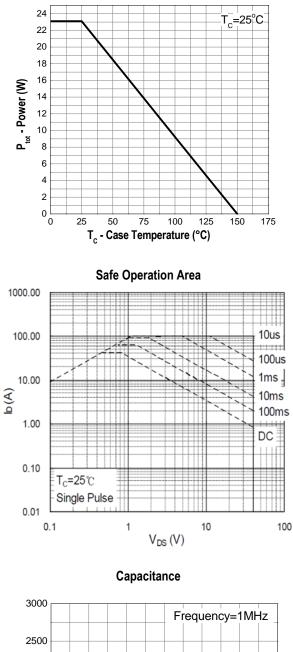


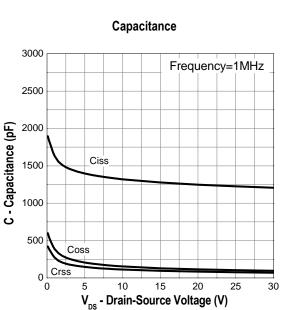


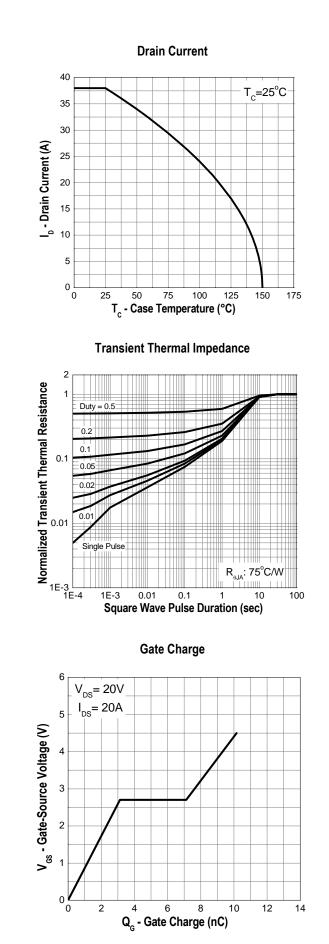
Typical Operating Characteristics (Cont.)

Power Dissipation

SG40N04E 40V N-Channel Power MOSFET











Marking Information

PDFN 3.3x3.3-8L (E)	Marking Rule
PDFN 3.3x3.3-8L (E) Laser Marking 40N04E YMMXXX	Marking Rule Line 1 : Device 40N04E Line 2 : Date Code YMMXXX Y : Year Code MM : Month Code
- <u></u>	XXX : Serial Number Year Code Description As Below

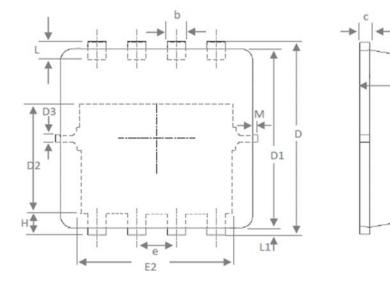
Year Code Description

Year Code	Year				
0	2010	2020			
1	2011	2021			
2	2012	2022			
3	2013	2023			
4	2014	2024			
5	2015	2025			
6	2016	2026			
7	2017	2027			
8	2018	2028			
9	2019	2029			

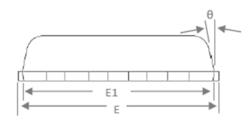


Package of Dimension





Symbol	Min	Nor	Max
Α	0.70	0.75	0.80
b	0.25	0.30	0.35
с	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.00	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
е	0.65BSC		
Н	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	_
θ	-	10°	12°
М	-	-	0.15



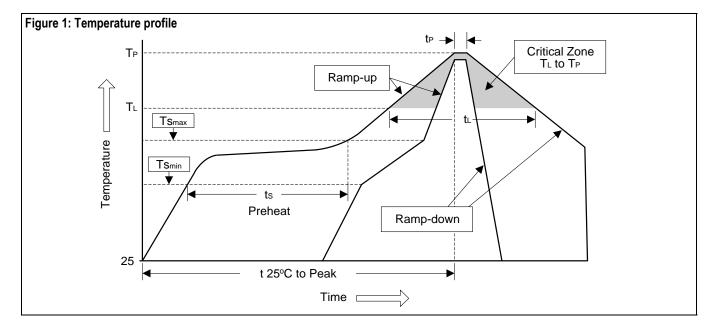
A





Soldering Methods for Silicongear's Products

- 1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
- 2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T⊾ to T _P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (Ts _{min})	100°C	150°C
- Temperature Max (Ts _{max})	150°C	200°C
- Time (min to max) (ts)	60 to 120 sec	60 to 180 sec
Tsmax to T∟		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T _L)	183°C	217°C
- Time (t _L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T _P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak	10 to 30 sec	20 to 40 sec
Temperature (t _P)	10 10 50 560	2010 40 360
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec





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