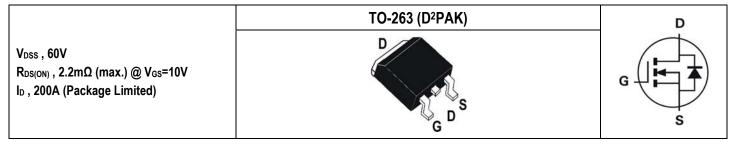


SG60N04G

60V N-Channel Power MOSFET



Description	Features
The SG60N04G uses advanced Trench technology and designs to provide excellent $R_{\text{DS}(\text{ON})}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	 Low On-Resistance Low Input Capacitance Low Miller Charge Pb-free lead plating; RoHS compliant
	Applications
	 Motor / Body Load Control Load Switch Solenoid and Motor Control DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG60N04G	Halogen-Free	TO-263 (D ² PAK)	G	Tape & Reel	800

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Parame	ter	Symbol	Value	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	V
Davia Quanta Quatizzana	Tc=25°C		200	Α
Drain Current-Continuous	T _c =100°C	lo lo	126	Α
Drain Current-Pulsed Note 1		Ідм	680	Α
Drain Current Continuous	T _A =25°C	1	18	Α
Drain Current-Continuous	T _A =100°C	lo lo	11.4	Α
Avalanche Current, L=0.5mH,VD=30V, VG	=20V, V _{DS} =60V	las	60	Α
Avalanche Energy, L=0.5mH,VD=30V, VG	=20V, V _{DS} =60V	Eas	900	mJ
	T _C =25°C		250	W
Maximum Dawar Dissinction	Tc=100°C		100	W
Maximum Power Dissipation	T _A =25°C		2	W
	T _A =100°C		0.8	W
Storage Temperature Range		Tstg	-55 to +175	°C
Operating Junction Temperature Range		TJ	-55 to +175	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Junction-to-Ambient	Reja	Steady State	-	-	62	°C/W
Maximum Junction-to-Case	R _{ejc}	Steady State	-	-	0.5	°C/W
Case-to-Sink, Flat Greased Surface	Recs	Steady State	-	-	0.3	°C/W



Electrical Characteristics (T_=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250µA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
Breakdown Voltage Temp. Coefficient	$\Delta V_{(BR)DSS}/\Delta T_J$	Reference to 25°C, I _D =5mA	-	0.06	-	V/°C
Gate-Body Leakage	Igss	$V_{GS}=\pm 20V$, $V_{DS}=0V$	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250µA	2	3	4	V
Drain-Source On-State Resistance	Rds(on)	V _{GS} =10V, I _{DS} =30A	-	-	2.2	mΩ
Internal Gate Resistance	RG	-	-	0.9	-	Ω

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Capacitance	Ciss		-	8499	-	
Output Capacitance	Coss	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	818	-	pF
Reverse Transfer Capacitance	Crss		-	293	-	
Forward Transconductance	gfs	V _{DS} =50V, I _D =30A	250	-	-	S

SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}		-	24	-	
Rise Time	tr	V _{DD} =30V, R _L =30Ω, I _D =30A, V _{GEN} =10V ,	-	41	-	
Turn-Off Delay Time	T _{d(off)}	$R_{\rm G}=3.3\Omega$	-	92	-	ns
Fall Time	t _f		-	48	-	
Total Gate Charge at 10V	Qg	1/22 = 201/1/22 = 101/12	-	190	-	
Gate to Source Gate Charge	Qgs	V _{DS} =30V, V _{GS} =10V, I _{DS} =30A	-	42	-	nC
Gate to Drain "Miller" Charge	Q_gd	105-007	-	45	-	

Parameter	Symbol	Conditions	Min.	Tun	Max.	Unit
Faiaillelei	Symbol	Conditions	IVIIII.	Тур.	IVIAX.	Unit
Drain-Source Diode Forward Voltage	Vsd	V _{GS} =0V, I _{DS} =30A	-	-	1.3	V
Body Diode Reverse Recovery Time	trr		-	120	-	ns
Body Diode Reverse Recovery Charge	Qrr	l⊧=90A, dl/dt=100A/µs	-	160	-	nC
Continuous Source Current	ls	-	-	-	200	А
Pulsed Source Current	Ism	-	-	-	680	Α

Notes:

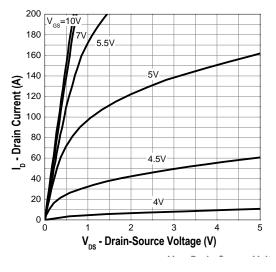
1. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

 R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJC} is guaranteed by design while R_{BJA} is determined by the user's board design. R_{BJA} shown below for single device operation on FR-4 in still air.

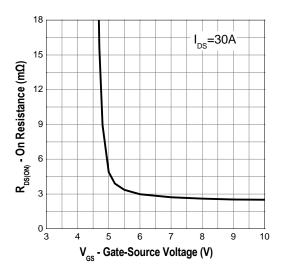


Typical Operating Characteristics

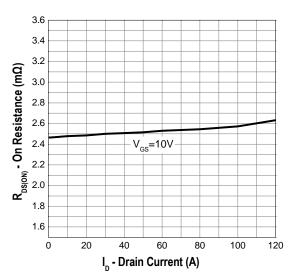
Output Characteristics



Gate-Source On Resistance



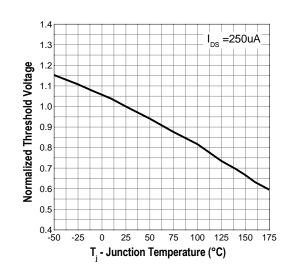
Drain-Source On Resistance



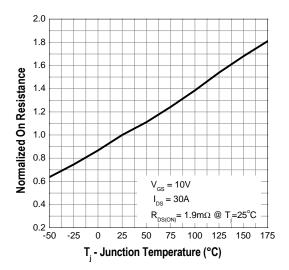
Gate Threshold Voltage

SG60N04G

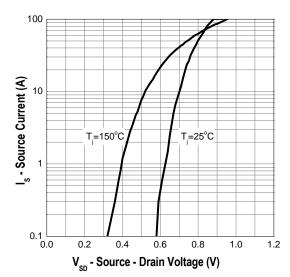
60V N-Channel Power MOSFET



Drain-Source On Resistance



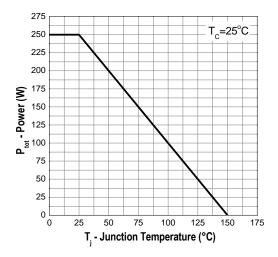




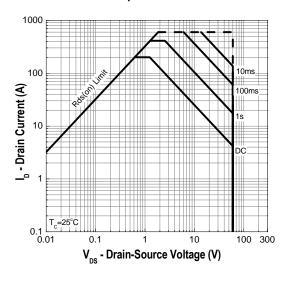


Typical Operating Characteristics (Cont.)

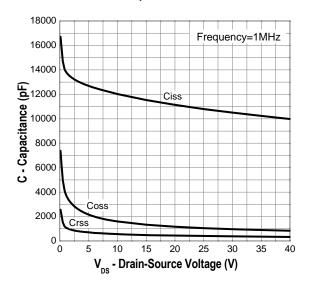
Power Dissipation

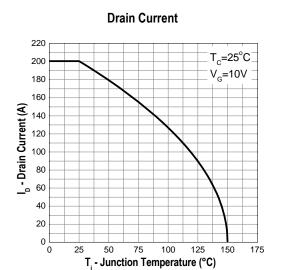


Safe Operation Area



Capacitance

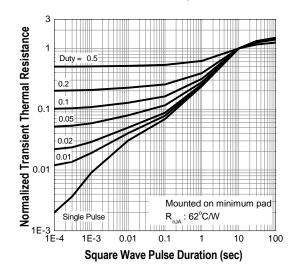




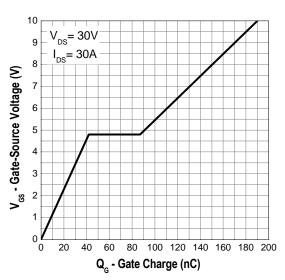
SG60N04G

60V N-Channel Power MOSFET

Transient Thermal Impedance

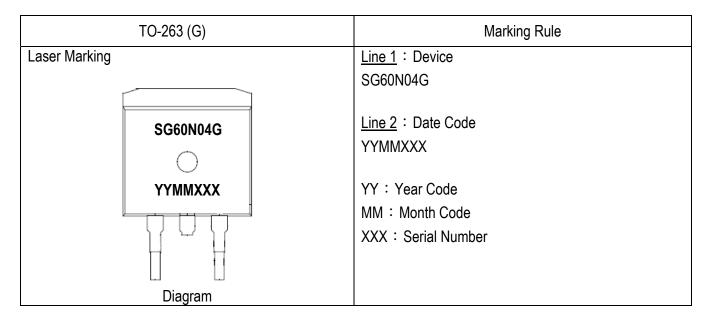








Marking Information

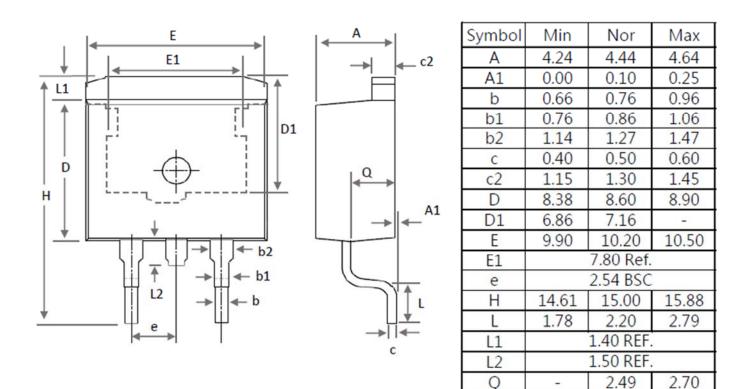




Package of Dimension

SG60N04G 60V N-Channel Power MOSFET

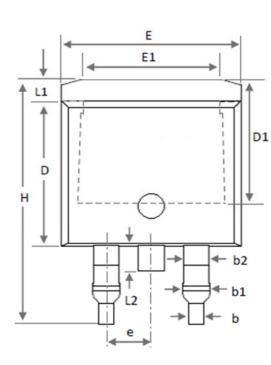
TO-263S

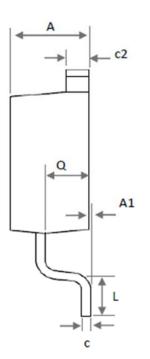




Package of Dimension

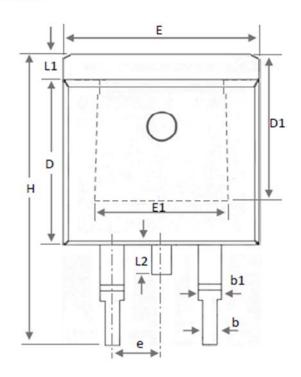
G-TYPE





C 1 1			
Symbol	Min	Nor	Max
A	4.24	4.51	4.77
A1	0.00	0.13	0.25
b	0.70	0.83	0.96
b1	1.17	1.46	1.75
b2	1.20	1.45	1.70
С	0.30	0.45	0.60
c2	1.15	1.29	1.42
D	8.50	8.76	9.02
D1	6.60	7.13	7.65
E	9.86	10.11	10.36
E1	6.89	7.39	7.89
е		2.54 BSC	
Н	14.61	15.25	15.88
L	1.78	2.29	2.79
L1	1.07	1.27	1.47
L2	1.40	1.55	1.70
Q	2.30	2.60	2.89

H-TYPE



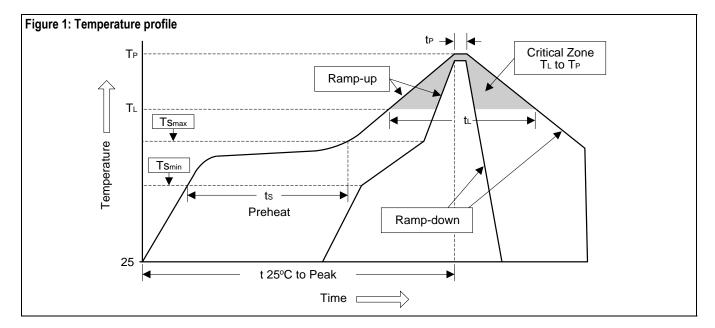
SG60N04G 60V N-Channel Power MOSFET

TO-263



Soldering Methods for Silicongear's Products

- 1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
- 2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate $(T_L \text{ to } T_P)$	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (Ts _{min})	100°C	150°C
- Temperature Max (Ts _{max})	150°C	200°C
- Time (min to max) (ts)	60 to 120 sec	60 to 180 sec
Tsmax to T∟		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T∟)	183°C	217°C
- Time (t _L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T _P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak	10 to 30 sec	20 to 40 sec
Temperature (t _P)	10 10 00 300	2010 40 300
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec



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