

V_{DSS} , -60V R_{DS(ON)} , 78mΩ (max.) @ V_{GS}=-10V R_{DS(ON)} , 116mΩ (max.) @ V_{GS}=-4.5V I_D , -14A	TO-252	

Description	Features
The SG60P68D uses advanced Trench technology and designs to provide excellent R _{DS(ON)} with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.	<ul style="list-style-type: none"> • Low On-Resistance • Low Input Capacitance • Low Miller Charge • Pb-free lead plating; RoHS compliant
	Applications <ul style="list-style-type: none"> • Motor / Body Load Control • Automotive Systems • Load Switch • DC-DC converters and Off-line UPS

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SG60P68D	Halogen-Free	TO-252	D	Tape & Reel	2,500

Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	T _C =25°C	-14
		T _C =100°C	-8.8
Drain Current-Pulsed ^{Note 1}	I _{DM}	-28	A
Maximum Power Dissipation	P _D	T _C =25°C	31.3
		T _C =100°C	12.5
Operating Junction Temperature Range	T _J T _{STG}	-55 to +175	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Maximum Junction-to-Ambient ^{Note 1}	R _{θJA}	Steady State	-	-	62	°C/W
Maximum Junction-to-Case	R _{θJC}	Steady State	-	-	4	°C/W

Electrical Characteristics (T_J=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

Electrical Characteristics (T_A=25°C unless otherwise noted)

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =-250μA	-1	-1.7	-2.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _{DS} =-10A	-	-	78	mΩ
		V _{GS} =-4.5V, I _{DS} =-5A	-	-	116	mΩ
Forward Transconductance	g _{fs}	V _{DS} =-5V, I _{DS} =-4A	-	8.5	-	S

DYNAMIC CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{ISS}	V _{DS} =-15V, V _{GS} =0V, f=1MHz	-	1070	-	pF
Output Capacitance	C _{OSS}		-	71	-	
Reverse Transfer Capacitance	C _{rss}		-	48	-	

SWITCHING CHARACTERISTICS

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	V _{DS} =-15V, I _{DS} =-1A, V _{GS} =-10V, R _{GEN} =3.3Ω	-	8	-	ns
Rise Time	t _r		-	17.8	-	
Turn-Off Delay Time	T _{d(off)}		-	42.9	-	
Fall Time	t _f		-	8.7	-	
Total Gate Charge at 10V	Q _g	V _{DS} =-12V, I _{DS} =-6A, V _{GS} =-4.5V	-	10.7	-	nC
Gate to Source Gate Charge	Q _{gs}		-	1.72	-	
Gate to Drain "Miller" Charge	Q _{gd}		-	5.9	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

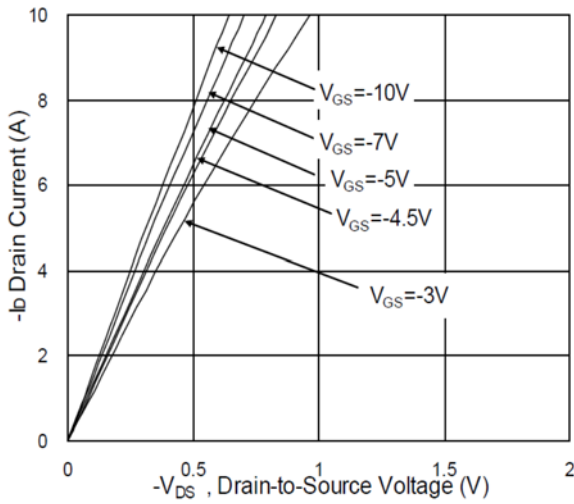
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-1A	-	-	-1.2	V
Continuous Source Current	I _S	V _G =V _D =0V, Force Current	-	-	-14	A

Notes:

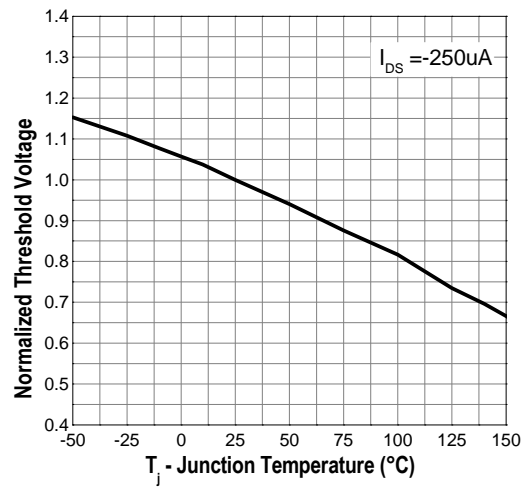
- Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
- R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{θJC} is guaranteed by design while R_{θJA} is determined by the user's board design. R_{θJA} shown below for single device operation on FR-4 in still air.

Typical Operating Characteristics

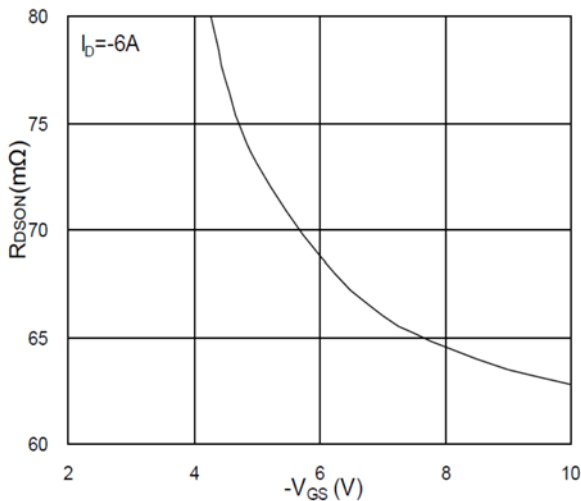
Output Characteristics



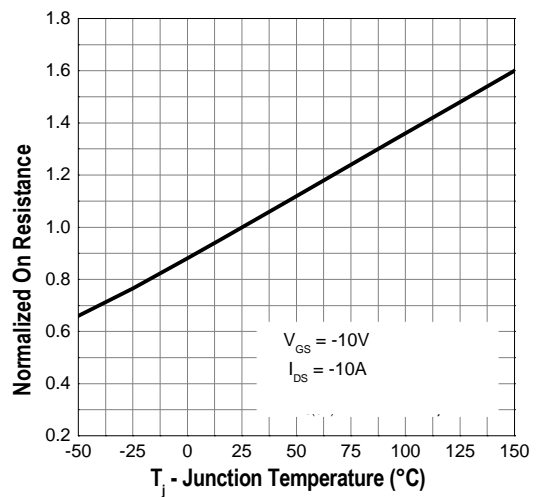
Gate Threshold Voltage



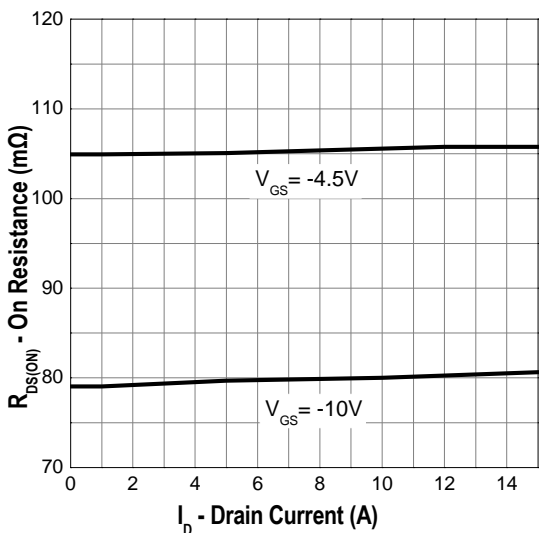
Gate-Source On Resistance



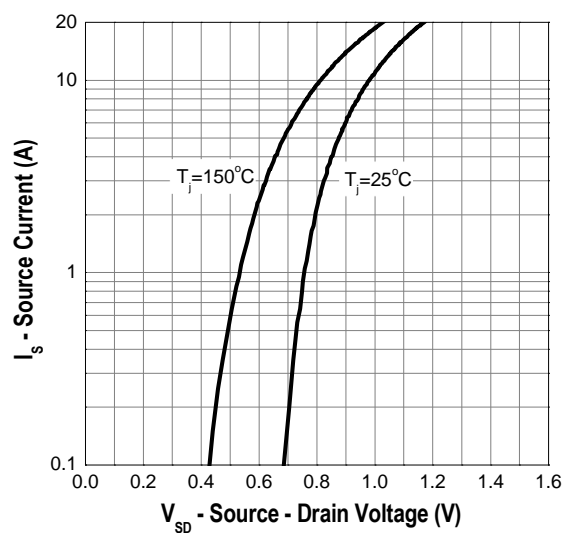
Drain-Source On Resistance



Drain-Source On Resistance

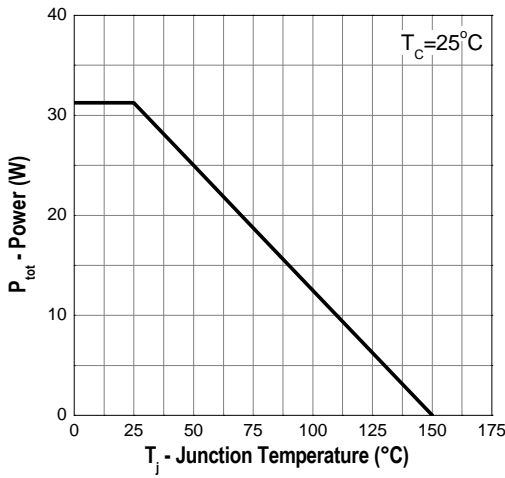


Source-Drain Diode Forward

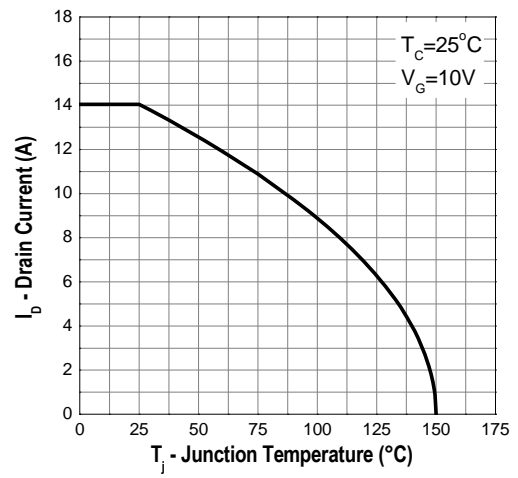


Typical Operating Characteristics (Cont.)

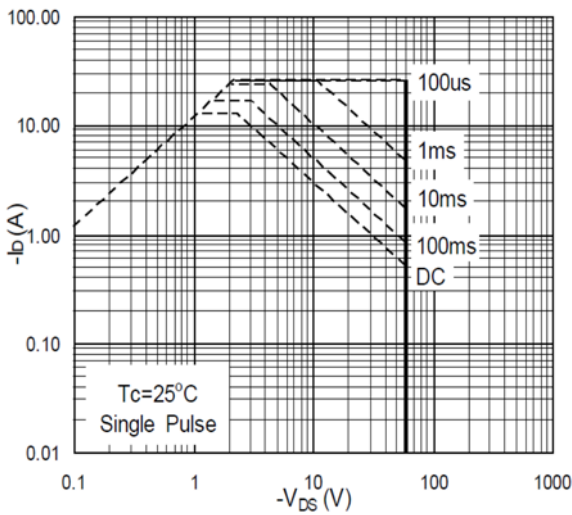
Power Dissipation



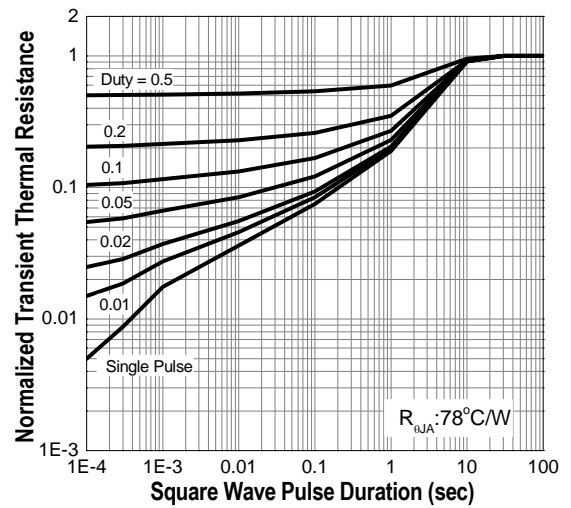
Drain Current



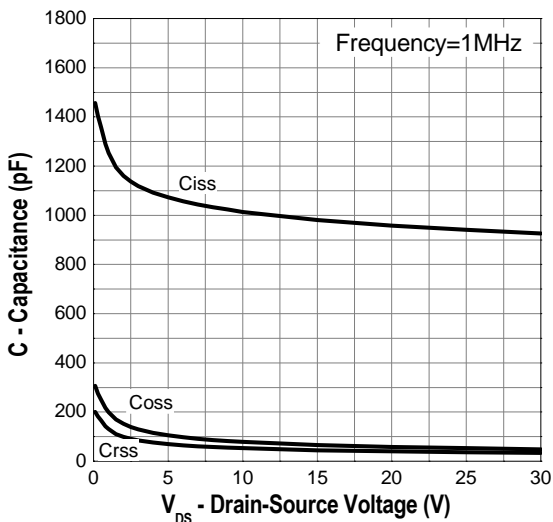
Safe Operation Area



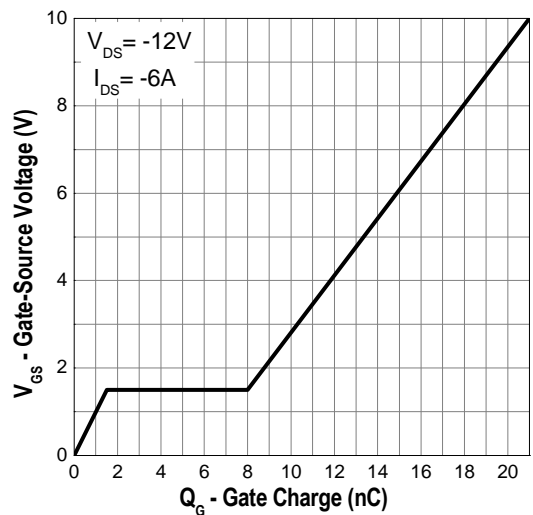
Thermal Transient Impedance



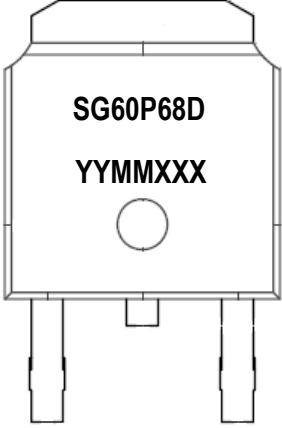
Capacitance



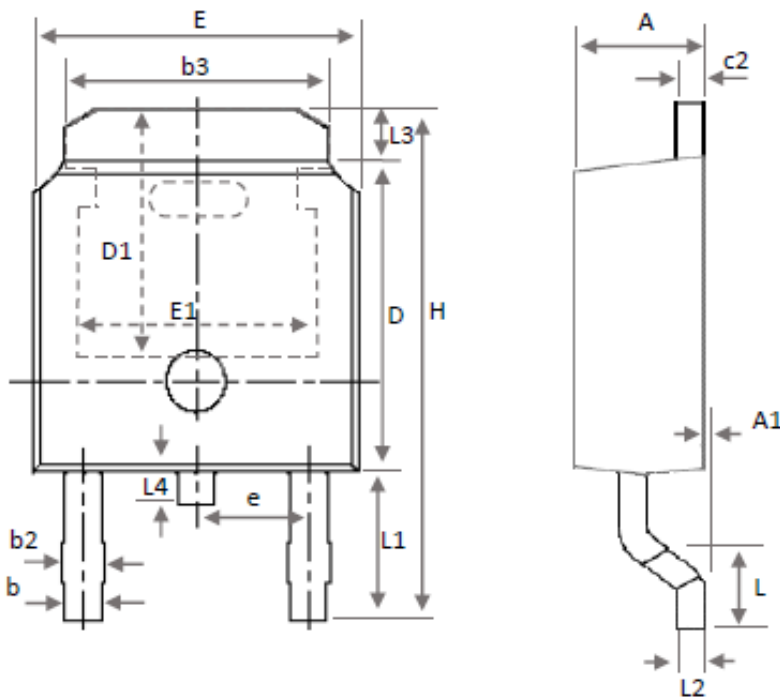
Gate Charge



Marking Information

TO-252 (D)	Marking Rule
<p data-bbox="135 362 304 398">Laser Marking</p>  <p data-bbox="405 880 507 911">Diagram</p>	<p data-bbox="805 362 1070 398"><u>Line 1</u> : Device Name</p> <p data-bbox="805 409 943 445">SG60P68D</p> <p data-bbox="805 495 1038 530"><u>Line 2</u> : Date Code</p> <p data-bbox="805 542 943 577">YYMMXXX</p> <p data-bbox="805 624 1002 660">YY : Year Code</p> <p data-bbox="805 672 1027 707">MM : Month Code</p> <p data-bbox="805 719 1062 754">XXX : Serial Number</p>

Package of Dimension



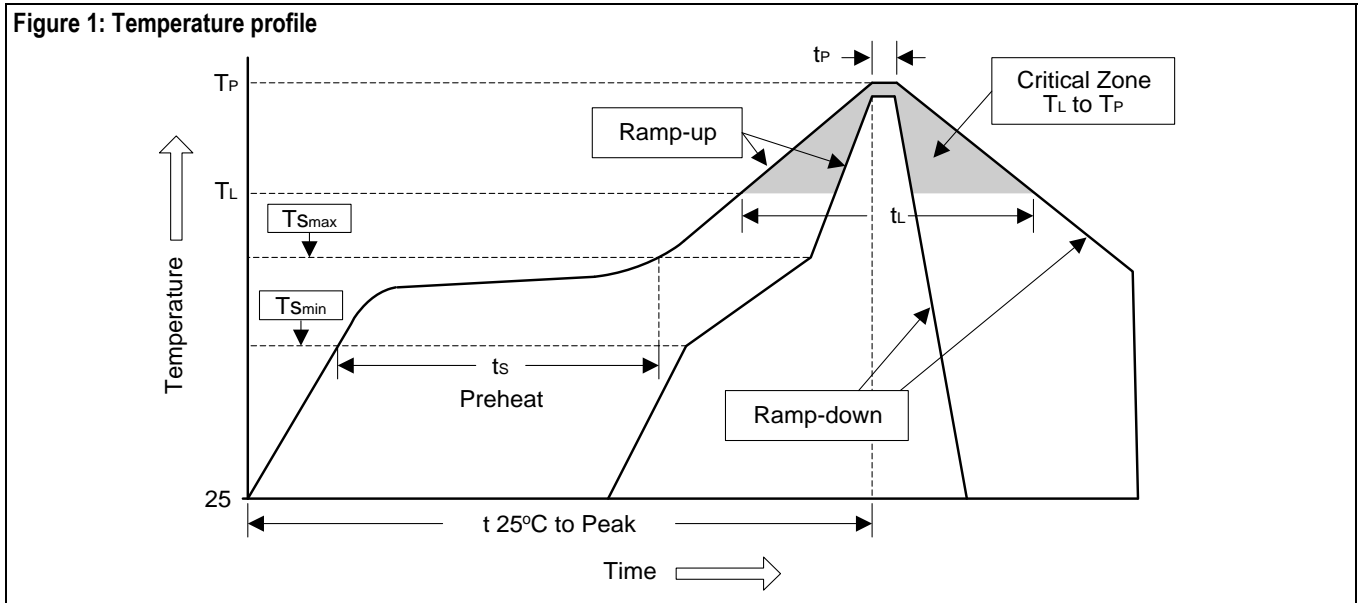
Symbol	Min	Nor	Max
E	6.35	6.54	6.731
L	1.40	1.59	1.78
L1	2.743 Ref.		
L2	0.508 BSC		
L3	0.89	1.08	1.27
L4	0.60	0.81	1.01
D	5.97	6.10	6.223
H	9.40	9.91	10.41
b	0.64	0.77	0.89
b2	0.76	0.95	1.14
b3	4.95	5.21	5.46
e	2.286 BSC		
A	2.18	2.29	2.39
A1	0.00	0.07	0.13
c2	0.46	0.68	0.89
D1	5.21	-	-
E1	4.32	-	-

1. All dimension are in millimeters.
2. Dimension does not include burrs and mold flash/protrusions.

Soldering Methods for Silicongear's Products

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices

Figure 1: Temperature profile



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (T_{Smin})	100°C	150°C
- Temperature Max (T_{Smax})	150°C	200°C
- Time (min to max) (t_s)	60 to 120 sec	60 to 180 sec
T_{Smax} to T_L		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (T_L)	183°C	217°C
- Time (t_L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T_P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (t_P)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

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