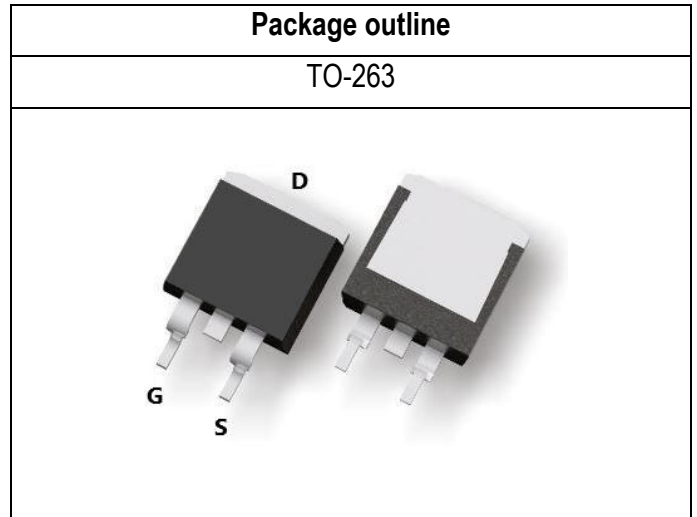


Key parameter	N _{channel}	Unit
V _{(BR)DSS} min.	80	V
R _{DS(ON)} max. V _{GS} =10V	9.0	mΩ
V _{GS(TH)} Typ.	3.0	V
I _D	97.3	A
C _{iss} Typ.	3942	pF
Q _g 10V Typ.	77.8	nC
E _{AS}	56.7	mJ



Description

The SG80N07HG uses double-gate structure of MOSFET to provide excellent electrical parameter. There is high speed switching capacity, low R_{DS(ON)} resistance, low gate charge and stable characteristics for these devices. Moreover, it is a helpful choose for raise efficiency or reduce consumption in circuit. These features combine to be an advantage design for use in wide variety of application including converter and inverter design.

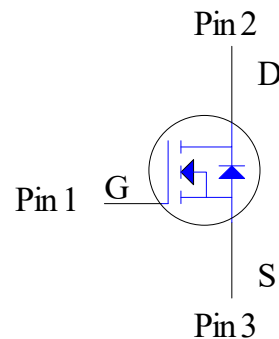
Features

- ◇ Fast switch capacity
- ◇ Low R_{DS(ON)} resistance
- ◇ Low input capacitance
- ◇ Low Switching Loss
- ◇ Ruggedness commutation capability
- ◇ Pb-free lead plating; RoHS compliant

Potential application

- AC-DC adaptor
- DC-DC converter
- Load Switch
- Electric tool application
- Motor/Fan driving application
- Synchronous Rectifier for Power Delivery

Symbol and Pin assignment



Order Information

Item	Description
1. Order Code	SG80N07HG
2. Part Number	SG80N07HG
3. Package Type	TO-263
4. Package Code	G
5. Packing Type	Tape & Reel
6. Quantity in Pack	800
7. RoHS Status	Halogen-Free

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1. Absolute Maximum Ratings ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	80	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current-Continuous ^{Note 1}	$T_C=25^\circ\text{C}$	I_D	97.3	A
	$T_C=100^\circ\text{C}$		61.5	A
Drain Current-Continuous ^{Note 2}	$T_A=25^\circ\text{C}$	I_D	12.7	A
	$T_A=70^\circ\text{C}$		10.1	A
Drain Current-Pulsed ^{Note 3}	$T_A=25^\circ\text{C}$	I_{DM}	150	A
Avalanche Current		I_{AR}	33.7	A
Single Pulse Avalanche Energy ^{Note 4}		E_{AS}	56.7	mJ
Maximum Power Dissipation	$T_C=25^\circ\text{C}$	P_D	181.1	W
	$T_C=100^\circ\text{C}$		72.4	W
	$T_A=25^\circ\text{C}$		3.0	W
	$T_A=70^\circ\text{C}$		1.9	W
	Derate Factor Above $T_C=25^\circ\text{C}$		1.4	W/ $^\circ\text{C}$
Max. Operating Junction Temperature		T_J	150	$^\circ\text{C}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

2. Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Thermal resistance, Junction-Case	$R_{\theta JC-N}$	Please refer to Note 5	-	-	0.69	$^\circ\text{C}/\text{W}$
Thermal resistance, Junction-Ambient	$R_{\theta JA-N}$	Please refer to Note 5	-	-	40.43	$^\circ\text{C}/\text{W}$

Notes:

- Limited by silicon chip capability and $R_{\theta JC-N}$ junction-to-case thermal resistance.
- The maximum current rating is limited by package and $R_{\theta JA-P}$ junction-to-ambient thermal resistance.
- Must be ensure junction temperature does not exceed 150-degree C. (Pulse Width $\leq 380\mu\text{s}$, Duty $\leq 2\%$)
- Limited by T_{Jmax} , starting $T_J=25^\circ\text{C}$, $L=0.1\text{mH}$, $R_g=25\Omega$, $I_D=33.7\text{A}$, $V_{GS}=10\text{V}$.
- The value of thermal resistance is measured with the single device put on cooling plate under a still air environment temperature is 25 degree C based on JEDEC standard JESD51-14 and JESD51-2a. Thermal resistance obtained depends on the user's specific board design and given application.

3. Electrical Characteristics (T_J=25°C unless otherwise noted)

STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _{DS} =250μA	80	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
		V _{DS} =80V, V _{GS} =0V, T _J =125°C	-	-	100	μA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

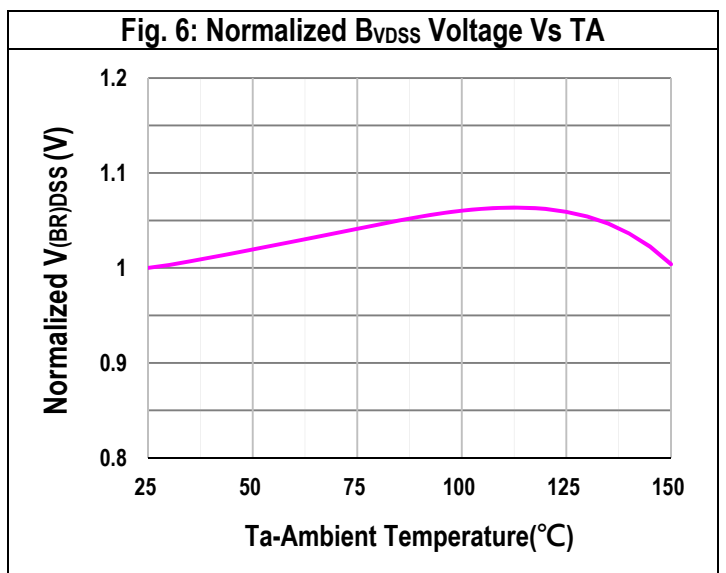
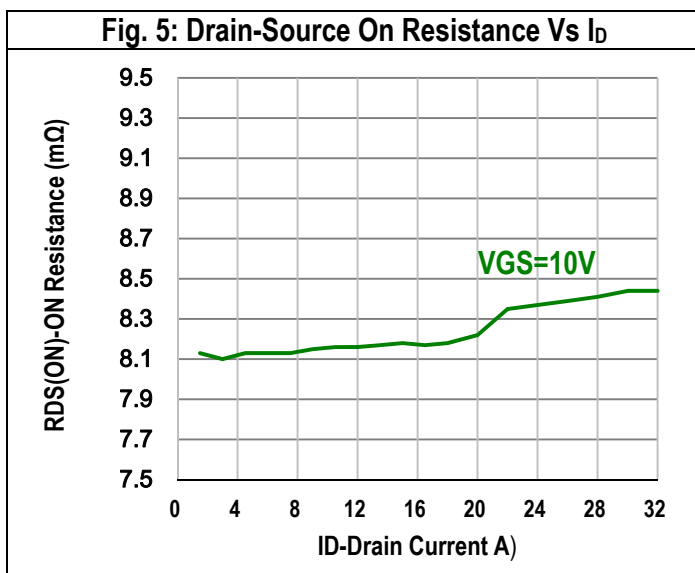
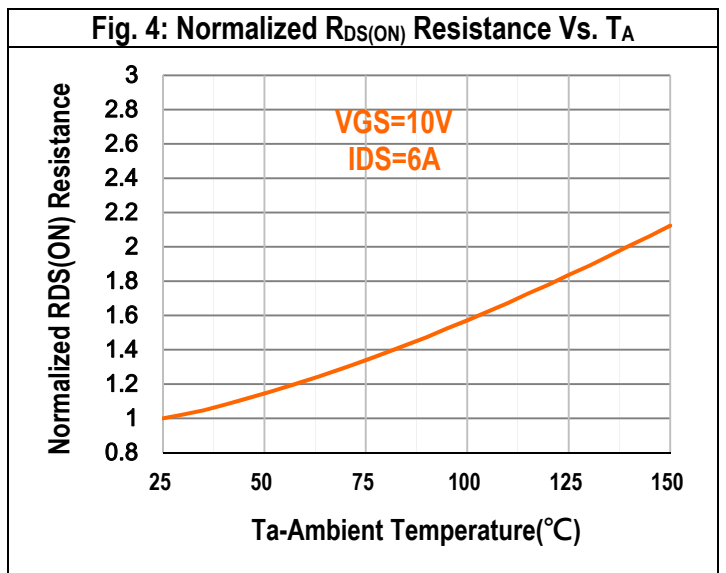
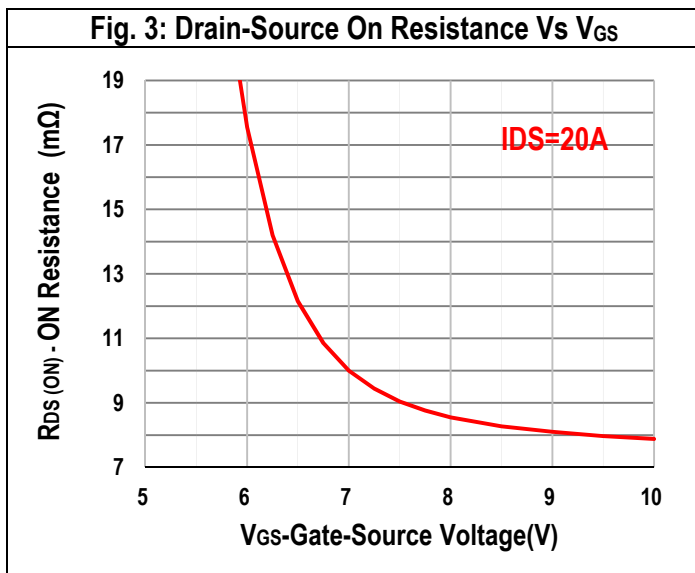
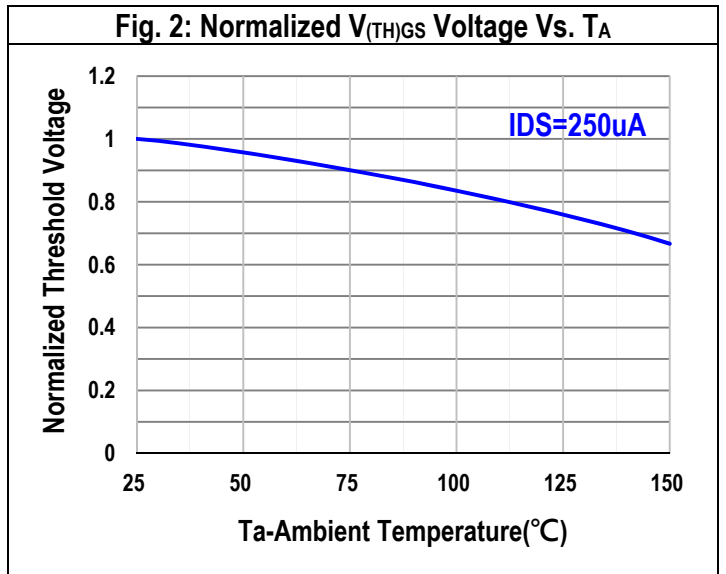
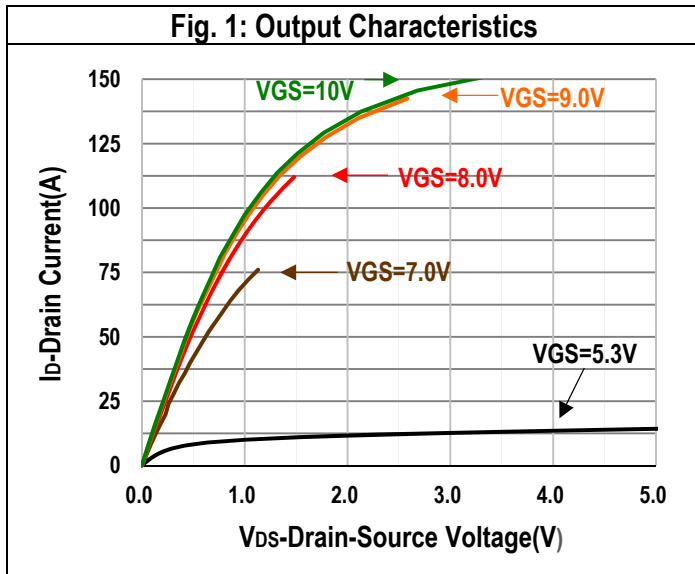
STATIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250μA	2.6	3.0	3.4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _{DS} =20A	-	7.8	9.0	mΩ
Gate Resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	0.8	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =5V, I _{DS} =20A	-	20	-	S

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iss}	V _{DD} =80V, V _{DS} =40V, V _{GS} =0V, f=1MHz	-	3942	-	pF
Output Capacitance	C _{oss}	V _{DD} =80V, V _{DS} =40V, V _{GS} =0V, f=1MHz	-	208	-	pF
Reverse Transfer Capacitance	C _{rss}	V _{DD} =80V, V _{DS} =40V, V _{GS} =0V, f=1MHz	-	117	-	pF
Turn-On Delay Time	T _{d(on)}	V _{DS} =40V, V _{GS} =10V, I _{DS} =20A, R _{GEN} =3.0Ω	-	18.5	-	nS
Rise Time	t _r	V _{DS} =40V, V _{GS} =10V, I _{DS} =20A, R _{GEN} =3.0Ω	-	35.6	-	nS
Turn-Off Delay Time	T _{d(off)}	V _{DS} =40V, V _{GS} =10V, I _{DS} =20A, R _{GEN} =3.0Ω	-	40.4	-	nS
Fall Time	t _f	V _{DS} =40V, V _{GS} =10V, I _{DS} =20A, R _{GEN} =3.0Ω	-	25.1	-	nS

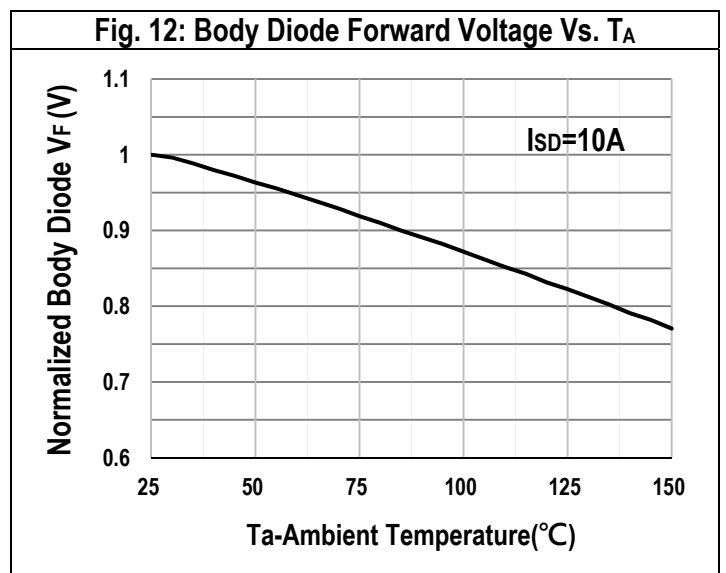
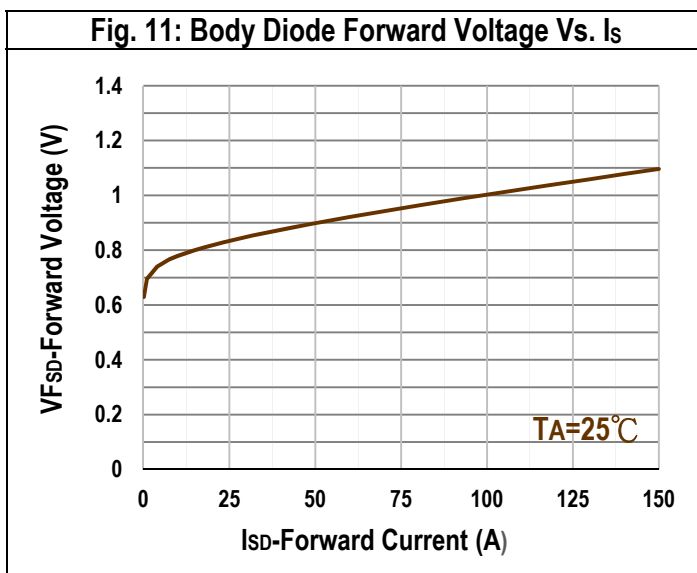
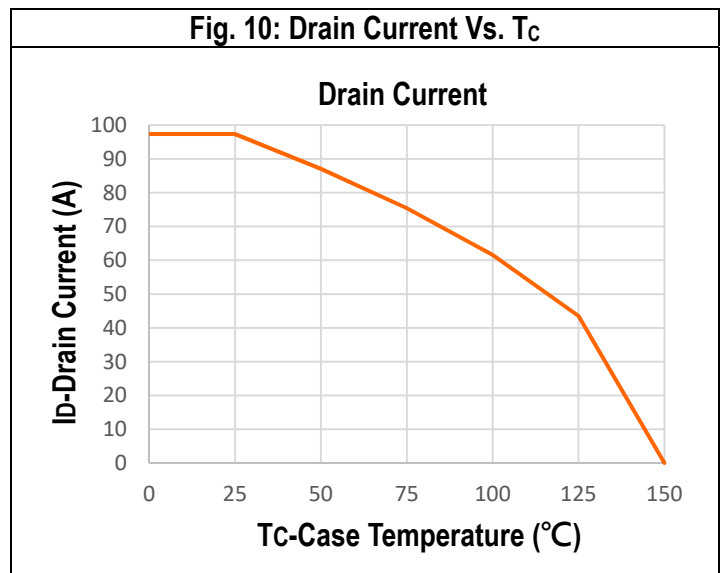
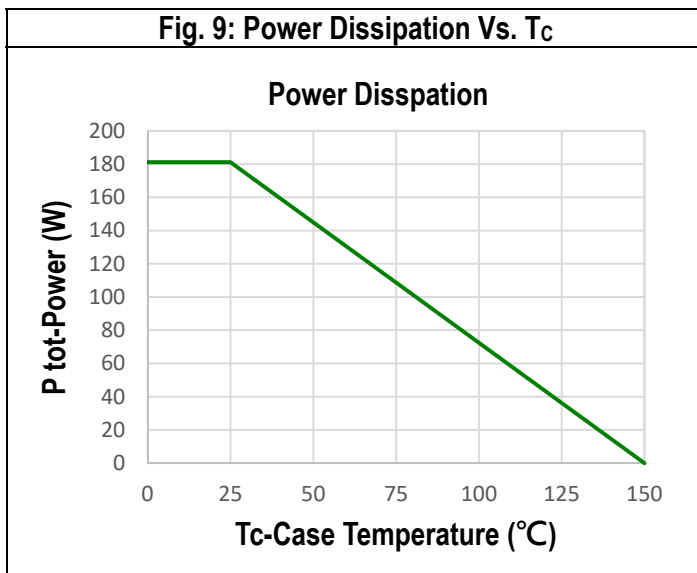
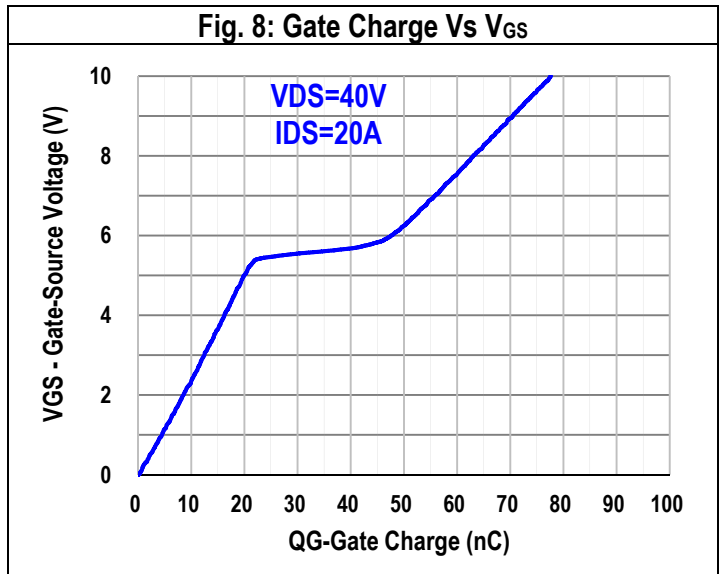
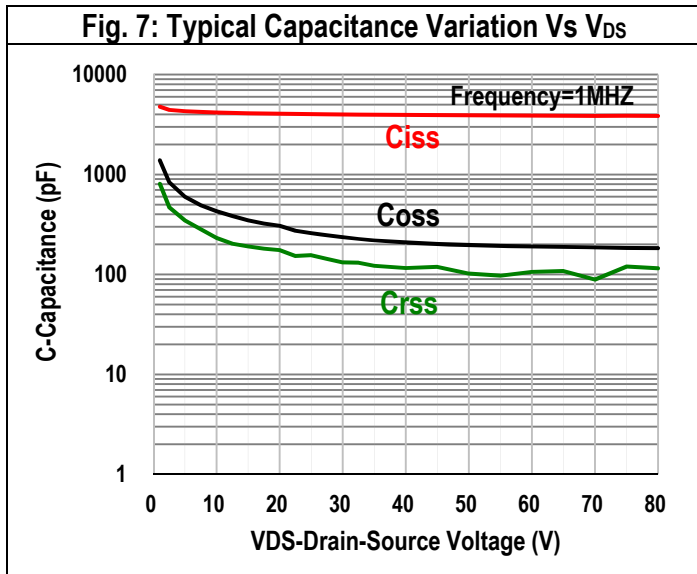
GATE CHARGE CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate to Source Gate Charge	Q _{gs}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	23.7	-	nC
Gate charge at threshold	Q _{g(th)}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	12.4	-	nC
Gate to Drain Charge	Q _{gd}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	23.7	-	nC
Switching charge	Q _{sw}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	35	-	nC
Gate charge total	Q _{g 10V}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	77.8	-	nC
Gate plateau voltage	V _{plateau}	V _{DD} =40V, I _D =20A, V _{GS} =0 to 10V	-	5.6	-	V
Gate charge total, sync. FET (Q _g - Q _{gd})	Q _{g(sync)}	V _{DS} =0.1V, V _{GS} =0 to 10V	-	54.1	-	nC

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Body diode continuous forward current	I _S	T _C =25°C	-	-	97.3	A
Body diode pulse current	I _{SM}	T _C =25°C	-	-	150	A
Body diode forward voltage	V _{SD}	V _{GS} =0V, I _S =20A	-	0.81	1.0	V
Body diode reverse recovery time	t _{rr}	V _{DD} =40V, I _F =20A, di/dt=100A/μs	-	26.9	-	nS
Body diode reverse recovery charge	Q _{rr}	V _{DD} =40V, I _F =20A, di/dt=100A/μs	-	29.6	-	nC
Body diode peak reverse recovery charge	I _{rm}	V _{DD} =40V, I _F =20A, di/dt=100A/μs	-	2.2	-	A

4. Typical Operating Characteristics



4. Typical Operating Characteristics



4. Typical Operating Characteristics

Fig. 13: Safe Operation Area

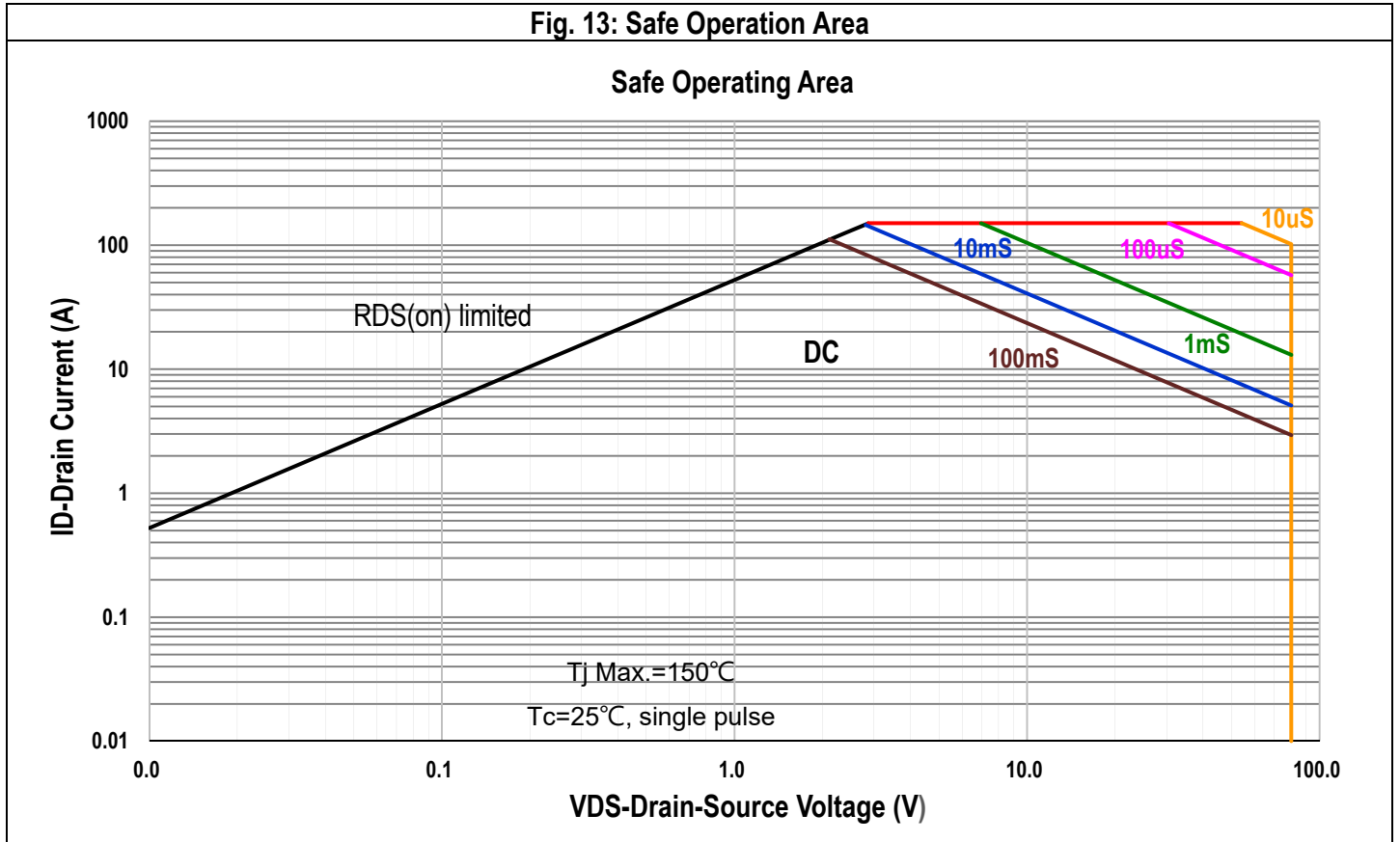
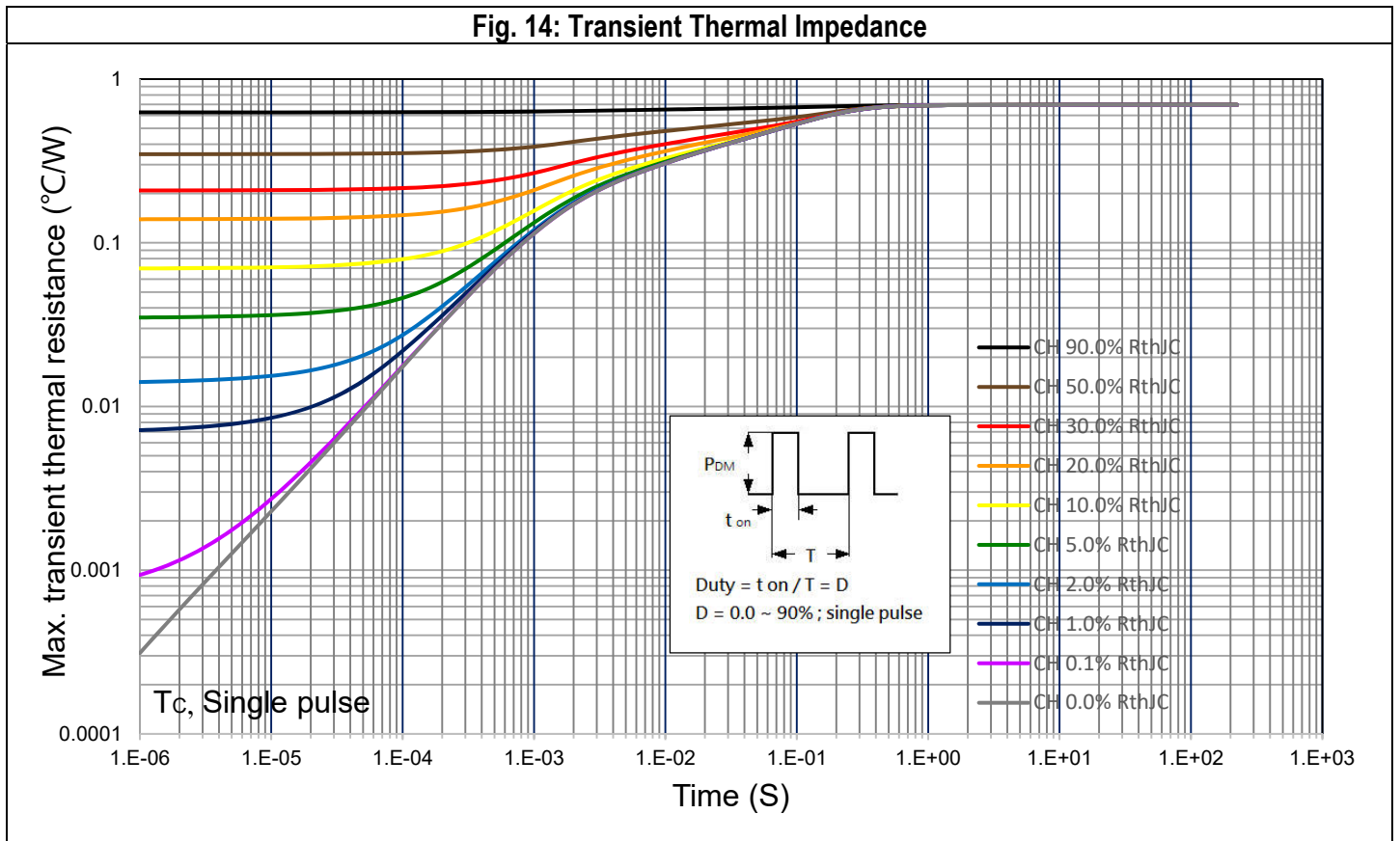
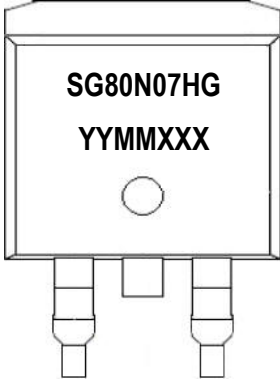


Fig. 14: Transient Thermal Impedance

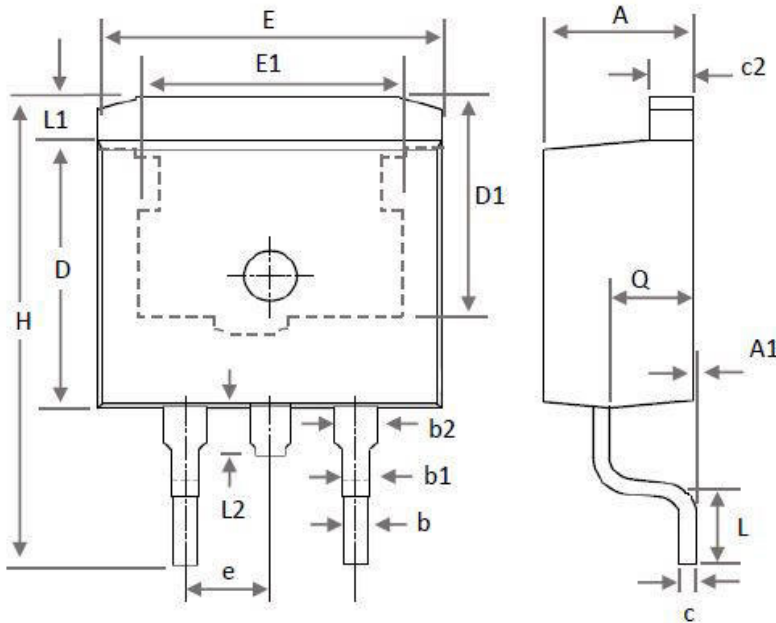


5. Marking Information

TO-263 (G)	Marking Rule
<p>Laser Marking</p> 	<p><u>Line 1</u> : Device SG80N07HG</p> <p><u>Line 2</u> : Date Code YYMMXXX</p> <p>YY : Year Code MM : Month Code XXX : Serial Number</p>

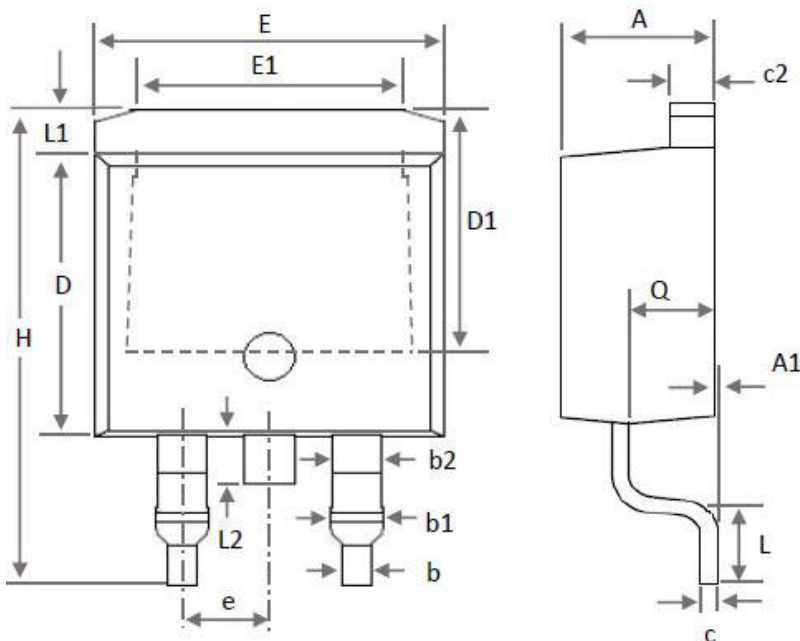
6. Package of Dimension

Package type: TO-263S



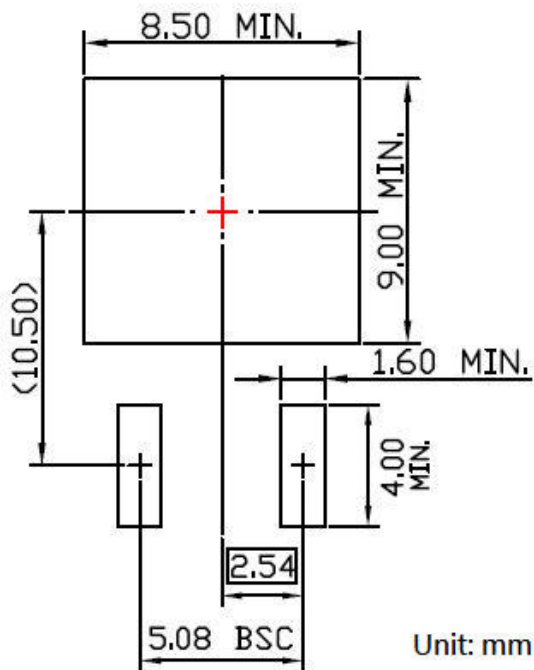
Symbol	Min	Nor	Max
A	4.24	4.44	4.64
A1	0.00	0.10	0.25
b	0.66	0.76	0.96
b1	0.76	0.86	1.06
b2	1.14	1.27	1.47
c	0.40	0.50	0.60
c2	1.15	1.30	1.45
D	8.38	8.60	8.90
D1	6.86	7.16	-
E	9.90	10.20	10.50
E1	7.80 Ref.		
e	2.54 BSC		
H	14.61	15.00	15.88
L	1.78	2.20	2.79
L1	1.40 REF.		
L2	1.50 REF.		
Q	-	2.49	2.70

Package type: TO-263



Symbol	Min	Nor	Max
A	4.24	4.51	4.77
A1	0.00	0.13	0.25
b	0.70	0.83	0.96
b1	1.17	1.46	1.75
b2	1.20	1.45	1.70
c	0.30	0.45	0.60
c2	1.15	1.29	1.42
D	8.50	8.76	9.02
D1	6.60	7.13	7.65
E	9.86	10.11	10.36
E1	6.89	7.39	7.89
e	2.54 BSC		
H	14.61	15.25	15.88
L	1.78	2.29	2.79
L1	1.07	1.27	1.47
L2	1.40	1.55	1.70
Q	2.30	2.60	2.89

7. Land pattern (Footprint)



Note 1: Land pattern (Footprint) design is for reference only.

Note 2: Package body sizes exclude mold flash and burrs.

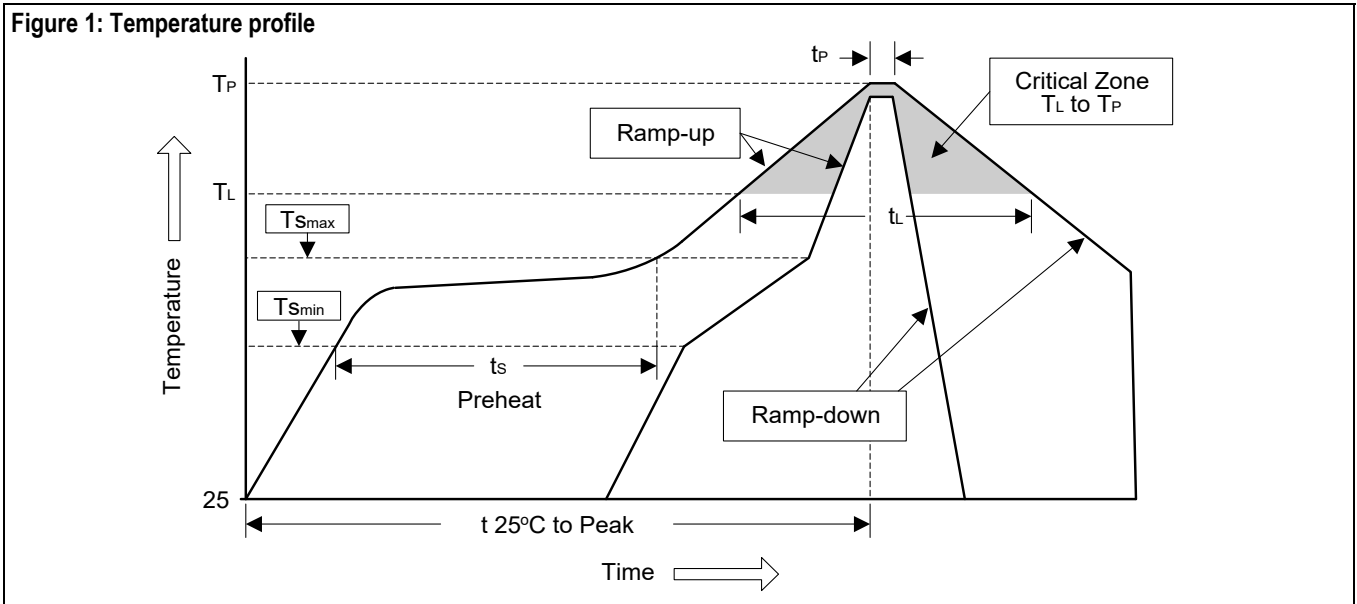
Note 3: Dimension is measured in gauge plane.

Note 4: Tolerance 0.1mm unless otherwise specified.

8. Appendix-A

Soldering Methods for Silicongear's Products (Just for SMD type of device)

1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (TL to TP)	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (Tsmmin)	100°C	150°C
- Temperature Max (Tsmmax)	150°C	200°C
- Time (min to max) (ts)	60 to 120 sec	60 to 180 sec
Tsmmax to TL		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (TL)	183°C	217°C
- Time (tL)	60 to 150 sec	60 to 150 sec
Peak Temperature (TP)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (tP)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec

8. Appendix-B**Important Notice****© Silicongear Corporation**

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