

-30V P-CHANNEL Power MOSFET

	TO-252	
$ V_{\text{DSS}} \ , \ -30V \\ R_{\text{DS(ON)}} \ , \ 14m\Omega \ (\text{max.}) \ @ \ V_{\text{GS}} \ = \ -10V \\ R_{\text{DS(ON)}} \ , \ 24m\Omega \ (\text{max.}) \ @ \ V_{\text{GS}} \ = \ -4.5V \\ I_D \ , \ -44A $		G

Description	Features
The SGP3011D uses advanced trench technology MOSFETs to provide excellent R _{DS(ON)} and low gate charge. The complementary Power MOSFETs may be used in H-bridge, Inverters and other applications.	 Low On-Resistance Low Input Capacitance Low Miller Charge Low Input/Output Leakage Pb-free lead plating; RoHS compliant
	Applications
	Motor / Body Load Control Automotive Systems Load Switch

Ordering Information

Ordering Code	RoHS Status	Package	Package Code	Packing	Quantity
SGP3011D	Halogen-Free	TO-252	D	Tape & Reel	2,500

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Para	meter	Symbol	Value	Unit
Drain-Source Voltage		VDS	-30	V
Gate-Source Voltage		Vgs	±20	V
Tc=25°C		1-	-44	Α
Drain Current-Continuous	Tc=100°C	ID ID	-28	Α
Drain Current-Pulsed Note 1		Ідм	-150	Α
Avalanche Current		I _{AS}	28	Α
Avalanche Energy, L=0.1mH		Eas	39	mJ
	Tc=25°C		44	W
Maximum Power Dissipation	Tc=100°C	PD PD	18	W
Storage Temperature Range		T _{STG}	-55 to +175	°C
Operating Junction Temperature Rang	е	TJ	-55 to +175	°C

Thermal Resistance Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Maximum Junction-to-Ambient Note 2	Reja	Steady State	-	-	62	°C/W
Maximum Junction-to-Case	Rejc	Steady State	-	-	2.8	°C/W



Electrical Characteristics (TJ=25°C unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =-250µA	-30	-	-	V
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage	lgss	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =-250µA	-1	-1.5	-2.5	V
Drain-Source On-State Resistance	Р	V _{GS} =-10V, I _{DS} =-20A	-	-	14	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-4.5V, I _{DS} =-10A	-		24	mΩ
		·				

DYNAMIC CHARACTERISTICS					
Symbol	Conditions	Min.	Тур.	Max.	Unit
Ciss		-	2170		
Coss	V _{DS} =-15V, V _{GS} =0V, f=1MHz	-	303	-	pF
Crss		-	232	-	1
gfs	V _D =-5V, I _D =-30A	-	29	-	S
-	Ciss Coss Crss	Ciss VDS=-15V, VGS=0V, f=1MHz Crss Crss	Ciss - Coss VDS=-15V, VGS=0V, f=1MHz - Crss - -	Ciss - 2170 Coss VDS=-15V, VGS=0V, f=1MHz - 303 Crss - 232	Ciss - 2170 - Coss VDS=-15V, VGS=0V, f=1MHz - 303 - Crss - 232 -

SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Turn-On Delay Time	T _{d(on)}		-	8	-	
Rise Time	tr	V_{DD} =-15V, V_{GS} =-10V, R_{G} =3.3 Ω , I_{D} =-	-	73.7	-	
Turn-Off Delay Time	T _{d(off)}	15A	-	61.8	-	ns
Fall Time	tr		-	24.4	-	
Total Gate Charge at -4.5V	Qg		-	21	-	
Gate to Source Gate Charge	Qgs	V _{DS} =-15V, V _{GS} =-4.5V, I _D =-15A	-	8.5	-	nC
Gate to Drain "Miller" Charge	Qgd		-	7.1	-	

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Drain-Source Diode Forward Voltage	Vsd	V _{GS} =0V, I _S =-1A	-	-	-1.2	V
Continuous Source Current	ls		-	-	-49	Α
Pulsed Source Current	I _{SM}	V _G =V _D =0V, Force Current	-	-	-150	Α
Body Diode Reverse Recovery Time	trr	V _{DD} =-15V, I _F =-15A, di/dt=100A/µs	-	18	-	ns
Body Diode Reverse Recovery Charge	Qrr	V _{DD} =-15V, I _F =-15A, di/dt=100A/µs	-	8	-	nC

Notes:

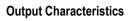
1. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

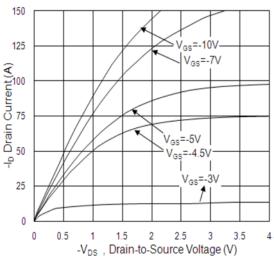
 R_{BJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{BJA} is guaranteed by design while R_{BCA} is determined by the user's board design. R_{BJA} shown below for single device operation on FR-4 in still air.



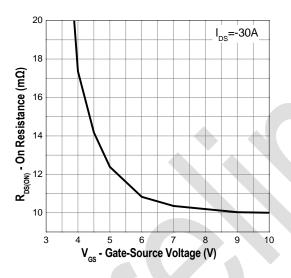
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Typical Operating Characteristics

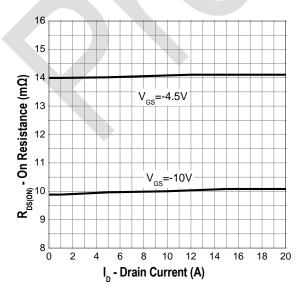




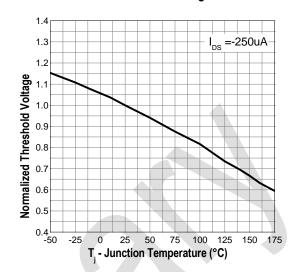
Gate-Source On Resistance



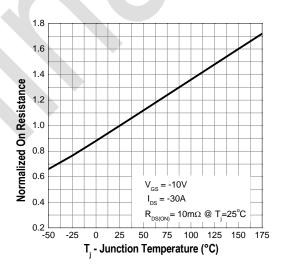
Drain-Source On Resistance



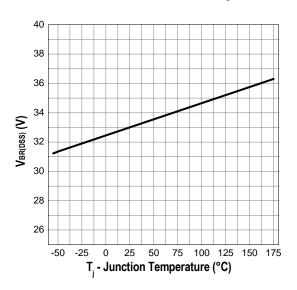
Gate Threshold Voltage



Drain-Source On Resistance



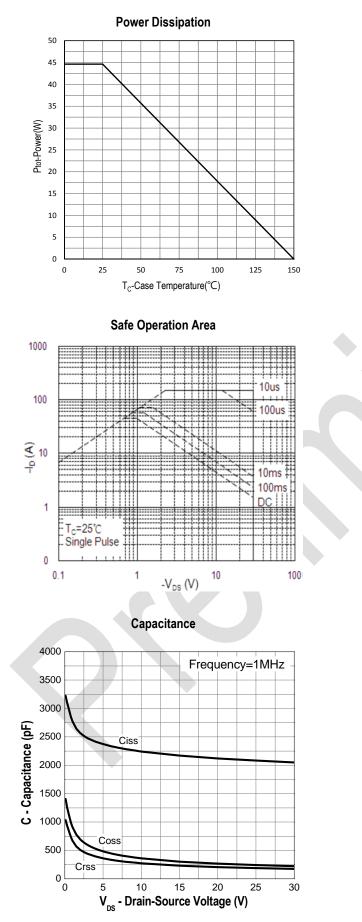
Drain-source Breakdown Voltage

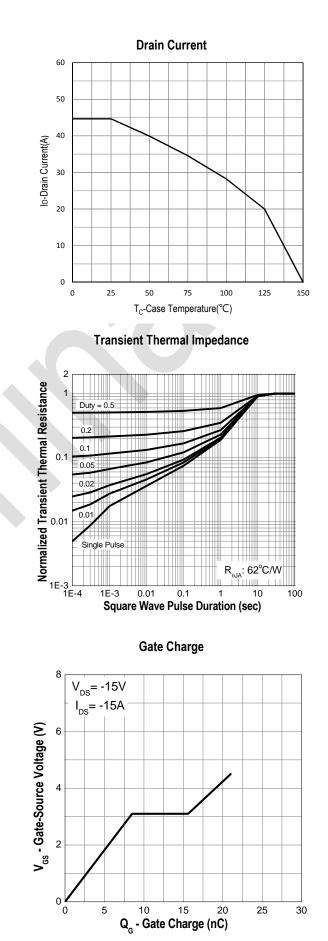




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Typical Operating Characteristics (Cont.)



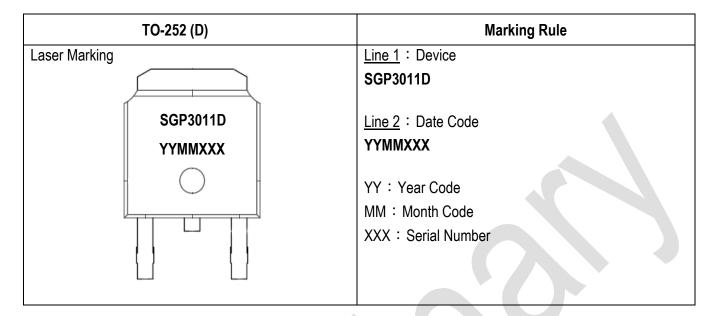


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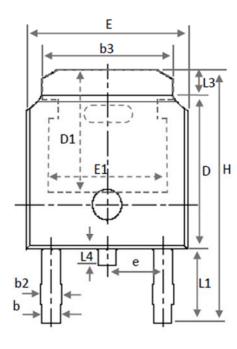


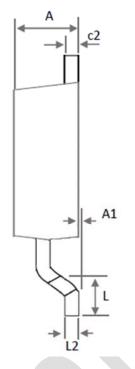
Marking Information





Package of Dimension



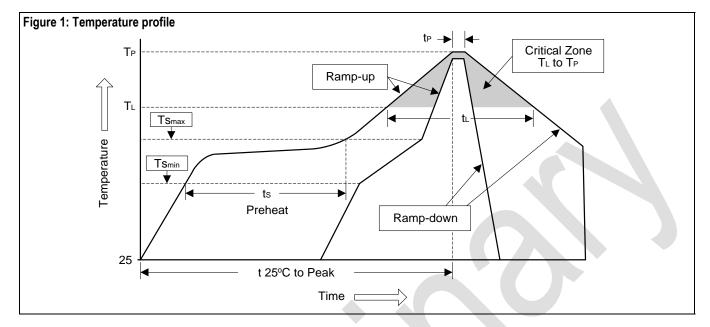


Symbol	Min	Nor	Max
E	6.35	6.54	6.731
L	1.40	1.59	1.78
L1		2.743 Ref	
L2	(0.508 BS(0
L3	0.89	1.08	1.27
L4	0.60	0.81	1.01
D	5.97	6.10	6.223
Η	9.40	9.91	10.41
b	0.64	0.77	0.89
b2	0.76	0.95	1.14
b3	4.95	5.21	5.46
e		2.286 BS(C
Α	2.18	2.29	2.39
A1	0.00	0.07	0.13
c2	0.46	0.68	0.89
D1	5.21	-	-
E1	4.32	-	-



Soldering Methods for Silicongear's Products

- 1. Storage environment: Temperature=10°C to 35°C Humidity=65%±15%
- 2. Reflow soldering of surface-mount devices



Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate $(T_L \text{ to } T_P)$	<3°C/sec	<3°C/sec
Preheat		
- Temperature Min (Ts _{min})	100°C	150°C
- Temperature Max (Ts _{max})	150°C	200°C
- Time (min to max) (ts)	60 to 120 sec	60 to 180 sec
Tsmax to T∟		
- Ramp-up Rate	<3°C/sec	<3°C/sec
Time maintained above:		
- Temperature (TL)	183°C	217°C
- Time (t _L)	60 to 150 sec	60 to 150 sec
Peak Temperature (T _P)	240°C +0/-5°C	260°C +0/-5°C
Time within 5°C of actual Peak Temperature (tթ)	10 to 30 sec	20 to 40 sec
Ramp-down Rate	<6°C/sec	<6°C/sec
Time 25°C to Peak Temperature	<6 minutes	<8 minutes

3. Flow (wave) soldering (solder dipping)

Products	Peak Temperature	Dipping Time
Pb devices.	245°C ±5°C	5sec ±1sec
Pb-Free devices.	260°C +0/-5°C	5sec ±1sec



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